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OPERATING MANUAL FOR THE RRL 8 CHANNEL DATA LOGGER

by

E. J. Paluch
J. D. Shelton
C. S. Gardner

RRL Publication No. 502

Technical Report
May 1979

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NATIONAL AERONAUTICS & SPACE ADMINISTRATION
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INTRODUCTION

This unit is a data collection device which takes measurements from external sensors at user specified time intervals. Three sensor ports are dedicated to temperature, air pressure, and dew point. Five general purpose sensor ports are also provided. Each sensor port supplies two sources of plus and minus 15 volts for sensor power. One of the power supplies is for sensors that stabilize quickly and the other power supply is for sensors that take longer than a few seconds to stabilize. (Sensor warm up time is under software control for maximum system flexibility.)

After connecting the desired sensors the user specifies when measurements are to be recorded. The user enters the desired starting and stopping dates and times for the data collection. The warm up time for the "slow" sensors and the frequency of data collection must also be specified. If more than the three dedicated sensors are connected the additional ports must also be entered. This completes the initialization and the unit may be left running completely powered or it may be switched to a low power consumption mode.

While the system is running the user can switch full power on and interrogate the unit to examine any of the measurements that have already been recorded. The user can also examine the

current readings on any of the sensors or the data that have been collected can be dumped to a peripheral device (a minicomputer, paper tape, etc.) if desired.

SECTION 1 OPERATION

1.1 FRONT PANEL DESCRIPTION

1. MAIN POWER SWITCH - This switch (1 in Figure 1) connects the power to the unit. The switch is a "lever locking switch" and must be pulled out before it can be operated. Since the data logger's memory is volatile, turning off the main power will destroy all stored information.

2. PROCESSOR POWER SWITCH - If the main power switch (1 on the front panel) is on, the processor power switch (2 on the Front Panel) will provide power to every part of the data logger except the RS-232 transmitting interface (I/O power). All sensors will be powered when this switch is on and the data logger will perform any user request if a terminal is connected and I/O power is on. (NOTE: If the data logger is supplying warm up power to the sensors and this switch is turned off the warm up power will also turn off and the next set of readings will be invalid. At any time, except when warm up power is on, this power may be switched off and the data logger will record all measurements correctly.)

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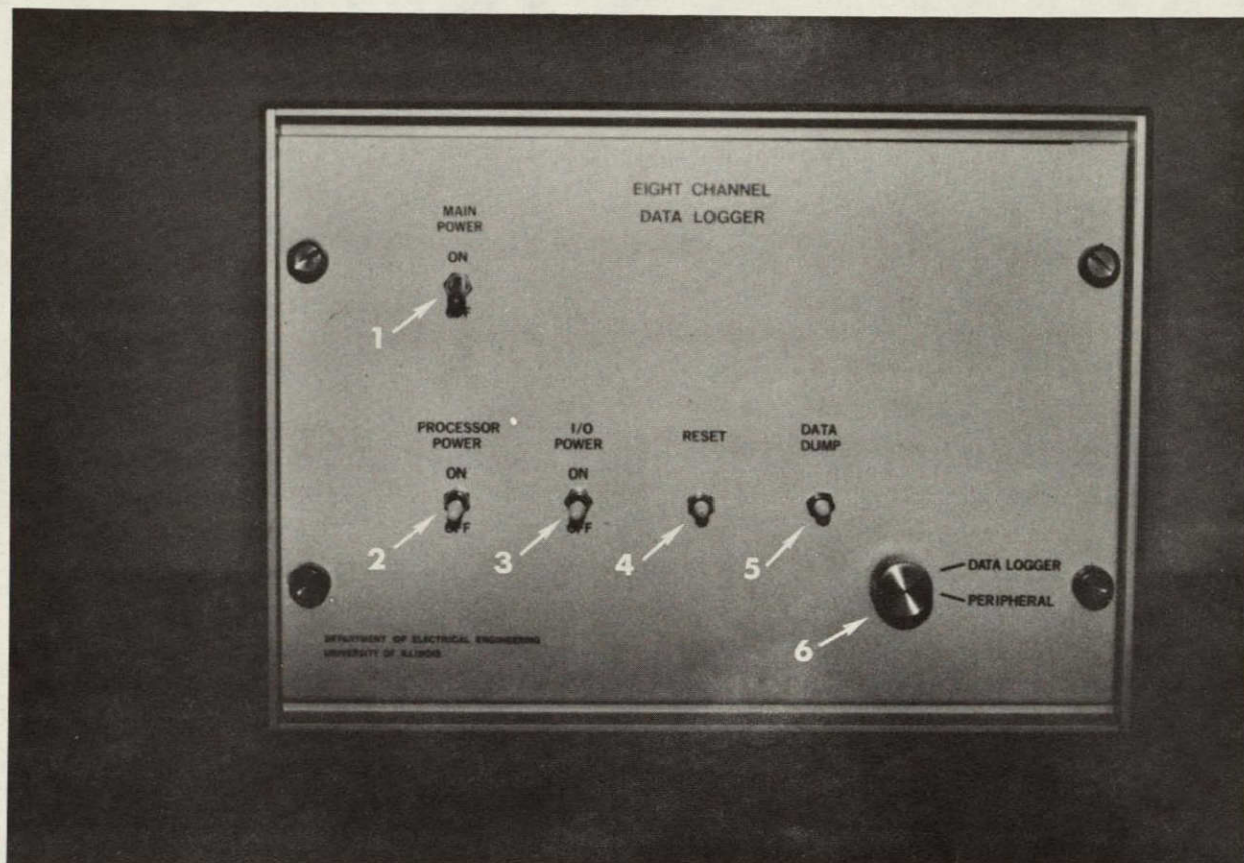


Figure 1

FRONT PANEL

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3. I/O POWER SWITCH - This switch controls the power to the RS-232 transmitter interface to the terminal connection (8 in Figure 2) and to the peripheral connection (7 on the Back Panel). If this switch is off the terminal and peripheral can transmit to the data logger, but the data logger will not transmit to them. (NOTE: It is advisable not to transmit either from a terminal or a peripheral device to the data logger if I/O power is off and the processor power is on.) In most cases the I/O power and the processor power will be both on or both off.

4. RESET - This switch resets the processor and should be pressed after the user turns the data logger on. If the data logger stops responding to user inputs the reset can be used to clear the transmission lockup.

5. DATA DUMP - This switch is only active if the user has selected the data dump option from the user options page (see section 1.3-6, DATA DUMP OPTION). If active, depressing the switch will transmit all of the collected data to the terminal and peripheral connections.

6. DEVICE SELECTION SWITCH - If terminal is selected, two way communications occur between the terminal (teletype or video terminal) and the data logger. Most of the time the data logger will be used in this manner.

If the peripheral is selected, the terminal transmits to the data logger and receives data through the peripheral connector. The data logger transmits to the peripheral connector. This feature can be used to transfer data from the data logger to another computer and is described in section 1.3-6, DATA DUMP OPTION.

1.2 BACK PANEL DESCRIPTION

7. PERIPHERAL CONNECTION - This connection allows the data logger to communicate with a peripheral device (i.e., computer, minicomputer, papertape puncher, etc.) through a terminal. The data logger can also dump readings to the peripheral device. (The connections are listed in the appendix.)

8. TERMINAL CONNECTION - This connection is for a teletype or video terminal that will be used to communicate with the data logger. The terminal will enter parameters that will initialize the data logger and can also request information from the data logger.

9. POWER CONNECTION - This connector is for +12 volt power for the data logger.

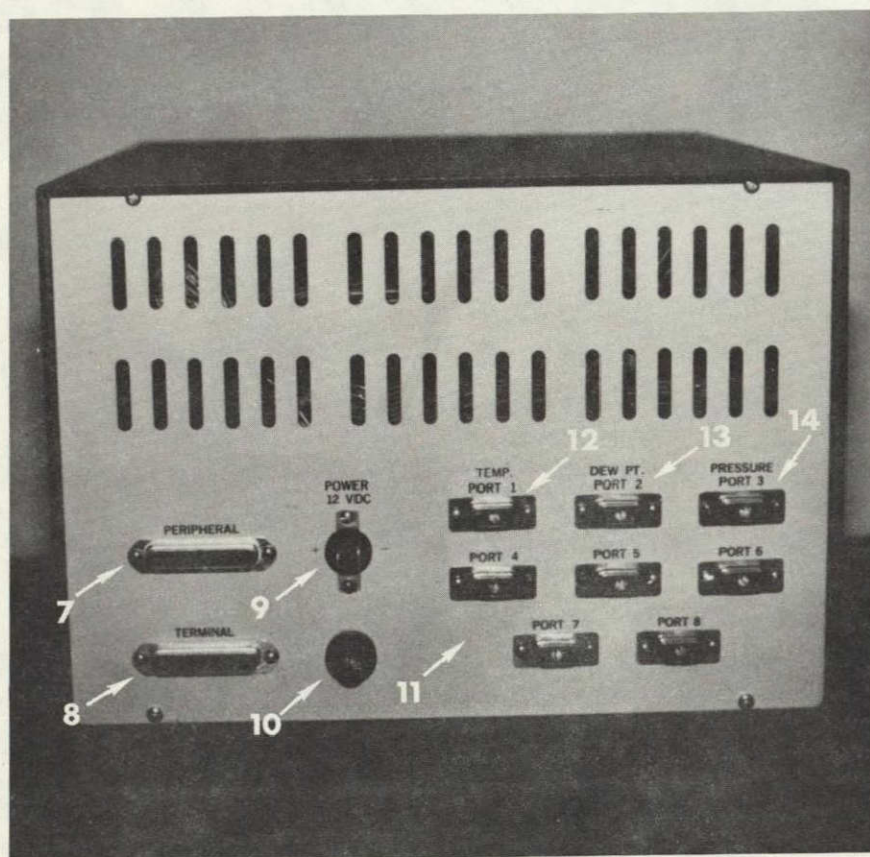


Figure 2
BACK PANEL

10. FUSE - A 1.5 Amp fuse should be used.

11. SENSOR PORTS - There are 8 sensor ports on the back panel. The ports are labeled 1-8 and the first three ports are dedicated. Each port connector has a coax signal connection and four power connections. (The connections for the ports are listed in the appendix.) The coax signal should be in the range of 0-5 Volts.

12. TEMPERATURE PORT - This port is dedicated to measuring temperature. 0-5 Volts corresponds to -30 - +70 degrees Centigrade.

13. DEW POINT PORT - This port is dedicated to measuring dew point. The 0-5 Volt range corresponds to -34 - +50 degrees Centigrade.

14. AIR PRESSURE PORT - This port is dedicated to measuring the air pressure. The 0-5 Volt range corresponds to 800 - 1100 millibars.

1.3 DESCRIPTION OF USER OPTION PAGE

The user has access to five basic routines that allow him to interact with the data logger. A list of these options is printed on the terminal whenever any key except CONTROL S, A, B, C, D or E is depressed and the system is not executing one of these routines. Samples of typical displays are included in each of the associated sections below. The underlined characters correspond to the user's responses.

1. LIST OF OPTIONS - The list of options is printed on the terminal whenever any key except CONTROL S, A, B, C, D or E is typed and the system is not executing one of these routines. It should be noted that the warm up power status is displayed prior to the option list. If warm up power is on, the PROCESSOR POWER switch should not be turned off immediately (this problem is discussed in section 1.1-2 of this manual). The following figure is an example of the user option page.

EXAMPLE: (user depresses the space bar and the system responds with:)

```
WARM-UP POWER IS ON
A) INITIALIZING PARAMETERS
B) CURRENT SENSOR READINGS
C) SEQUENTIAL READINGS
D) INITIALIZATION
E) DUMP
    (CONTROL 'S' STOPS PRINTOUT)
```

To invoke any option, the letter preceding it on the option page should be typed on the terminal. To exit any option prior to its end, CONTROL S may be typed.

2. OPTION A) INITIALIZING PARAMETERS - After the system has been initialized, the initializing parameters may be examined by typing A. As shown in the example contained in this section, the measurement starting date and time, ending date and time, warm up time in minutes and interval of measurements are displayed at the terminal.

EXAMPLE:

```
A
START      04/01      03:00
STOP       04/29      16:00
WARMUP TIME 02 MINS
FREQUENCY OF MEASUREMENTS      00:05
```

3. OPTION B) CURRENT SENSOR READINGS - Sensor outputs may be examined at any time with this routine. After typing B, a heading is printed which lists all ports to be interrogated. Temperature (port 1), dew point (port 2) and pressure (port 3) are dedicated ports and will always be printed. Any additional ports selected during system initialization will then be printed. To measure the sensor outputs, type R. Temperature and dew point readings have units of degrees centigrade while pressure readings have units of millibars. Any additional

sensors selected will yield readings with units of volts. As many sets of readings as desired may be accumulated. To exit this option, CONTROL S must be typed. This routine does not store these readings. Therefore, these readings cannot be retrieved at a later date by using the data dump option. If the data logger takes a reading during the execution of this option, the current line of sensor readings is stopped and the program waits for the entry of another R or CONTROL S. An example of this option is included below.

EXAMPLE:

```

B
TYPE 'R' FOR A READING, CONTROL 'S' WHEN DONE
  TEMP  DEW PT  PRESS  PORT1 PORT2 PORT3
R  6.72 - 3.81  983.78 1.853 1.812 3.062
R  6.72 - 3.81  983.78 1.848 1.812 3.062

```

4. OPTION C) SEQUENTIAL READINGS - This routine permits the user to examine data the data logger has accumulated. After typing C, the system requests a starting date and time followed by the number of readings that are desired for display. Dates are input in five-character fields by typing the number of the month in two digits, a slash (/) and the day of the month in two digits. The time is input in a five-character field by typing the two digit number of the hour, a colon (:) and the number of minutes in two digits. All two digit numbers must have unsurpressed leading zeros. The number of the hour is based on a 24 hour clock. A heading similar to the heading of option B

but including columns for date and time is printed. If any readings were taken on or after the specified date, they are printed as shown in the example in this section. When the requested number of readings has been printed or when the last reading taken has been displayed, the routine is exited. Similarly, if no readings were taken after the specified date and time, the processor exits the routine after printing the heading. As before, this routine may be terminated at any time by typing CONTROL S. If a reading (NMI) occurs during the data printout portion of this option, the current line of data is interrupted. The complete line is reprinted on the next line. NOTE: To avoid possible problems, care should be exercised to avoid entering data (dates, times and number of readings) when the data logger is scheduled to take a reading.

EXAMPLE:

C

```

START DATE?(MM/DD) 04/01      TIME?(HH:MM) 08:03
HOW MANY?(XXX) 003
  DATE  TIME  TEMP  DEW PT  PRESS  PORT1  PORT2  PORT3
04/01 08:05   1.25 - 7.09  989.12 1.565 1.607 3.152
04/01 08:10   1.25 - 7.09  989.12 1.563 1.604 3.152
04/01 08:15   1.25 - 7.09  989.12 1.563 1.604 3.152

```

5. OPTION D) INITIALIZATION - This option is used to prepare the data logger to take a series of readings. If data is stored in the system, it should be dumped before this routine is entered. After entering the initialization procedure, any old data stored in the system is no longer accessible. To

protect data in case this option is inadvertently entered, after D is typed the user is prompted with INITIALIZATION?(YES,NO). If YES is typed, this routine is entered. Any other character string will cause the processor to back out of this option.

If YES is entered, the user is asked for the current date and time. Format for the date and time are the same as that used in the sequential reading option. When the last character of the time is entered, an internal clock is reset and starts keeping track of elapsed time. The desired starting date and time are requested followed by a request for the date and time of the final reading to be taken. Each date and time are entered in the same format as described in section 4. After the corresponding prompt, the number of minutes of warm up time desired should be entered as a two digit number. When asked for the frequency of measurements, the user should enter a three digit number corresponding to the number of minutes between readings.

If a mistake is made during the preceding part of this option, the ESCAPE key may be used. The user is then prompted to reenter the necessary data. The ESCAPE key may be used several times in order to step backwards through this routine and correct mistakes at any point in the option.

The second part of this routine is now entered. The user is asked if he desires any additional ports. If NO is typed, only the first three dedicated ports are used for temperature, dew point and pressure measurements. If YES is typed, the user

is asked which port he wishes to use. He responds by typing a number between 1 and 8 corresponding to the desired port. He is then asked once more if he desires any additional ports. This process is repeated until all the desired ports are specified and a NO is entered. At this point the user is prompted REASSIGN PORTS?(YES,NO). It is suggested that the port assignments be checked and if an error is found, type YES. This causes the port assignment portion of this option to be reexecuted. If NO is typed, the processor exits this option. It should be noted that an error will result if the same additional port is specified more than once in the second part of this option (i.e., if port 2 is selected twice as an additional port). However, ports 1, 2 and 3 may be specified as additional ports even though they are dedicated for temperature, dew point and pressure measurements. This may be desirable because as dedicated ports, the voltages present at ports 1 and 2 are stored as 8 bit binary words while if they are selected as additional ports, the voltages are stored as 12 bit binary words the second time they are measured. This permits the voltages present at ports 1 and 2 to be stored with greater precision. The remaining port voltages are always stored as 12 bit binary words.

EXAMPLE:

D

INITIALIZATION?(YES,NO)

YESDATE?(MM/DD) 03/31TIME?(HH:MM) 17:22START DATE?(MM/DD) 04/01TIME?(HH:MM) 03:00STOP DATE?(MM/DD) 04/29TIME?(HH:MM) 16:00WARMUP TIME?(MM) 02FREQUENCY OF MEASUREMENTS?(XXX) 005

ADDITIONAL SENSORS?(YES,NO)

YESPORT NUMBER?(1-8) 1

ADDITIONAL SENSORS?(YES,NO)

YESPORT NUMBER?(1-8) 2

ADDITIONAL SENSORS?(YES,NO)

YESPORT NUMBER?(1-8) 3

ADDITIONAL SENSORS?(YES,NO)

NO

REASSIGN PORTS?(YES,NO)

NO

NOTE: No provision has been made to keep track of the year in which a measurement is made. Because of this, if the system is initialized in the year before readings are to begin, the months of the year in which readings occur are numbered, 13, 14, etc. Further, the data logger treats all months in the second year as if they contain 28 days. Since this system was not intended to run for periods of more than one month unattended, this should present no problem. As an example, if the data logger is initialized on December 30, 1979 and is to start taking readings on January 3, 1980, the starting date used in the initialization routine would be 13/03. (Regardless of the year the data logger assumes February has 28 days.)

6. OPTION E) DATA DUMP - This option is designed to permit the data logger to be interfaced to an external device through the peripheral connector in order to dump the collected readings. The routine is entered by typing E. At this point, any key may be typed except CONTROL S and it will merely be echoed to the terminal or peripheral (depending on the position of the DEVICE SELECTION switch as described in section 1.1-6). To dump data to an external device (through the peripheral connector), place the DEVICE SELECTION switch to PERIPHERAL. The external device now transmits to the terminal and the terminal transmits to the external device through the data logger. If the data dump switch is depressed, the data logger transmits a short header followed by all the data it has collected. When all of the data has been transmitted, the data logger once again echos all input characters. Control S must be used to leave this routine. The formats of the header and data are now discussed.

a). HEADER - After the data dump switch is depressed, the first six printed lines contain the initializing parameters. All lines in the header are left justified and separated by a carriage return, line feed and two delete characters. The first two lines consist of starting date and time and ending date and time respectively. All dates and times are transmitted in five character fields identical to the format required to enter dates and times in the initialization option. Separating each date and time is a field of five blanks. Line three contains the warm up time represented as a two digit decimal number. Line

four contains the frequency of measurements in the same five-character field as described for the starting and stopping times. A two digit hexadecimal number in the fifth line gives the number of data words stored each time a reading is taken. Another two digit hexadecimal number in the sixth line tells which ports are selected for measurement in addition to the three dedicated ports (which are always measured). This information is conveyed by examining the binary equivalent of the number. Each of the eight binary digits corresponds to a port. If a digit is one, the corresponding port is selected for additional measurement. Letting the least significant digit be bit 0 and the most significant digit be bit 7, the correspondences of binary bits in this eight bit word and ports are as follows;

```
BIT 0 - PORT 1
BIT 1 - PORT 2
BIT 2 - PORT 3
BIT 3 - PORT 4
BIT 4 - PORT 5
BIT 5 - PORT 6
BIT 6 - PORT 7
BIT 7 - PORT 8
```

b) DATA FORMAT - The seventh and all following lines are devoted to collected data. Each line corresponds to a separate reading and is set off from succeeding lines by a carriage return, line feed and two delete characters as before. Within each line the unsigned hexadecimal data words are separated from each other by a single space. The first two data words of each line are two digits long. They represent the temperature port

voltage (port 1) and the dew point port voltage (port 2) respectively. The remaining data words on each line are three digits long. The first of these numbers always represents the pressure port voltage (port 3). From this point on, the data words represent voltages on additional ports selected by the user starting with the smallest port number and continuing to the largest port number. To convert these hexadecimal data words to corresponding voltages, the decimal equivalent of each data word must first be obtained. Equation 1 is used to convert the first two data words. The remaining data words are converted using equation 2.

- 1) DATA WORD X 5/256 = V(PORT)
- 2) DATA WORD X 5/4096 = V(PORT)

In order to convert port voltages on ports 1, 2 and 3 to temperature, dew point and pressure, the conversions listed below may be used;

PORT 1	TEMP(DEGREES C) = V(PORT1) X 20.00 - 30.00
PORT 2	DEW PT.(DEGREES C) = V(PORT2) X 16.80 - 34.00
PORT 3	PRESSURE(mb) = V(PORT3) X 60.00 + 800.00

1.4 HARDWARE SETUP

Before applying power the system must be configured correctly. This involves checking the memory configuration, the power switching board, and the baud rate.

The memory must start at address 4000 (hex) and be contiguous upward. Each memory board has a dip switch which determines the location of the memory board. The starting addresses of the memory are written next to each switch on the board. (Either the full four digits or the first two digits of the address are given. For example, on the 2K memory board 42 indicates the starting address is 4200 (hex).) The memory addresses must not overlap, therefore, if both 2K and 4K memory boards are used in the data logger the 4K boards must reside at lower addresses than the 2K boards. When a 4K board and a 2K board are used, the 4K board must be set at 4000 and the 2K board at 5000 (50). (In hex 1K=400, 2K=800, 3K=C00, 4K=1000.) It is important that only one switch is closed at any time.

The available memory must be checked to insure that enough is available to record the number of readings desired. The following two formulas may be used to calculate how much memory is required or how many readings may be taken:

ADDSEN=The number of additional ports selected.

MEM=The amount of memory in system.

READINGS=The total number of readings you want to take.

Memory needed(bytes)=READINGSX $\lceil 2+1.5X(ADDSEN+1) \rceil +64$

Number of readings allowable=(MEM-64)/ $\lceil 2+1.5X(ADDSEN+1) \rceil$

The symbols \lceil, \rceil indicate "smallest integer larger than".

EXAMPLES:

For example, if one additional port is selected and readings are taken every hour for 31 days, then the amount of memory needed would be

$$\begin{aligned} \text{MEMORY} &= (31 \times 24) \times \lceil 2 + 1.5 \times (1 + 1) \rceil + 64 \\ &= 744 \times \lceil 5 \rceil + 64 \\ &= 3784 \\ \text{APPROX.} &= 3.8\text{K} \end{aligned}$$

If no additional ports are selected how long can readings be taken every half hour with 6K of memory?

$$\begin{aligned} \text{NUMBER OF ALLOWABLE READINGS} &= (6\text{K} - 64) / \lceil 2 + 1.5 * (0 + 1) \rceil \\ &= 6080 / \lceil 3.5 \rceil \\ &= 1520 \end{aligned}$$

Number of Days= 31.5

It is important to remember that the data logger can not tell if it tries to store readings and has exceeded the available memory. If this happens, sequential readings and data

dump will print out incorrect values for the readings that it tried to store after exceeding the available memory. The data logger may be configured with up to 12K bytes of memory.

A dip switch for the baud rate selection is located on the processor board. All communication between the data logger, terminal, and peripheral must occur at the same rate. The available baud rates are 110, 150, 200, 300, 600, 1200, 1800, and 2400. It is important that only one of the switches is closed at any one time!

The power board has three dip switches that control the power switching relays. If the processor power is going to be left on during the entire time the data logger is operating then these three switches should be left off. This will disable the relays and extend their life. If the data logger will operate with the power off, these three switches must be on.

1.5 RECOMMENDED STARTUP PROCEDURE

1. Configure the memory correctly.
2. Ensure that sufficient memory is available.
3. Check the relay dip switches on the power board for the correct position.

4. Check the baud rate.
5. Turn off the MAIN POWER switch.
6. Connect the +12VDC to the Back Panel.
7. Turn on the PROCESSOR POWER and I/O POWER switches.
8. Turn the DEVICE SELECTION switch to data logger.
9. Connect the sensors.
10. Turn on the MAIN POWER switch.
11. Depress the RESET switch.
12. Use the software options as desired. When initializing the data logger remember;
 1. The internal clock is reset when the last digit of the present time is entered.
 2. The starting date/time must be after the present date/time.
 3. The stopping date/time must be after the starting date/time.
 4. The warm up time must be less than (stopping date/time - present date/time).
 5. The frequency of measurements must be greater than the warm up time. If the warm up time is greater than the desired frequency of measurements, then set warm up time

equal to zero and always leave the processor power on.

6. If a correction is made when specifying the additional ports all ports must be respecified.

If any of these rules are violated the data logger will not work correctly.

13. Use current readings to check sensor connections.
14. Turn off the I/O power when done.
15. If the processor power does not have to be left on, turn it off. Turn off processor power only when informed by the user option page that warm up power is off.

Once initialized the system can be turned on again as follows:

- 1) Turn I/O power on.
- 2) Turn the processor power on.
- 3) Depress the reset switch.

The terminal and/or peripheral can be connected (or disconnected) to the data logger at any time.

SECTION 2 HARDWARE DESCRIPTION

2.1 SYSTEM DESCRIPTION

This data logger was designed using the Motorola 6800 microprocessor family. A detailed understanding of the system operation requires familiarity with this 8-bit microprocessor family. However, the general overview which follows requires only a broad knowledge of a few of the 6800 family components.

a). The microprocessor (6802) has an eight bit bi-directional data bus and a sixteen bit address bus. Included on the chip are 128 bytes of random access memory (RAM) which are used as scratchpad memory and stack. Three other inputs of interest are IRQ, NMI and RESET. When IRQ or NMI is driven to zero volts, an interrupt or non-maskable interrupt, respectively, is initiated. The processor then starts executing routines to service these interrupts before returning to its original activity. When RESET is driven low, processor activity halts and its registers are initialized. When RESET goes high, the processor starts executing the restart routine.

b). The ACIA (M6850) is an Asynchronous Communications Interface Adapter. This device interfaces the microprocessor to a serial communications line. It requires an external clock to establish the baud rate.

c). The PIA (M6821) is a Peripheral Interface Adapter. This chip contains two eight bit ports and additional inputs which may be used to trigger interrupt requests to the processor. Each port (referred to as ports A and B) may have lines individually selected as inputs or outputs by software. In addition, port A is CMOS compatible.

The data logger is broken into the following five assemblies (see Figure 3).

- 1). PROCESSOR
- 2). CLOCK
- 3). A/D CONVERTER
- 4). MEMORY
- 5). POWER SWITCHING

Each assembly is housed on a separate printed circuit board. The memory, however, can be expanded simply by adding cards until a maximum of 12K bytes of RAM are used. This permits more data storage and thus longer periods of operation before dumping data is required.

The following sections briefly describe each board and indicate how boards interact with each other. A voltage followed by (s) is a switched voltage and is present only when the processor is powered. When +15 and -15 volts are referred to and no (s) follows them, the voltages are present when warm up power is on. If +5 volts is not followed by (s), it is the

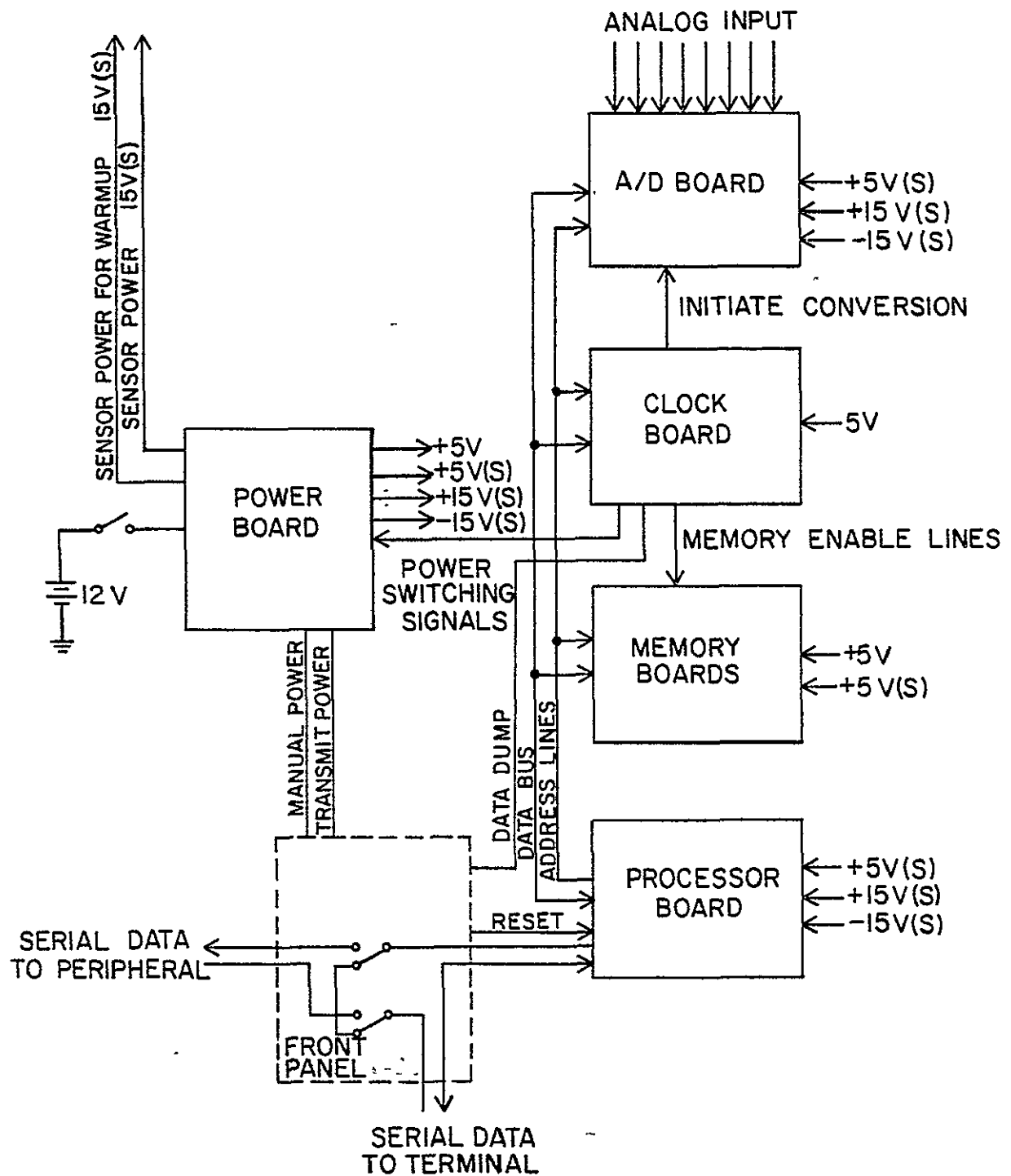


FIGURE 3
DATA LOGGER BLOCK DIAGRAM

unswitched voltage which is present whenever the MAIN POWER switch is on and +12 volts are applied to the system.

2.2 PROCESSOR BOARD

The processor board controls the overall operation of the system and provides serial communications with external devices via an RS-232C interface over which serial ASCII characters with even parity are transmitted and received. A block diagram of the board (as seen in Figure 4) is utilized to permit a general discussion of the board's operation. Detailed schematics of all the boards may be found in the appendix.

a). Power source requirements are listed below.

VOLTAGE	CURRENT (TYPICAL)	CURRENT (MAXIMUM)
+5(s)	400 ma	700 ma
+15 and -15	14.6 ma	19.3 ma

b). Operation - The processor may be broken into four basic sections.

Processor Section - The bulk of the processor board is devoted to the processor section. It is organized around the M6802 microprocessor. Decoding of high order address lines to enable the clock PIA, A/D PIA, ACIA and EPROMS (eraseable programable read only memories) is achieved with a 4 line to 16 line decoder (74154). Reset signals for PIA and processor initialization are generated here through a combination of RC

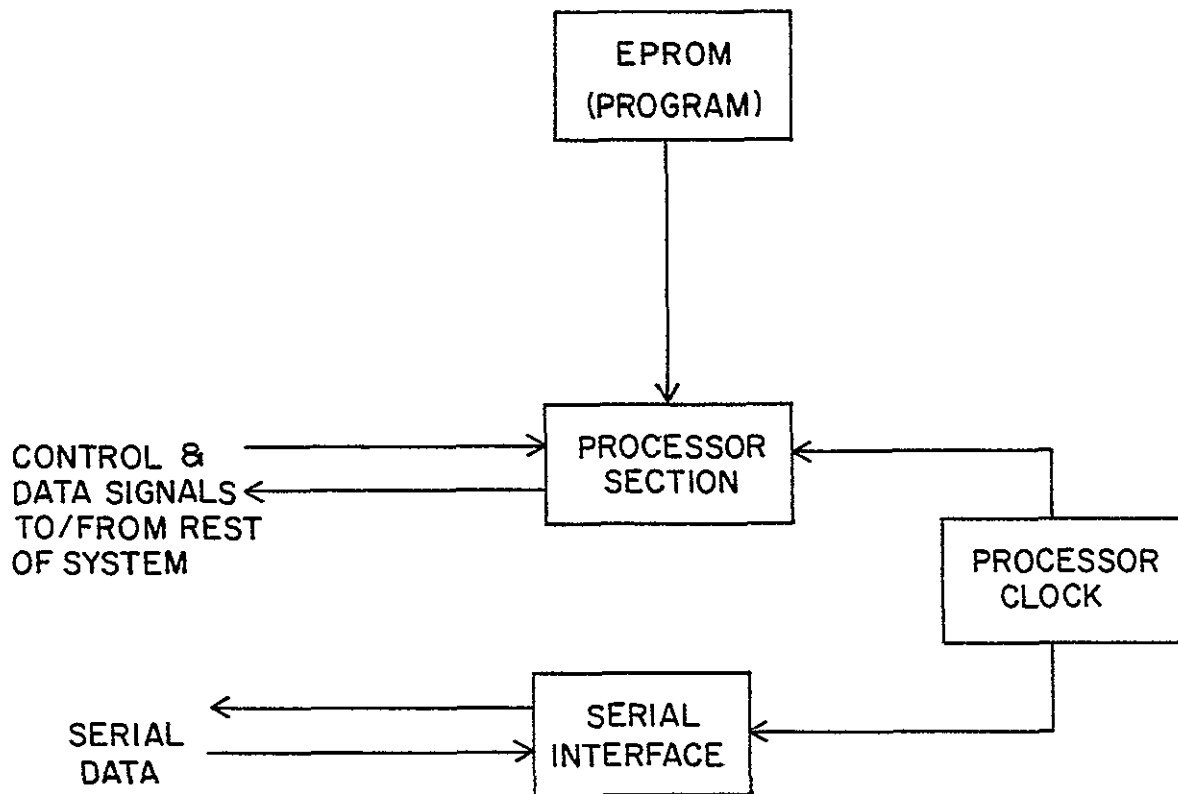


FIGURE 4
PROCESSOR BOARD BLOCK DIAGRAM

networks and schmidt triggers (7414). In order to interface the processor to the (relatively) slow EPROMs, the memory ready signal (MR) is held low for 870 nanoseconds after VMA (valid memory address) and E (enable) go high, thus slowing down the processor when it accesses external devices. Finally, read/write signals (R/\overline{W} + MR) and enable ($E \cdot VMA$) for use by memory boards originate in this section.

Processor Clock - Basic timing for both the processor and the serial interface portions of the processor board are provided by the processor clock. It consists of a baud-rate generator (MC14411) and a 1.843 MHz crystal. A buffered 1.843 MHz signal drives the processor while eight signals ranging from 1.7588 KHz to 38.4 KHz drive the serial interface. This variety of frequencies permits manual selection of baud rates from 110 to 2400 baud.

Serial Interface - This section transmits and receives asynchronous serial data via the ACIA and makes it available to the processor on the eight bit data bus. Upon receipt of a complete, even parity ASCII word, an ACIA generated interrupt request causes the processor to execute the IRQ routine. The TTL transmit and receive lines from the ACIA are interfaced to RS-232C levels through standard interface adapters (M1488 and M1489 chips).

EPROM - The program for the data logger is contained in two

INTEL 2716 EPROMS. This yields a total program space of 4K bytes.

2.3 CLOCK BOARD

The clock board times the intervals between periods of processor activity such as data collection and switching on warm up power. It is also the source of various control signals used by other boards in the data logger.

a). Power supply requirements are listed below.

VOLTAGE	CURRENT (TYPICAL)	CURRENT (MAXIMUM)
+5	1 ma	9 ma
+5(s)	120 ma	300 ma

b). Operation - Timing is achieved by a combination of crystal controlled oscillator, counters, shift registers and coincidence gates. The crystal accuracy is approximately 0.01% over the temperature range -25 to +85 degrees centigrade. A one cycle per minute pulse is obtained from a 262.144 KHz oscillator and counting circuits. This signal increments a 12 bit counter. A comparison occurs between the contents of this counter and a 12 bit shift register containing the number of minutes in the desired interval. As soon as the two numbers match, a monostable multivibrator is triggered. This signal resets the minute counter, triggers an NMI request to the processor (in

conjunction with the clock PIA) and exits the board to turn on all power at the power board.

The monostables which turn power off are also located on this board. In order to conserve power, the PIA which controls them is powered down except when the processor is in operation. To prevent false triggering, two bits of different levels are required from the PIA before either monostable is triggered. The same technique is used to reset the clock when the system is first initialized and to disable RAM whenever the processor is not powered.

Two more lines from the clock PIA are used elsewhere in the data logger. One triggers a conversion cycle by the A/D converter. The remaining line is configured as an input and responds to the data dump switch on the front panel. Table 1 lists the PIA port lines and their uses.

2.4 A/D BOARD

This board interfaces 8 single ended analog inputs to the processor. The digitized data is read by the processor as a 12 bit CSB (complementary straight binary) word.

a). Power supply specifications are listed below.

TABLE 1

Clock PIA

PORT A			PORT B		
BIT	PIN	FUNCTION	BIT	PIN	FUNCTION
0	2	Reset Time	2	12	Turns Off Interrupt
1	3	Clock for Shift Registers	3	13	Data Dump Switch
2	4	Data for Shift Registers	5	15	Initiate A/D Conversion
3	5	Enables Power Down Monostables			
4	6	Power Off (2)			
5	7	Power Off (3)			
6	8	Memory Enable			
7	9	Memory Enable			

A/D PIA

PORT A			PORT B		
BIT	PIN	FUNCTION	BIT	PIN	FUNCTION
0	2	D0 (LSB)	0	10	D8
1	3	D1	1	11	D9
2	4	D2	2	12	D10
3	5	D3	3	13	D11 (MSB)
4	6	D4	4	14	Conversion Status
5	7	D5	5	15	Channel Select (S0)
6	8	D6	6	16	Channel Select (S1)
7	9	D7	7	17	Channel Select (S2)

VOLTAGE	CURRENT (TYPICAL)	CURRENT (MAXIMUM)
+5(s)	150 ma	278 ma
+15 and -15	21.3 ma	23.0 ma

b). Operation - Three PIA lines are used to select the desired analog input at a CMOS switch (14529B). Before reaching the A/D converter (ADC80AG-12), the signal is buffered by a unity gain non-inverting operational amplifier (OP-02). The DC offset of this op-amp is adjusted by a potentiometer located on this board.

A conversion is initiated by (INIT CONV) • (CONV STATUS) where INIT CONV is the processor controlled signal from the clock board PIA and CONV STATUS is a status bit from the A/D converter. A low on CONV STATUS indicates that no conversion is currently occurring. Upon satisfaction of these requirements, a monostable is triggered which ultimately yields a 2.0 microsecond pulse to the A/D converter. This triggers a conversion cycle which takes 25 microseconds to complete. Upon completion, the status bit goes low informing the processor, through the A/D PIA, of the conversion status and enabling subsequent conversions. Table 1 lists the A/D PIA port lines and their uses.

2.5 MEMORY BOARDS

Two types of CMOS memory boards are available. Both types are similar except in memory capacity (2K bytes versus 4K bytes) and in the differences in address decoding this necessitates.

a). Power requirements are listed below. Standby currents pertain to the boards' current requirements when the boards are not being accessed by the microprocessor. Operation current refers to the current requirements when the board is being written to or read from by the microprocessor.

<u>CURRENT</u> VOLTAGE	4K BOARD		2K BOARD	
	TYPICAL	MAXIMUM	TYPICAL	MAXIMUM
+5 (STANDBY)	8	5 ma	8	80
+5 (OPERATION)	40 ma	50 ma	80 ma	96 ma
(UNLESS NOTED, ALL CURRENTS ARE IN MICROAMPS)				

b). 4K Byte Board - Eight 2K X 4 bit Harris 6514 CMOS memory chips form the nucleus of this board. Address decoding is performed in such a manner as to permit each board to be located at hex locations 4000, 5000, 6000 or 7000 in the memory space. These locations are selected by a DIP switch mounted on the board. The board may be enabled or disabled under processor control by MEM EN and MEN. This combination of two signals to enable memory prevents erroneous data being written into the memory boards when processor and PIAs are powered down.

c). 2K Byte Board - Sixteen 1K X 1 bit Harris 6508 CMOS memory chips are utilized on this board. Operation of this board is similar to the 4K board except that the board may be located at 4000, 4800, 5000, 5800, 6000, 6800, 7000 or 7800 in the memory space.

2.6 POWER SWITCHING BOARD

The power switching board generates all the required supply voltages and switches +5, +15 and -15 volt supply lines in order to conserve power.

a). Power requirements are listed below.

QUIESCENT CURRENT			
VOLTAGE	DEVICE	TYP	MAX
+12	LM 240LH	1 ma	5 ma
	LM 209K	2 ma	8 ma
A12/D15/150/Z			
EFFICIENCY RATED AT 70% AT FULL LOAD (150 ma AT +15 AND -15 VOLTS)			

b). Operation - Two five volt regulators and one plus and minus fifteen volt converter are utilized on this board. The LM240LH +5 volt regulator provides for the low current requirements of the memory boards and clock board and is therefore always in operation. The higher current requirements of the A/D and processor boards are handled by the LM209K. In order to conserve power, the input to this regulator is switched

off when the processor and A/D boards are not in use. The output of the A12/D15/150/Z plus and minus fifteen volt voltage converter is used to drive sensors (external to the data logger), components on the A/D board and the RS-232C transmitter on the processor board. The input and outputs to this device are switched in such a manner as to permit a warm up period for sensors during which the processor and A/D boards are not powered.

Voltage switching is accomplished with three Teledyne 720-5 magnetic latching relays. These relays control inputs and outputs of the regulators and converter on this board. Signals controlling these relays originate on the clock board and have the following effects;

POWER ON (1) Turns on all switched voltages

POWER OFF (2) Turns off all switched voltages

POWER OFF (3) Turns off switched voltages except warm up voltages

If switched operation is not desired (i.e. when power need not be conserved) switches have been provided to disconnect these signals and thus extend relay life.

2.7 SYSTEM POWER REQUIREMENTS

As previously mentioned, only 12 vdc is required to power the data logger and all external sensors. The current requirement of the system is dependent upon the activity of the system.

This may be broken into three areas;

Period 1) All voltages on (processor taking readings).

Period 2) Warm up power on, all switched voltages off (sensors warming up).

Period 3) Warm up power and all switched voltages off (waiting for next NMI).

The approximate current requirement in milliamps for each of these periods is given below;

	TYPICAL	MAXIMUM
Period 1)	$(820 + R/0.65)$	$(1300 + R/0.65)$
Period 2)	$(10 + RW/0.65)$	$(40 + RW/0.65)$
Period 3)	3	40

In the above, R is the total current required by all sensors and RW is the current required by sensors powered by the warm up voltages. Although the length of Period 1 is dependent upon the number of sensors selected during the initialization procedure, it is typically on the order of 0.75 seconds for three sensors.

If the system is to be powered by batteries, the required amp hour rating may be found by multiplying the current drain associated with each of the above periods by the total time each of these periods occurs in the anticipated period of system operation.

SECTION 3 SOFTWARE DESCRIPTION

The software controls all of the hardware in the data logger, the selection of which power is on or off and the acquisition of data. A detailed description of the software (the variables used and the function of each subroutine) is given in the program listing in the appendix. (The reader is advised to read this section first before trying to understand the program listing.)

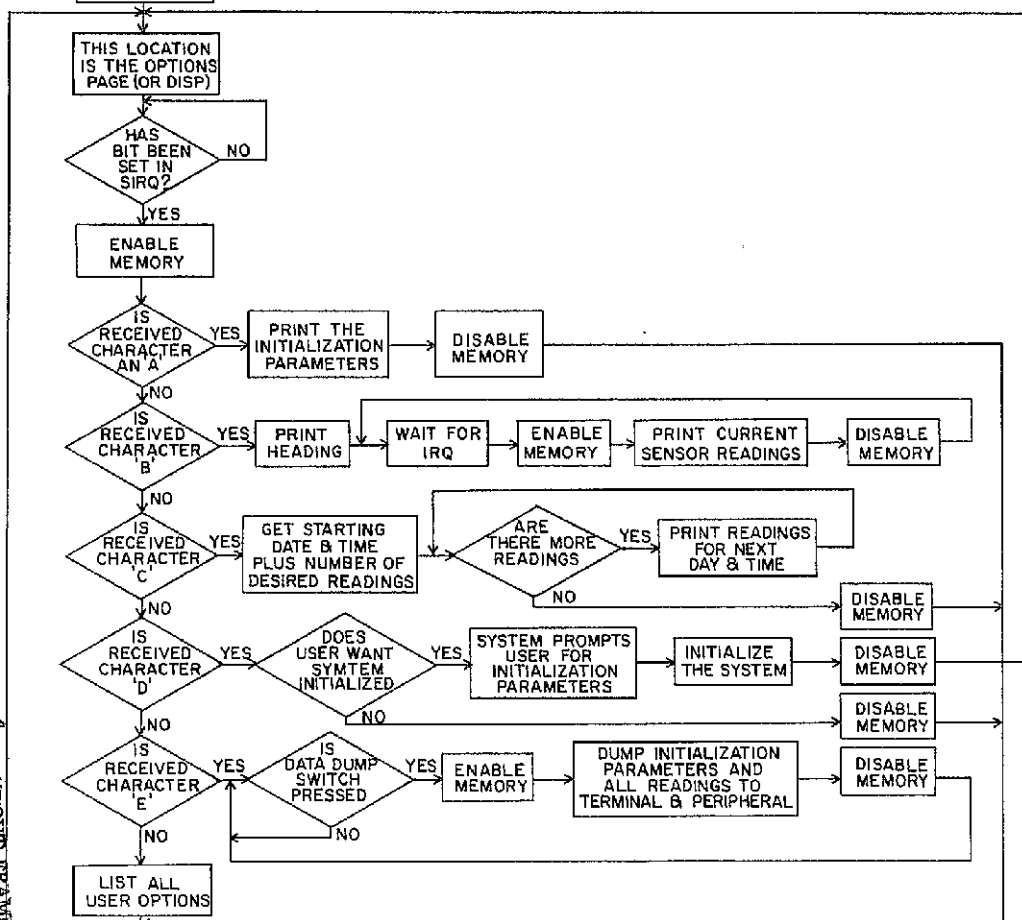
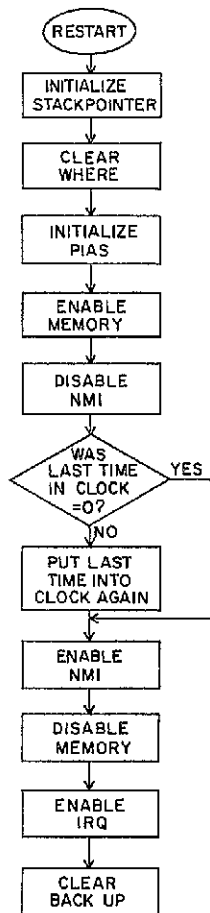
Whenever a user turns the system on and presses reset, or anytime the clock board finishes timing an interval and turns on the power the microprocessor starts the RESET routine (see figure 5). The reset routine can be divided into two parts; initialization of the newly powered system, and the user options. The reset routine initializes the stack pointer in order that stack operations and subroutine calls can be done. The PIAS are then initialized. Since turning on the power may change the timing interval in the clock, the clock is momentarily disabled and the time for the NMI is reset. Some of the on board RAM variables are cleared (specifically WHERE and BACKUP).

The second part of the reset routine waits for the user to type a key. If the key corresponds to a user option, the option is entered and the desired information is displayed or the user is prompted further. If the user types an invalid key the user options are retyped. NOTICE: When the user is at the user option page the memory is always disabled; at NO other time should the processor power be turned off.

The hardware interrupt (IRQ) is connected to the ACIA. Thus, whenever a character is received an IRQ interrupt is triggered. The IRQ flowchart is shown in figure 6. The received character is first checked for validity (even parity is used). An invalid character results in the printing of a question mark. A valid character is printed and stored in the location RECEIVE.

If the received character is an ESCAPE then BACKUP is checked. If BACKUP is zero the escape does nothing and a RTI (Return from Interrupt) is executed. If the escape is valid, the correct number of words are removed from the stack (until address STRIP is reached), a '*DEL*' is printed, and the user jumps back an input line. If the received character is a CONTROL S, several words from the stack are removed (as if a RTI was executed) and the user is returned to the options page. Every time a valid character is received a bit in location SIRQ is set. This tells the user option page that the user has pressed a key and would like something done for him.

1
10100101
FRAME



2
10100101
FRAME

Figure 5

RESET ROUTINE FLOWCHART

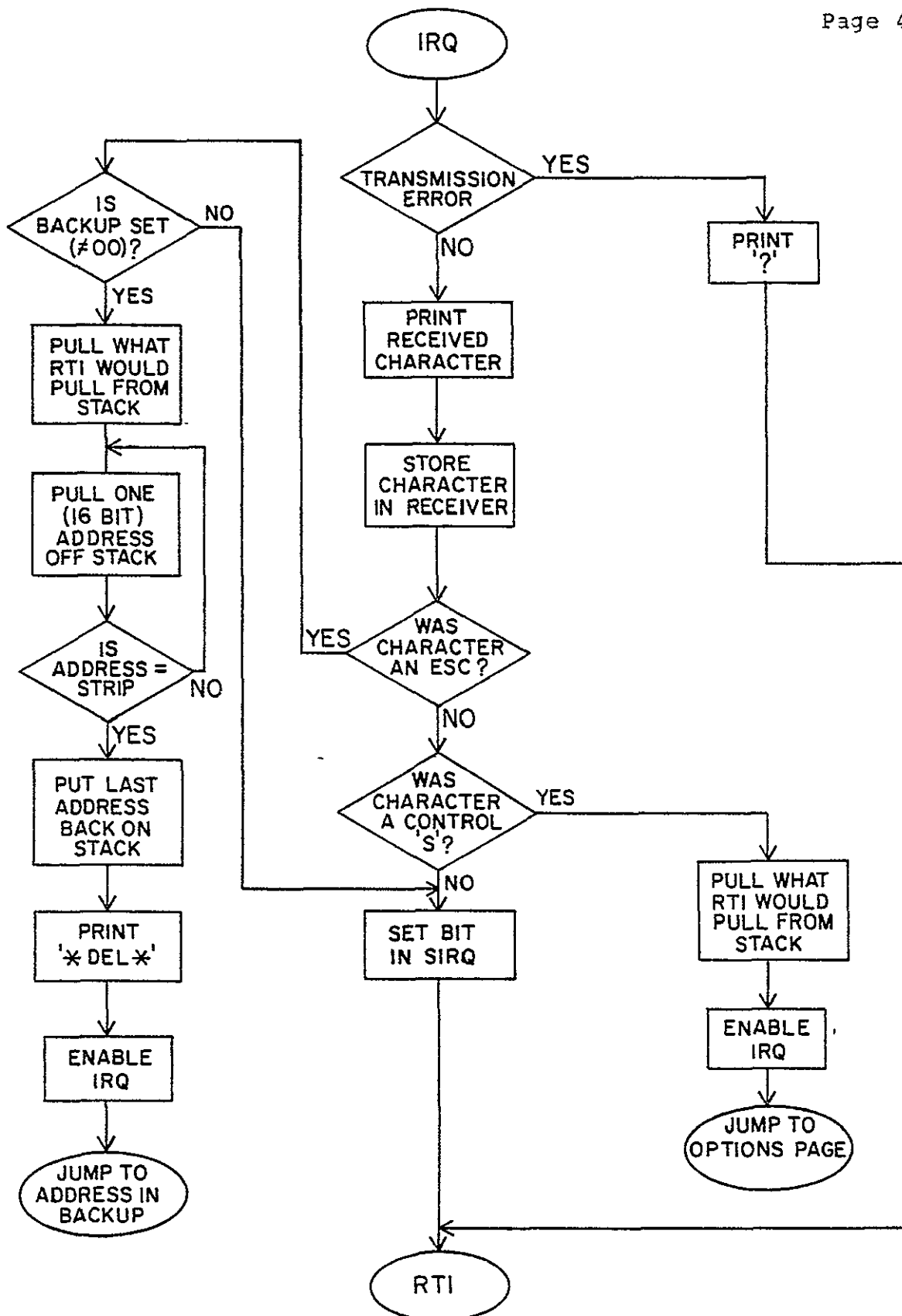


FIGURE 6

IRQ ROUTINE FLOWCHART

The non-maskable interrupt (NMI) is triggered by the falling edge of the one-shot that turns the power on. This occurs after the clock matches a time (typically the interrupt is triggered 8 ms after the processor has started the restart routine). The NMI usually indicates that warm up power to the sensors must be turned on or that a reading must be taken. The NMI routine (see figure 7) can be divided into three parts; preliminary initialization, determining the reason for the NMI and performing that function, and lastly, properly returning to the interrupted section of the restart routine.

The preliminary initialization finishes the initialization of clock b (the clock PIA, port B), and disables the clock board from further (or multiple) interrupts. A delay is performed (approximately 0.5 seconds) to allow sensors without warm up power to stabilize. Variables used in sequential readings are saved, and all variables are restored to their correct values. The present time (stored in memory) is then updated.

The NMI routine decides what should be done from the value of the status word (see figure 7). Before the NMI routine is finished the status word is changed to reflect what functions need to be performed next time. If all measurements have been taken, 68 hours and 15 minutes is loaded into the clock and all power is turned off. If measurements have not started yet and if they start today, their starting time is loaded into the clock. Otherwise 24 hours is loaded into the clock. (To allow a warm up time, on the days before the readings start NMI occurs

at a warm up time before midnight.) All power is then turned off.

If the readings have already started, either warm up power is turned on or the appropriate readings are taken. If readings are taken, eight are averaged together for each port. After the readings are taken a check is performed for stopping time. If the stopping time has not been reached, the frequency of measurements minus the warm up time is loaded into the clock. If it wasn't warm up time all power is turned off; otherwise warm up power is left on.

Every time power is turned off a software delay (approximately 0.5 seconds) is performed. If the processor power switch is on, the delay has no purpose. If the switch is off, the delay makes certain that the processor executes no other instructions while power is turning off.

The final section of the NMI routine checks if the user options current readings, sequential readings, or dump was interrupted. If none of the above options were interrupted, a RTI is executed (the memory is still disabled). If dump was interrupted, the memory is enabled before the RTI. If sequential readings were interrupted, the current line of output is restarted on the next line. If current readings were interrupted, the current line of output is terminated.

WOLDOU FRAME

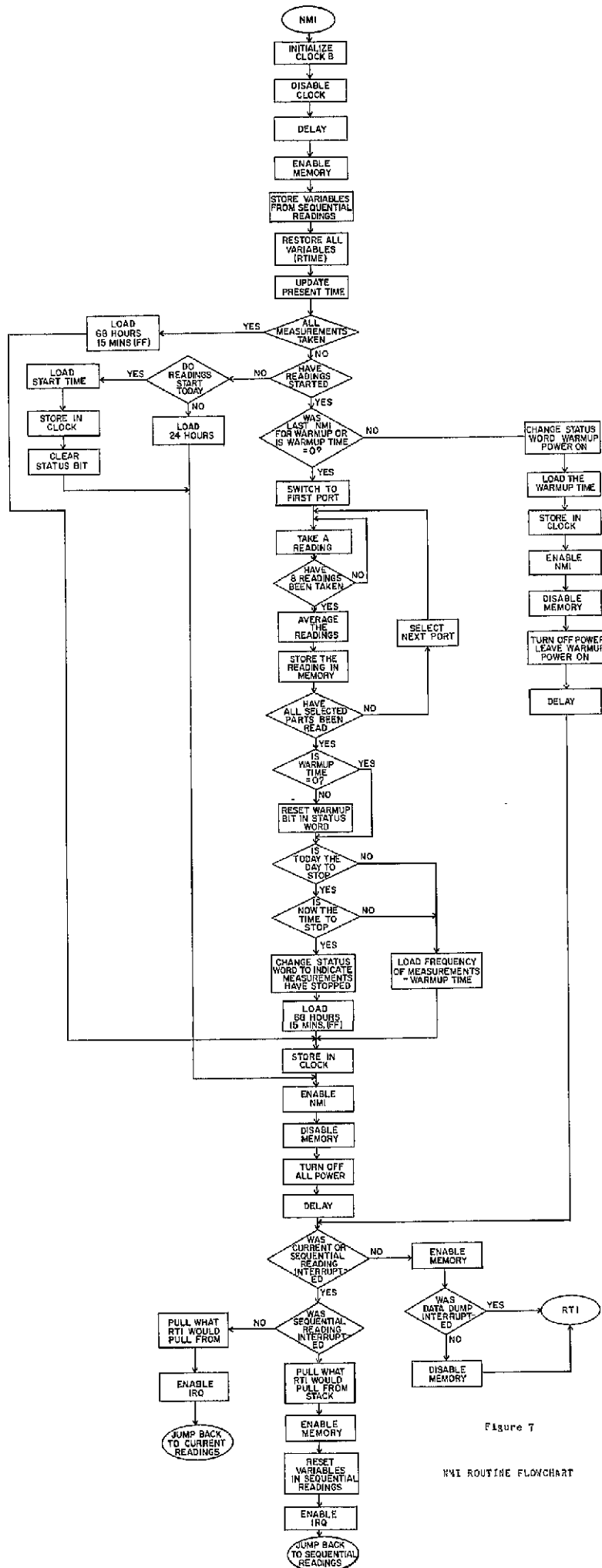


Figure 7

NMI ROUTINE FLOWCHART

WOLDOU FRAME

APPENDIX

CONNECTOR DESCRIPTIONS

A) TERMINAL AND PERIPHERAL CONNECTORS AMPHENOL 25 PIN (FEMALE) D-SERIES BODY

PIN 2	TRANSMITTED DATA (even parity ASCII, RS232-C standard)
PIN 3	RECEIVED DATA (even parity ASCII, RS232-C standard)
PIN 4	REQUEST TO SEND
PIN 5	CLEAR TO SEND
PIN 6	DATA SET READY
PIN 7	GROUND

B) PORT (SENSOR) CONNECTORS CANNON DEM5W1S WITH CENTER COAXIAL CONNECTION (RG/50)

PIN 1	-15 VOLT (WARM UP)
PIN 2	+15 VOLT (SWITCHED)
PIN 3	+15 VOLT (WARM UP)
PIN 4	-15 VOLT (SWITCHED)

COAXIAL

CENTER	ANALOG INPUT (0-5 VOLTS)
SHEILD	GROUND

POWER SWITCHING BOARD
EDGE CONNECTOR SIGNALS

1	SWITCHED +12V FROM FRONT PANEL
2	SWITCHED +12V FROM FRONT PANEL
3	
4	POWER ON (1)
5	POWER OFF (2)
6	POWER OFF (3)
7	
8	
9	
10	GND
11	+12VDC
12	
13	
14	
15	GND
16	+5V
17	
18	+5V (SWITCHED)
19	+15V (SWITCHED)
20	-15V (SWITCHED)
21	-15V
22	+15V

MICROPROCESSOR BOARD
EDGE CONNECTOR SIGNALS

1	A0
A	A1
2	A2
B	A3
3	A4
C	A5
4	A6
D	A7
5	A8
E	A9
6	A10
F	A11
7	A12
H	A13
8	A14
J	A15
9	D0
K	D1
10	D2
L	D3
11	D4
M	D5
12	D6
N	D7
13	R/ \overline{W}
P	E
14	\overline{RESET}
R	\overline{NMI}
15	RESET (MANUAL)
S	RESET (MANUAL)
16	PIA SELECT (CLOCK)
T	PIA SELECT (A/D)
17	GND
U	R/W + MR
18	GND
V	
19	
W	+5V (SWITCHED)
20	E • VMA
Y	
21	+15V (TRANSMIT POWER)
Y	TRANSMIT SERIAL DATA
22	-15V (TRANSMIT POWER)
Z	RECEIVE SERIAL DATA FROM TERMINAL

CLOCK BOARD
EDGE CONNECTOR SIGNALS

1	A0
A	A1
2	<u>MEMORY ENABLE</u>
B	<u>MEMORY ENABLE</u>
3	
C	
4	DATA DUMP
D	POWER ON (1)
5	
E	POWER OFF (2)
6	
F	POWER OFF (3)
7	
H	INITIATE CONVERSION (TO A/D BOARD)
8	
J	
9	D0
K	D1
10	D2
L	D3
11	D4
M	D5
12	D6
N	D7
13	<u>R/\overline{W}</u>
P	<u>E</u>
14	<u>RESET</u>
R	<u>NMI</u>
15	
S	
16	PIA SELECT (CLOCK)
T	
17	GND
U	
18	
V	+5V
19	
W	+5V (SWITCHED)
20	
X	
21	
Y	
22	
Z	

A/D BOARD
EDGE CONNECTOR SIGNALS

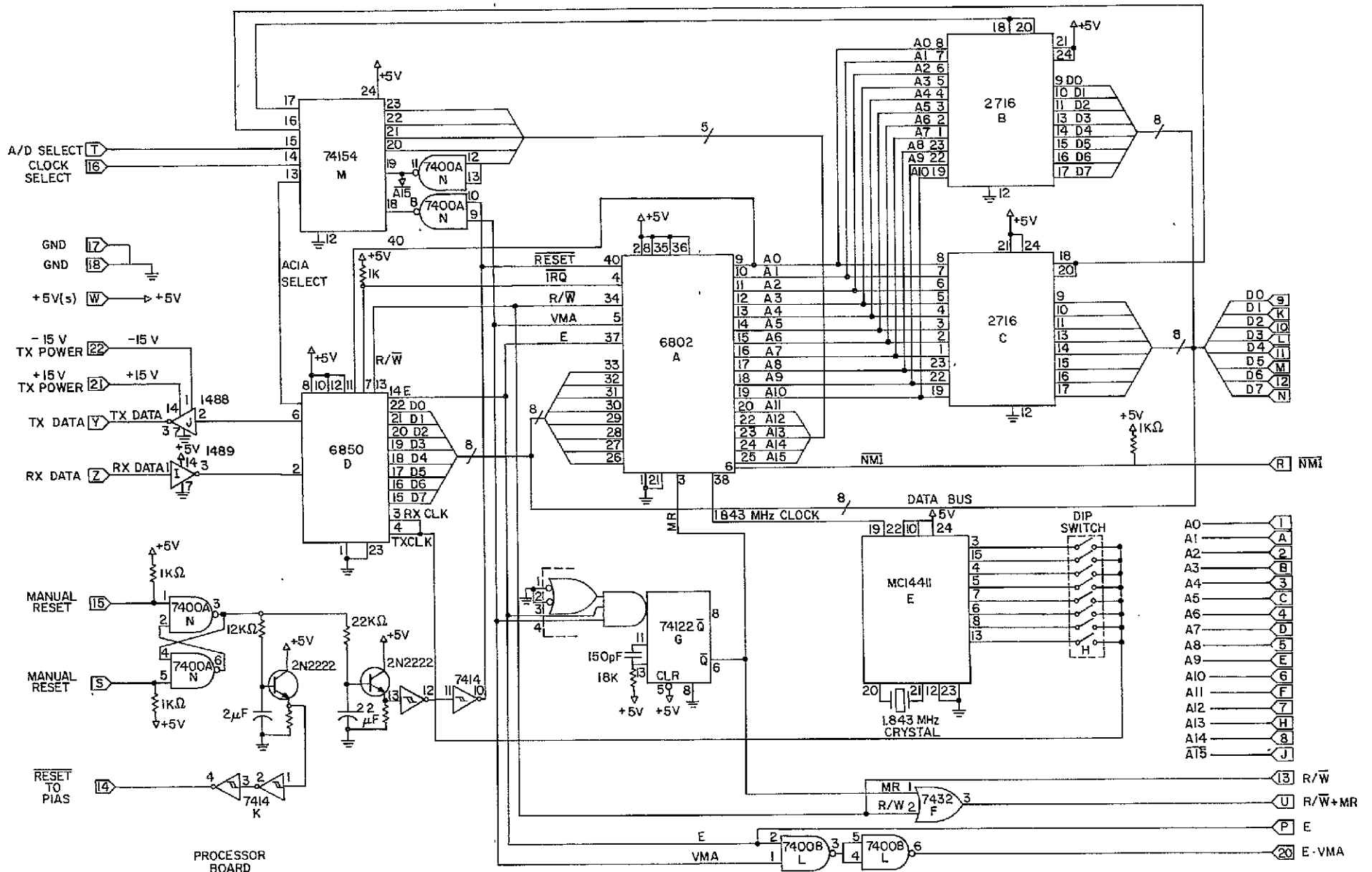
1	A0
A	A1
2	
B	
3	
C	
4	
D	
5	
E	
6	
F	
7	INITIATE CONVERSION
H	
8	
J	
9	D0
K	D1
10	D2
L	D3
11	D4
M	D5
12	D6
N	D7
13	R/ \overline{W}
P	\overline{E}
14	\overline{RESET}
R	
15	
S	
16	
T	PIA SELECT (A/D)
17	GND
U	
18	GND
V	
19	
W	+5V (SWITCHED)
20	
X	+15V (SWITCHED)
21	
Y	-15V (SWITCHED)
22	
Z	

MEMORY BOARDS
EDGE CONNECTOR SIGNALS

1	A0
A	A1
2	A2
B	A3
3	A4
C	A5
4	A6
D	A7
5	A8
E	A9
6	A10
F	A11
7	A12
H	A13
8	<u>A14</u>
J	A15
9	D0
K	D1
10	D2
L	D3
11	D4
M	D5
12	D6
N	D7
13	R/ \overline{W} + MR
P	E • VMA
14	
R	
15	
S	
16	
T	<u>MEMORY ENABLE</u>
17	<u>MEMORY ENABLE</u>
U	
18	GND
V	+5V
19	
W	
20	
X	
21	
Y	
22	
Z	

FRONT PANEL CONNECTOR

1	+12V (IN)
2	+12V (OUT)
3	+12V (IN)
4	+12V (OUT)
5	+15V (IN)
6	+15V (OUT)
7	-15V (IN)
8	-15V (OUT)
9	+15V (TRANSMIT POWER)
10	-15V (TRANSMIT POWER)
11	GND
12	RESET (MANUAL)
13	RESET (MANUAL)
14	DATA DUMP
15	REQUEST TO SEND (IN)
16	REQUEST TO SEND (OUT)
17	CLEAR TO SEND (IN)
18	CLEAR TO SEND (OUT)
19	DATA SET READY (IN)
20	DATA SET READY (OUT)
21	SERIAL DATA (FROM PROCESSOR)
22	SERIAL DATA (FROM PERIPHERAL)
23	SERIAL DATA (TO PERIPHERAL)
24	SERIAL DATA (TO TERMINAL)
25	GND



PROCESSOR BOARD SCHEMATIC

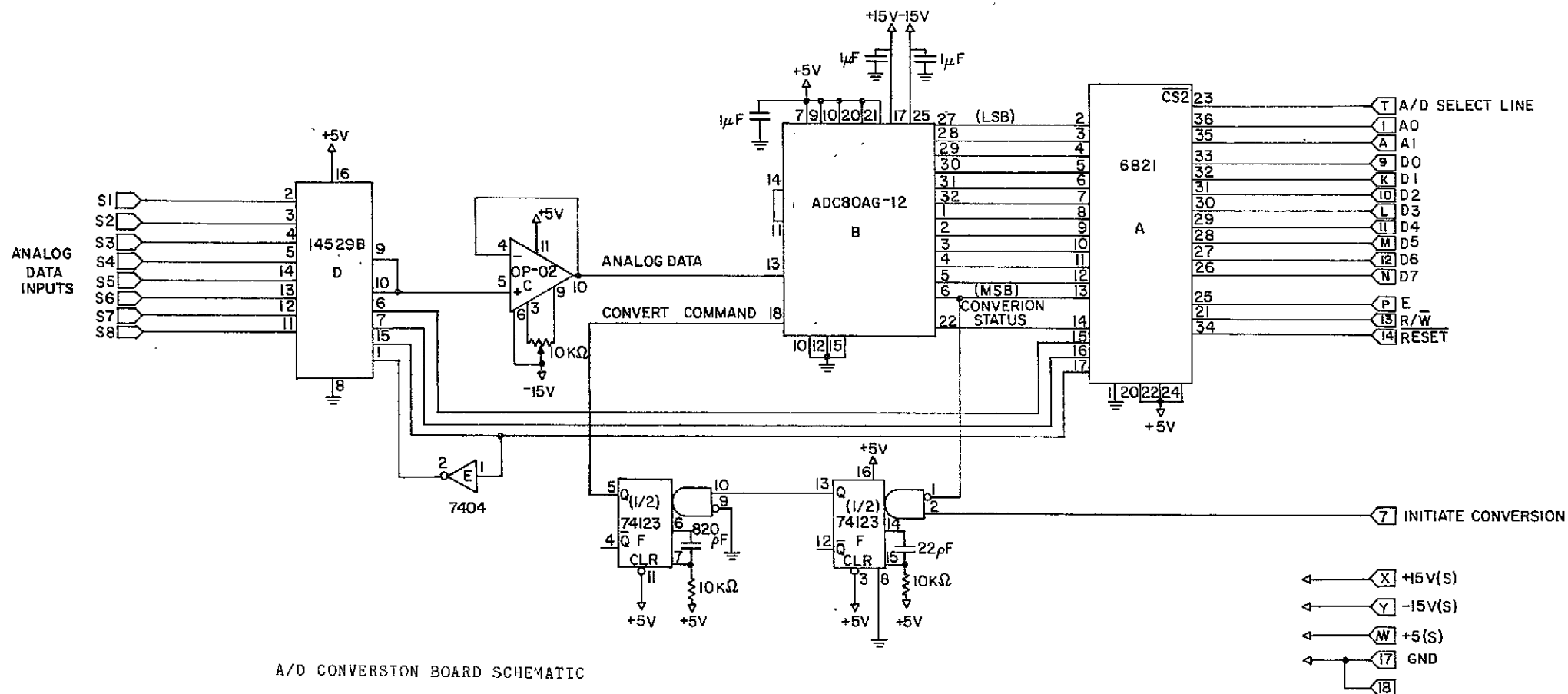
PROCESSOR BOARD
/ BOLDOUT FRAME

2 BOLDOUT FRAME



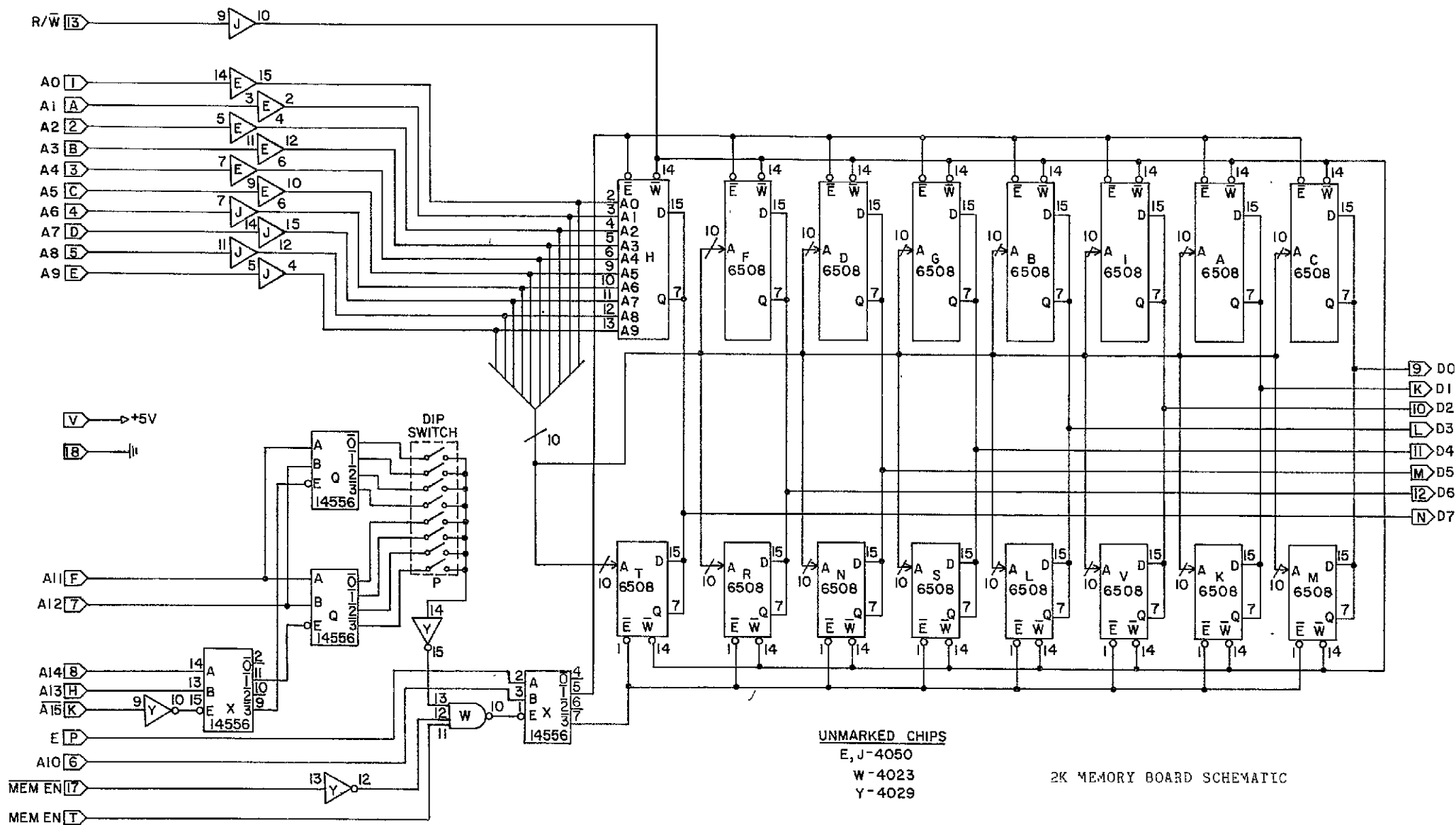
C-4082
D,R,S-4001
H,G-4000
J,K,L-4030
Q-4011

FOLDOUT FRAME

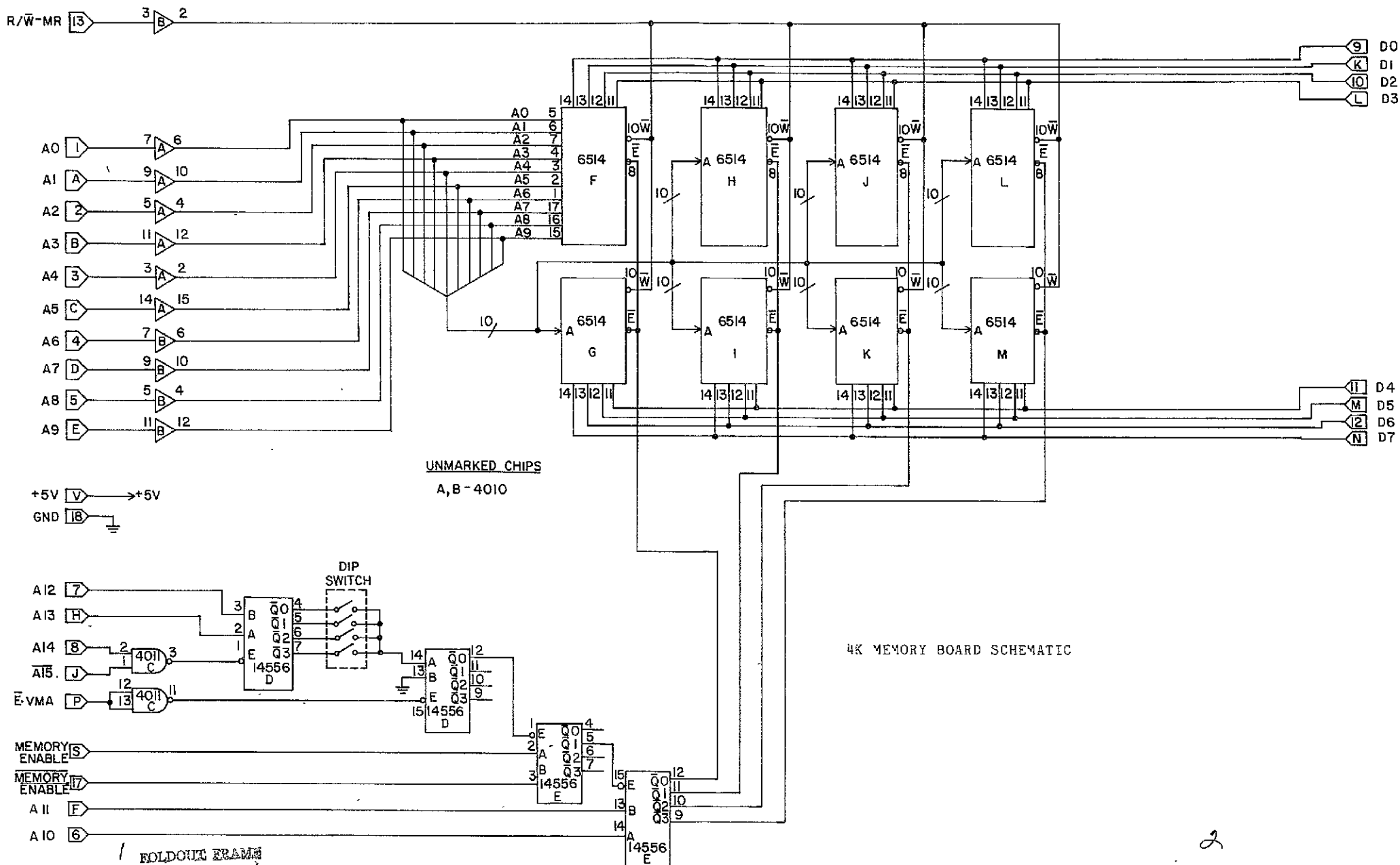


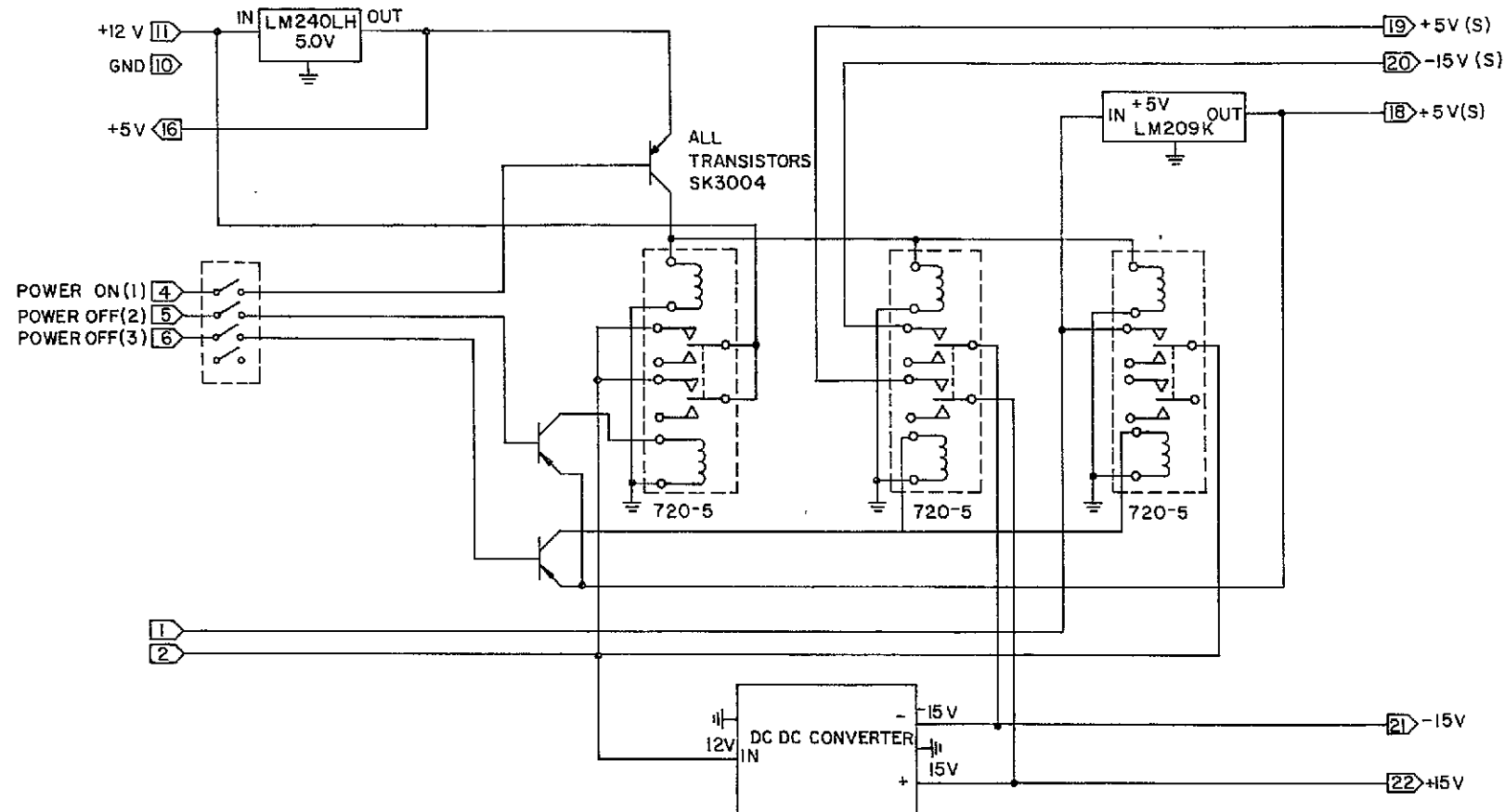
FOLDOUT FRAME

FOLDOUT FRAME



2 WOLFOUT FRAME

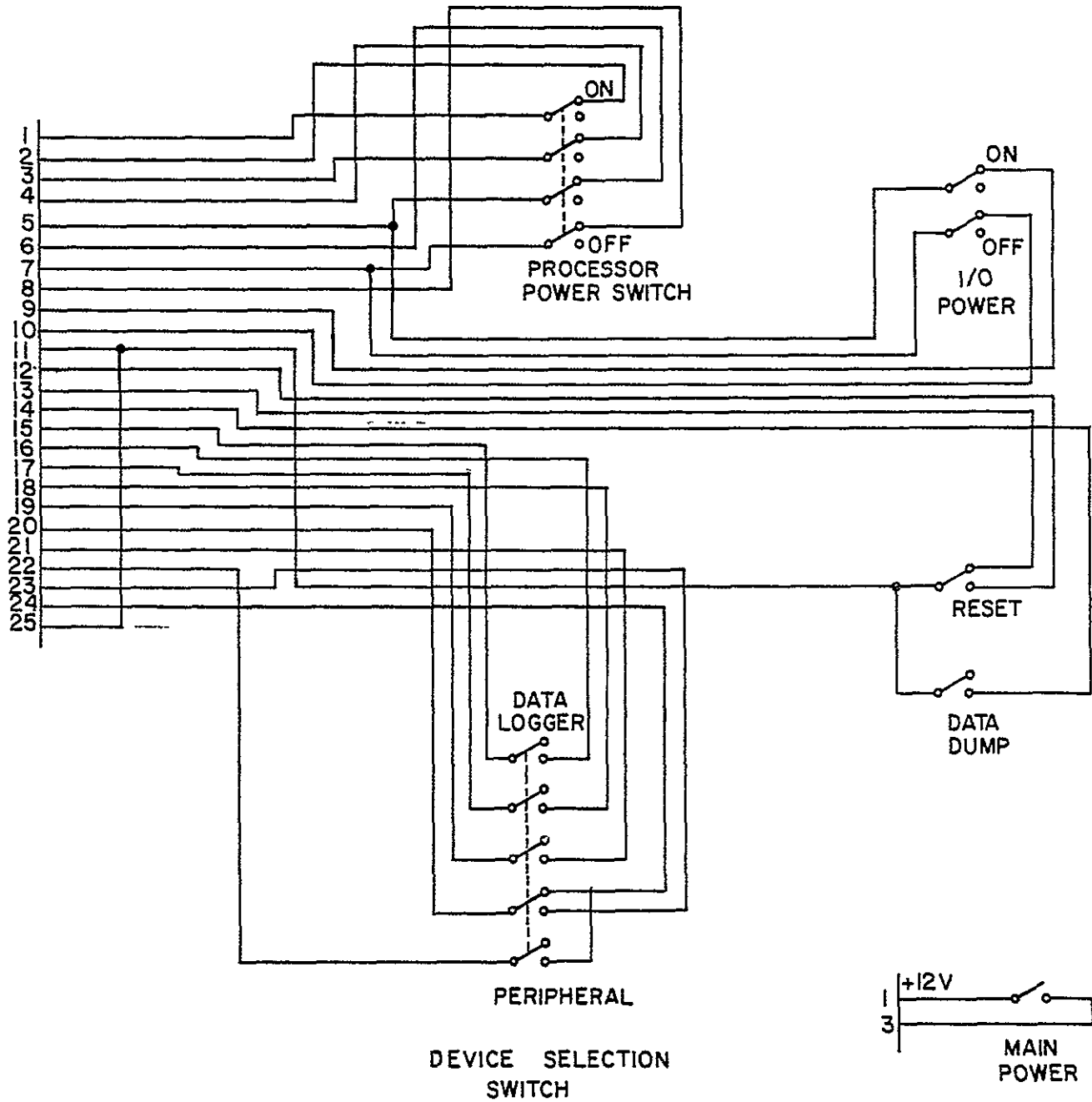




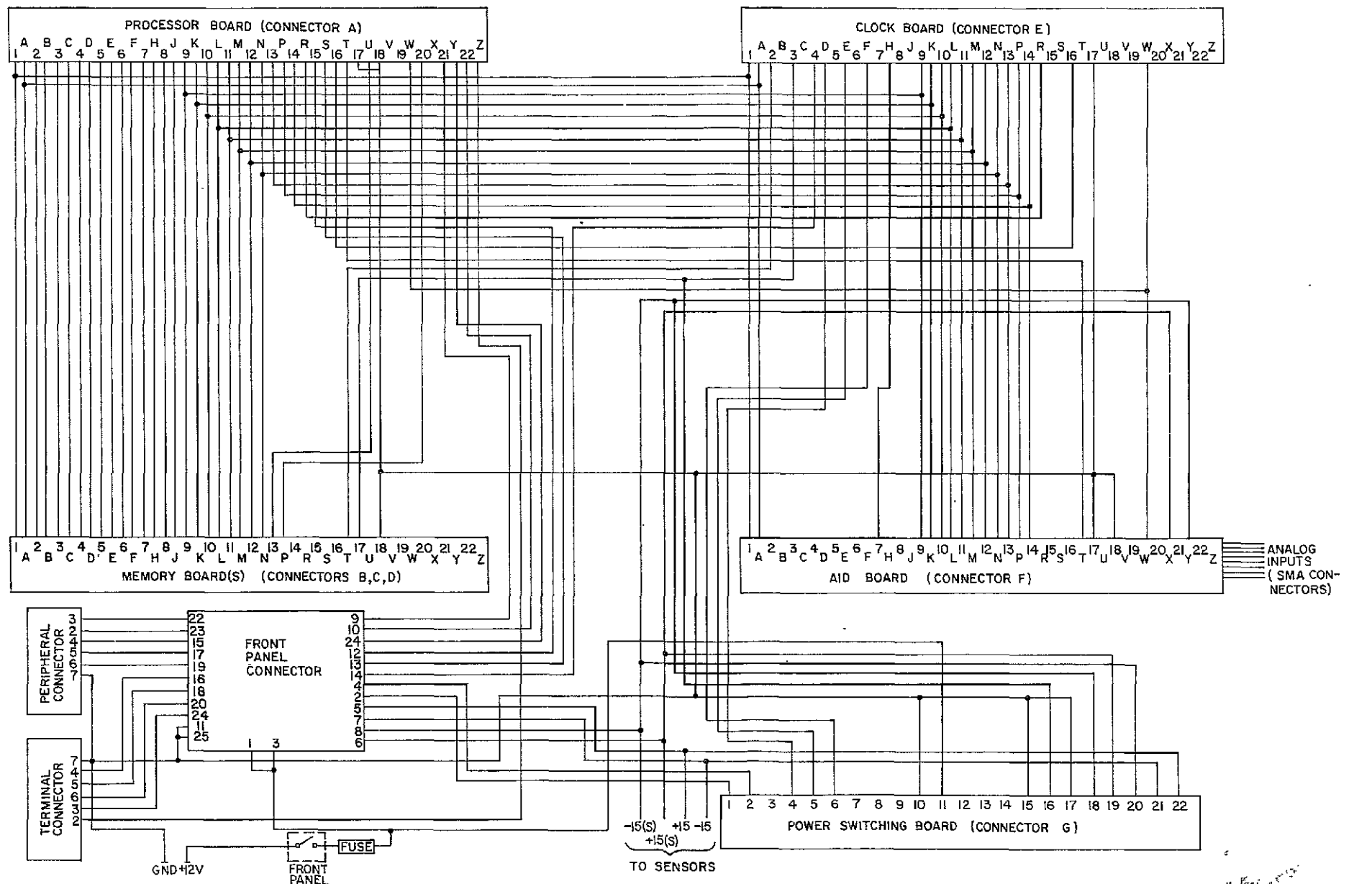
POWER SWITCHING BOARD SCHEMATIC

FOLDOUT FRAME

FOLDOUT FRAME

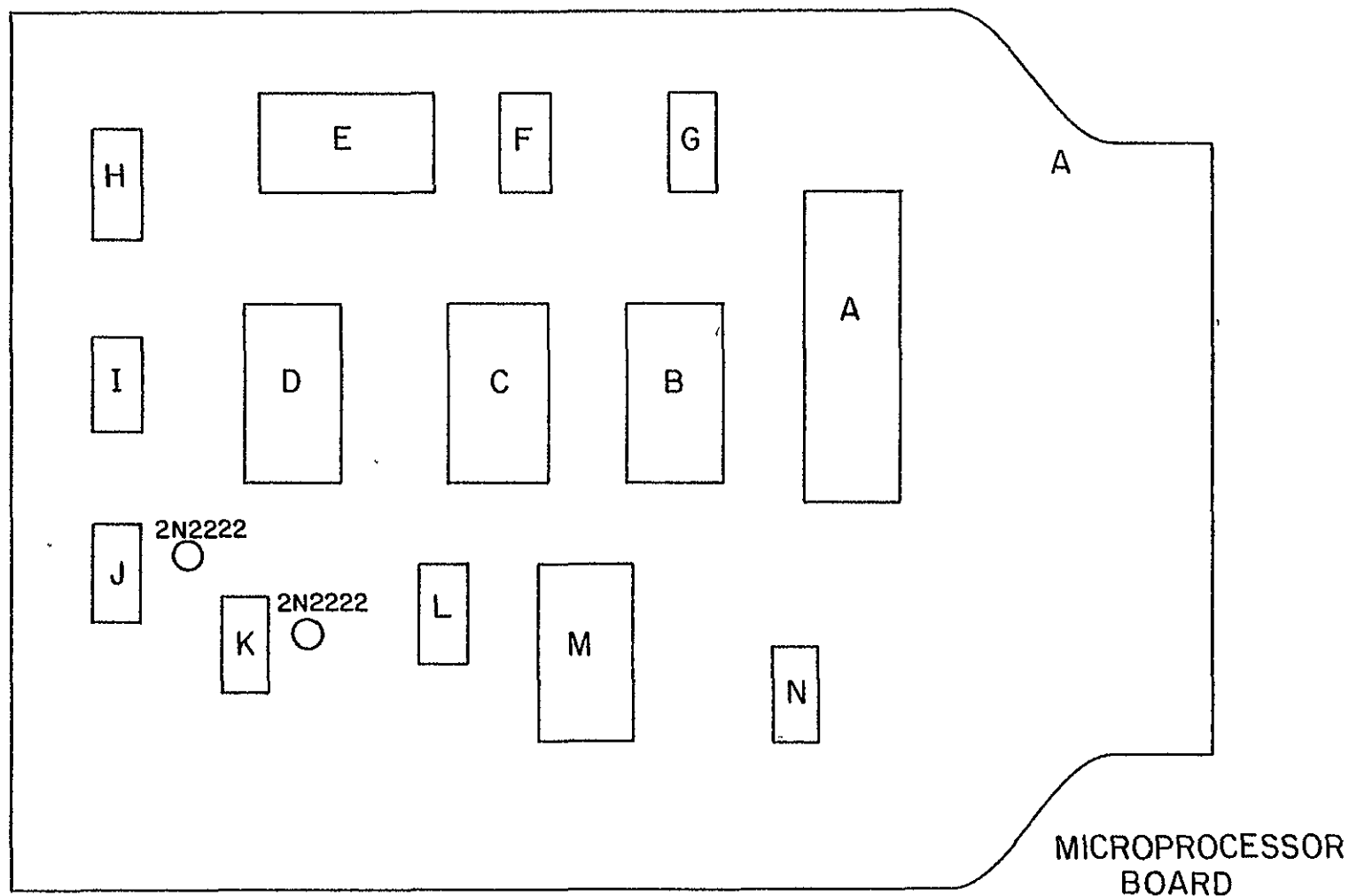


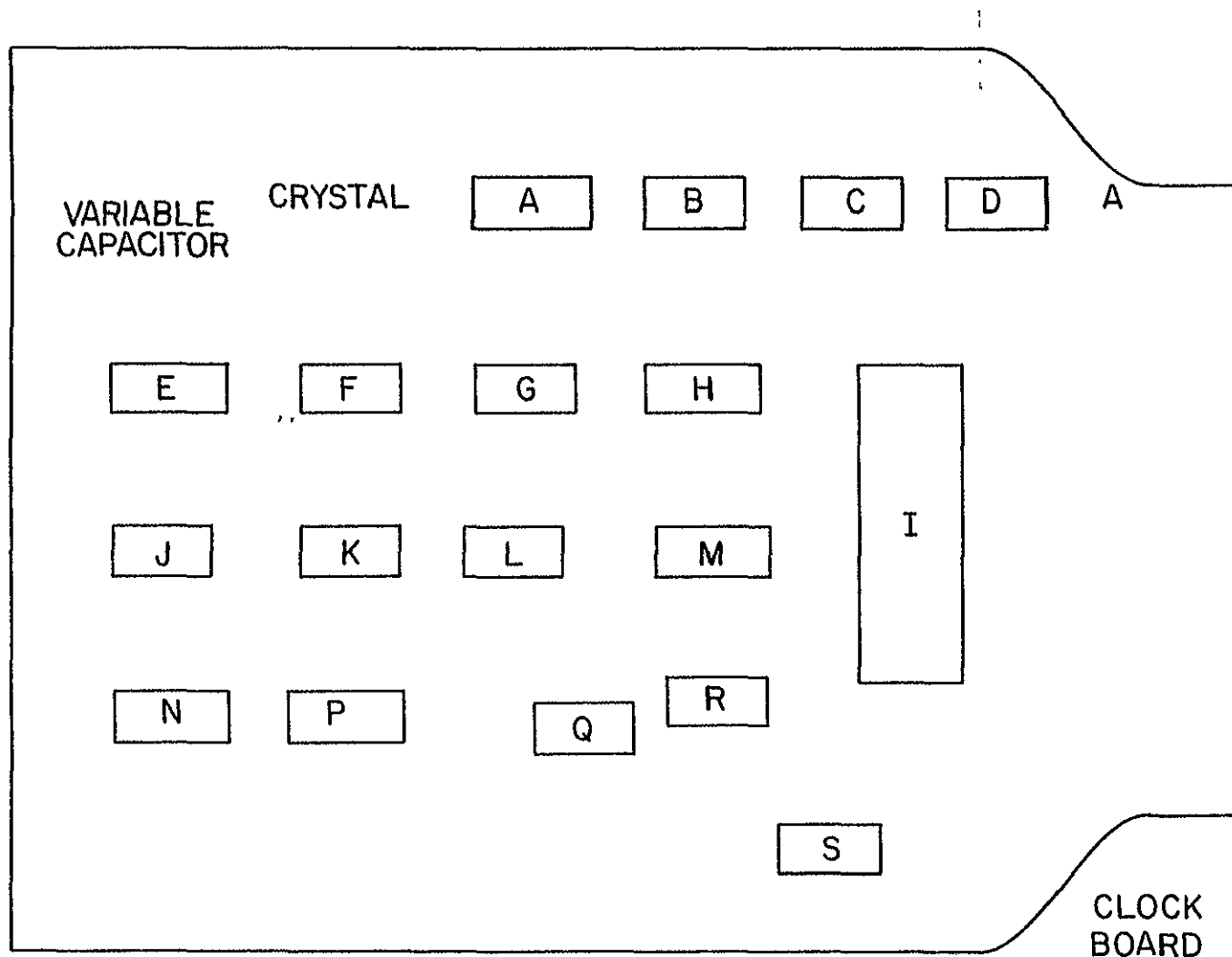
FRONT PANEL WIRING DIAGRAM

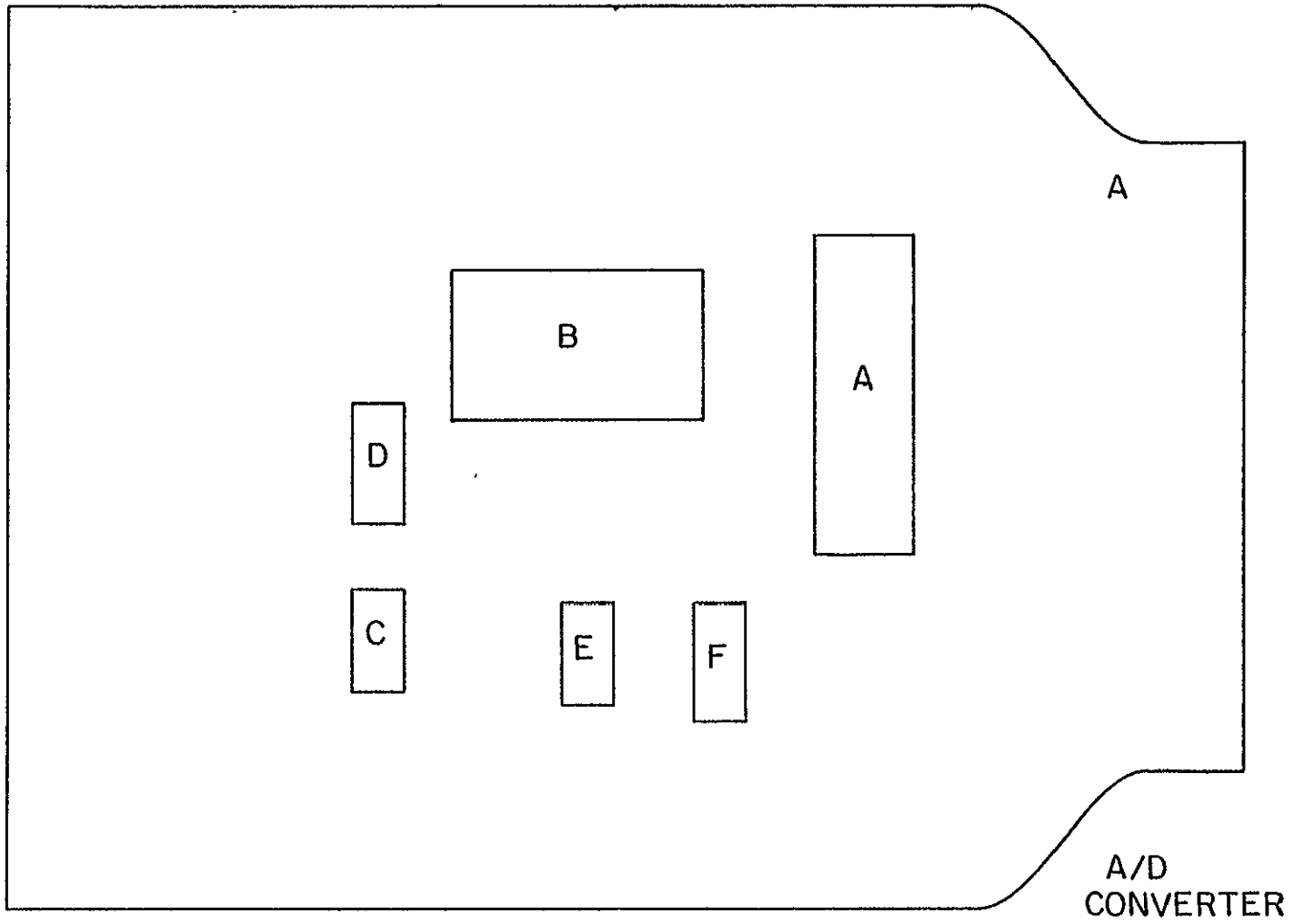


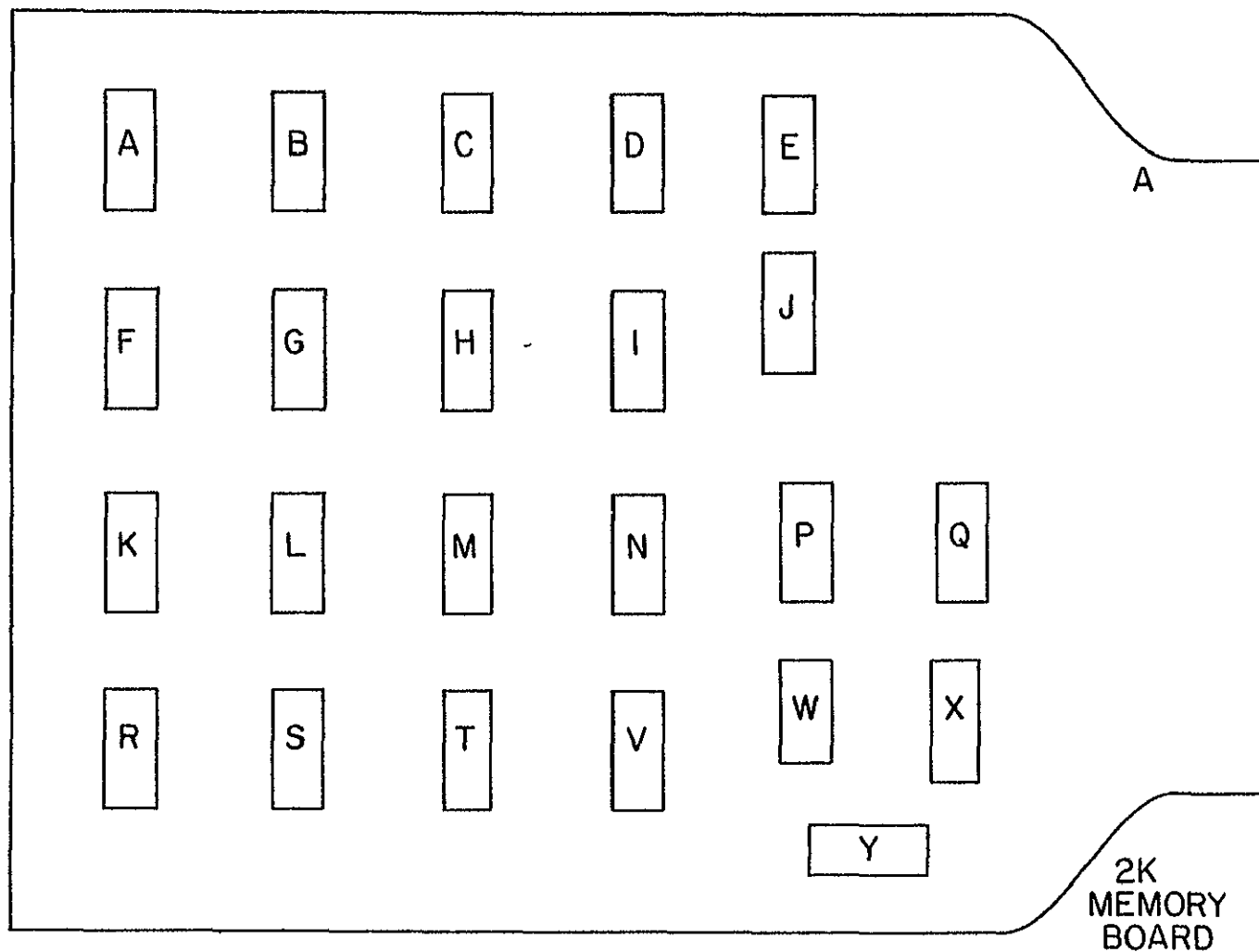
1 BOLDOUT FRAME

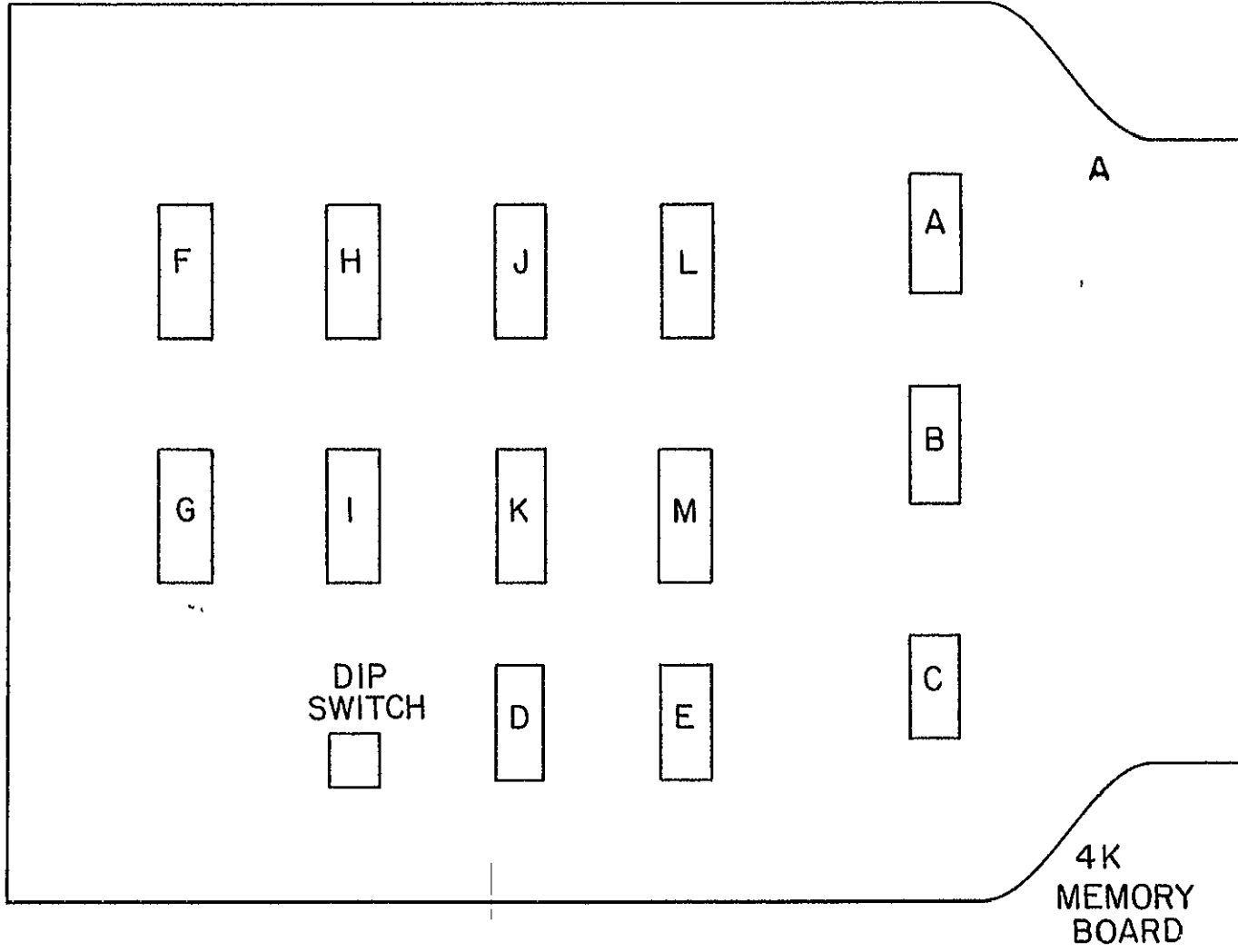
2 BOLDOUT FRAME












```

*****
*
*
*
*      ↑VARIABLES
*      AND
*      ↑CONSTANTS
*
*
*
*****
*
*
*   RECEIV      ↑THE LAST WORD RECEIVED BY THE ↑A↑C↑I↑A IS
*                STORED HERE.
*
*   SIRC        ↑THE THIRD BIT OF THIS WORD IS SET WHEN-
*                EVER AN ↑I↑R↑Q OCCURS.
*
*   MONTH       ↑WHEN A MONTH IS ENTERED FROM THE TERMINAL
*                IT IS STORED HERE.
*
*   DAY         ↑WHEN A DAY IS ENTERED FROM THE TERMINAL
*                IT IS STORED HERE.
*
*   TIMEH       ↑WHEN A TIME IS ENTERED FROM THE TERMINAL
*                THE HIGH ORDER 8 BITS OF THE TIME IN
*                MINUTES IS STORED HERE.
*
*   TIMEL       ↑WHEN A TIME IS ENTERED FROM THE TERMINAL
*                THE LOW ORDER 8 BITS OF THE TIME IN
*                MINUTES IS STORED HERE.
*
*   PMONTH      ↑THE PRESENT MONTH.
*
*   PDAY        ↑THE PRESENT DAY.
*
*   PTIMEH      ↑THE 8 HIGH ORDER BITS OF THE PRESENT TIME.
*
*   PTIMEL      ↑THE 8 LOW ORDER BITS OF THE PRESENT TIME.
*
*   LMIH        ↑THE 8 HIGH ORDER BITS OF THE LAST TIME
*                STUFFED INTO THE CLOCK.
*
*   LMIL        ↑THE 8 LOW ORDER BITS OF THE LAST TIME
*                STUFFED INTO THE CLOCK.
*
*   TEMP1-13    ↑THIRTEEN TEMPORARY LOCATIONS.
*
*   SMONTH      ↑THE MONTH THE DATA LOGGER IS TO START
*                TAKING READINGS.
*
*   SDAY        ↑THE DAY THAT THE DATA LOGGER IS TO START
*                TAKING READINGS.
*
*   STIMEH      ↑THE 8 HIGH ORDER BITS OF THE TIME THAT

```

```

* THE DATA LOGGER IS TO START TAKING READINGS.
*
* STIMEL      ↑THE 8 LOW ORDER BITS OF THE TIME THAT
*              THE DATA LOGGER IS TO START TAKING READINGS.
*
* HFFREQ      ↑THE 8 HIGH ORDER BITS OF THE NUMBER OF
*              MINUTES BETWEEN READINGS.
*
* LFFREQ      ↑THE 8 LOW ORDER BITS OF THE NUMBER OF
*              MINUTES BETWEEN READINGS.
*
* FMONTH      ↑THE MONTH THAT THE DATA LOGGER IS TO
*              STOP TAKING READINGS.
*
* FDAY        ↑THE DAY THAT THE DATA LOGGER IS TO
*              STOP TAKING READINGS.
*
* FTIMEH      ↑THE 8 HIGH ORDER BITS OF THE TIME THAT
*              THE DATA LOGGER IS TO STOP TAKING READINGS.
*
* FTIMEL      ↑THE 8 LOW ORDER BITS OF THE TIME THAT
*              THE DATA LOGGER IS TO STOP TAKING READINGS.
*
* CLCCKA      ↑THE ADDRESS OF PORT ↑A OF THE ↑P↑I↑A ON
*              THE CLOCK BOARD.
*
* CLCCKB      ↑THE ADDRESS OF PORT ↑B OF THE ↑P↑I↑A ON
*              THE CLOCK BOARD.
*
* WTIME       ↑THE WARMUP TIME IN MINUTES.
*
* CONE,       ↑A CODED VERSION OF HOW MANY
* CTWC,       DAYS, OVER 28, THERE ARE IN
* CTFFEE      EACH MONTH.
*
* NUMAVR      ↑A CONSTANT, EQUAL TO EIGHT, THAT
*              INDICATES HOW MANY MEASUREMENTS TO
*              AVERAGE TOGETHER FOR ONE READING.
*
* CONVH       ↑AFTER A MEASUREMENT IS TAKEN THE HIGH
*              ORDER 8 BITS OF THE MEASUREMENT IS STORED
*              IN THIS LOCATION. ↑THIS LOCATION IS ALSO
*              USED AS THE SOURCE FOR THE CONVERSIONS
*              TO THE PROPER UNITS.
*
* CONVL       ↑AFTER A MEASUREMENT IS TAKEN THE LOW ORDER
*              8 BITS OF THE MEASUREMENT ARE STORED IN
*              THIS LOCATION. ↑THIS LOCATION IS ALSO USED
*              AS THE SOURCE FOR THE CONVERSIONS TO THE
*              PROPER UNITS.
*
* MDATAH      ↑THE HIGH ORDER 8 BITS OF THE NEXT LOCATION
*              THAT A READING IS STORED IN IS KEPT HERE.
*
* MDATAL      ↑THE LOW ORDER 8 BITS OF THE NEXT LOCATION
*              THAT A READING IS STORED IN IS KEPT HERE.

```

* STAFM	* THIS CONSTANT IS THE ADDRESS WHERE MEASUREMENTS ARE FIRST STORED IN MEMORY.
* CNTRL	* THIS LOCATION IS A READ ONLY REGISTER IN THE $\uparrow A \uparrow C \uparrow I \uparrow A$ THAT HOLDS STATUS INFORMATION ABOUT THE LAST RECEIVED WORD.
* DATA	* THIS LOCATION IS THE REGISTER IN THE $\uparrow A \uparrow C \uparrow I \uparrow A$ THAT RECEIVES A WORD OR TRANSMITS A WORD.
* EMASK	* THIS CONSTANT IS $\uparrow A \uparrow N \uparrow D \uparrow E \uparrow D$ WITH THE CNTRL WORD TO CHECK FOR A PARITY OR FRAME ERROR.
* TMASK	* THE CONSTANT IS $\uparrow A \uparrow N \uparrow D \uparrow E \uparrow D$ WITH THE CNTRL WORD TO TELL IF THE $\uparrow A \uparrow C \uparrow I \uparrow A$ IS FREE TO TRANSMIT A WORD.
* ADFIAA	* THIS IS THE LOCATION OF PORT $\uparrow A$ OF THE $\uparrow P \uparrow I \uparrow A$ ON THE $\uparrow A / \uparrow D$ BOARD.
* ADFIAB	* THIS IS THE LOCATION OF PORT $\uparrow B$ OF THE $\uparrow P \uparrow I \uparrow A$ ON THE $\uparrow A / \uparrow D$ BOARD.
* STATUS	* THIS WORD KEEPS STATUS INFORMATION THAT TELLS THE $\uparrow N \uparrow M \uparrow I$ ROUTINE WHAT FUNCTION TO PERFORM NEXT.
* TEMFIR, SECCND	* THESE LOCATIONS ARE USED TO TEMPORARILY SAVE THE CONTENTS OF THE $\uparrow I \uparrow R$.
* PORTBT	* THIS WORD TELLS WHICH ADDITIONAL PORTS HAVE BEEN SPECIFIED IN THE INITIALIZATION PROCEDURE. $\uparrow A$ ONE IN A BIT 0-7 SPECIFIES A PORT 1-8 RESPECTIVELY.
* NPCFTS	* THIS LOCATION HOLDS THE TOTAL NUMBER OF MEASUREMENTS THAT ARE TO BE TAKEN DURING EACH SET OF READINGS.
* SENNUM	* THIS LOCATION TELLS SUBROUTINE ADCVAL WHICH PORT TO READ.
* FULLR	* THIS LOCATION TEMPORARILY HOLDS THE NUMBER READINGS THAT ARE TO BE TAKEN.
* INCIR	* THIS LOCATION INDICATES HOW MANY MEMORY LOCATIONS ARE NEEDED TO STORE EACH SET OF MEASUREMENTS.
* WHICH1, WHICH2	* THESE LOCATIONS ARE USED IN DETERMINING WHICH PORT IS TO BE READ NEXT.
* CONVL1, CONVL2	* THESE LOCATIONS ACT AS TEMPORARY LOCATIONS FOR THE VALUE OF CONVL, CONVL.

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*      NUMLOW,  +THESE LOCATIONS INDICATE HOW MANY READINGS
*      NUMHI    THE USER WOULD LIKE DISPLAYED DURING
*              SEQUENTIAL READINGS.
*
*      BACKUP,  +THESE LOCATIONS HOLD AN ADDRESS TO JUMP
*      BACKP    BACK TO IF THE USER PRESSES THE +E+S+C+A+P+E
*              KEY WHEN HE IS INITIALIZING THE DATA LOGGER.
*
*      SAVINGS, +THESE LOCATIONS ARE USED TO TEMPORARILY
*      SAVI     STORE THE CONTENTS OF THE +I+R.
*
*
*
*
*
*
RECEIV  EGU    $12
SIRW    EGU    $13
MONTH   EGU    $4001
DAY      EGU    $4002
TIMEH   EGU    $4003
TIMEL   EGU    $4004
PMONTH  EGU    $4005
PDAY    EGU    $4006
PTIMEH  EGU    $4007
PTIMEL  EGU    $4008
LMINH   EGU    $4009
LMINL   EGU    $400A
TEMP6   EGU    $400B
TEMP7   EGU    $400C
TEMP8   EGU    $400D
TEMP9   EGU    $400E
TEMP10  EGU    $400F
TEMP11  EGU    $4010
SMONTH  EGU    $4011
SDAY    EGU    $4012
STIMEH  EGU    $4013
STIMEL  EGU    $4014
HFREQ   EGU    $4015
LFREQ   EGU    $4016
FMONTH  EGU    $4017
FDAY    EGU    $4018
FTIMEH  EGU    $4019
FTIMEL  EGU    $401A
CLOCKA  EGU    $E000
CLOCKB  EGU    $E002
WTIME   EGU    $4018
TEMP1   EGU    $401C
TEMP2   EGU    $401D
TEMP3   EGU    $401E
TEMP4   EGU    $401F
TEMP5   EGU    $4020
TEMP12  EGU    $4021
TEMP13  EGU    $4022
CONE    EGU    $CE
CTWO    EGU    $EF
CTHREE  EGU    $BB
STKPTR  EGU    $7F

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		NUMAVR	EGU	\$08	
		CONVM	EGU	\$4023	
		CONVL	EGU	\$4024	
		MDATEH	EGU	\$4025	
		MDATAL	EGU	\$4026	
		STARTM	EGU	\$4040	
		CNTRL	EGU	\$D800	
		EMASK	EGU	\$70	
		DATA	EGU	\$D801	
		TMASK	EGU	\$02	
		ADP1AA	EGU	\$E800	
		ADP1AB	EGU	\$E802	
		STATUS	EGU	\$4000	
		SDATE	EGU	\$4027	
		TEMP1R	EGU	\$4028	
		SECOND	EGU	\$4029	
		TEMP13	EGU	\$02	
		TEMP14	EGU	\$03	
		PORTBT	EGU	\$402A	
		NPORTS	EGU	\$402B	
		SENNUM	EGU	\$402C	
		FULLR	EGU	\$402D	
		INC1R	EGU	\$402E	
		WHICH1	EGU	\$402F	
		WHICH2	EGU	\$4030	
		CONVM1	EGU	\$4031	
		CONVL1	EGU	\$4032	
		NUMLOW	EGU	\$4033	
		NUMHI	EGU	\$4034	
		BACKUP	EGU	\$00	
		BACKP	EGU	\$01	
		SAVINGS	EGU	\$14	
		SAVI	EGU	\$15	
		SENSCV	EGU	\$F800	
		MDELAY	EGU	\$F9E5	
		RSHIFT4	EGU	\$F94A	
		DELAY	EGU	\$F9DE	

			CRG	\$F000	
F000	202020	SPACES	FCC	+7	+7
F003	2020				
F005	00		FCB	00	
F006	444154	DATEQ	FCC	+7DATE+/(MM/DD)	+7
F009	453F2b				
F00C	4D4D2F				
F00F	444429				
F012	20				
F013	00		FCB	00	
F014	54494D	TIMEQ	FCC	+7TIME+/(HH+MM)	+7
F017	453F2b				
F01A	48483A				
F01D	4D4D29				
F020	20				
F021	00		FCB	00	
F022	535441	START	FCC	+7START	+7
F025	525420				
F028	00		FCB	00	

F02Y	53544F	STOP	FCC	+7STOP +7
F02L	5020			
F02E	00		FCB	00
F02F	465245	FREQ	FCC	+7FREQUENCY OF +7
F032	515545			
F035	4E4359			
F038	204F46			
F03B	20			
F03C	4D4541		FCC	+7MEASUREMENTS+7
F03F	535552			
F042	454D45			
F045	4E5453			
F048	00		FCB	00
F049	3F2858	QFREQ	FCC	+7+/(XXX) +7
F04C	585829			
F04F	20			
F050	00		FCB	00
F051	574152	WARMUP	FCC	+7WARMUP +7
F054	4D555C			
F057	20			
F058	00		FCB	00
F059	444154	DA	FCC	+7DATE +7
F05C	4520			
F05E	00		FCB	00
F05F	54494D	TI	FCC	+7TIME +7
F062	4520			
F064	00		FCB	00
F065	2D4D49	MIN	FCC	+7 MINS +7
F068	4E5320			
F06B	00		FCB	00
F06C	4E4558	NEXTR	FCC	+7NEXT READING AT +7
F06F	542052			
F072	454144			
F075	494E47			
F078	204154			
F07B	20			
F07C	00		FCB	00
F07D	504153	PR	FCC	+7PAST READINGS +7
F080	542052			
F083	454144			
F086	494E47			
F089	5320			
F08B	00		FCB	00
F08C	54454D	TE	FCC	+7TEMP +7
F08F	5020			
F091	00		FCB	00
F092	54494D	WTIMEG	FCC	+7TIME+/(MM) +7
F095	453F28			
F098	4D4D29			
F09B	20			
F09C	00		FCB	00
F09D	414444	ADDSEN	FCC	+7ADDITIONAL SENSOR+/+7
F0A0	495449			
F0A3	4F4E41			
F0A6	4C2053			
F0A9	454E53			
F0AC	4F523F			

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FOAF 00          FCB 00
FOBV 504F52  PORTB FCC  +7PORT NUMBER+/(1-8)+7
FOB3 54204E
FOB6 554D42
FCB9 45523F
FOBC 283120
FOBF 3629
FOC1 00          FCB 00
FOC2 494E49  PINIT FCC  +7INITIALIZATION+/(+7-
FOC5 544941
FOC8 4C495A
FOCB 415449
FOCE 4F4E3F
FOD1 00          FCB 00
FOD2 285945  YESNO FCC  +7(YES,NO) +7
FOD5 532C4E
FOD8 4F2920
FODB 0D0A7F          FCB  $0D,$0A,$7F,$7F,00
FODE 7F00
FOEV 574152  WCURRS FCC  +7WARM-UP TIME =+7
FOE3 402D55
FOEb 562054
FOE9 494D45
FOEC 203D
FOEE 00          FCB 00
FOEF 545950  RS      FCC  /TYPE +7R+7 FOR A READING, CONTROL/
FOF2 452027
FOF5 522720
FOF8 464F52
FOFB 204120
FOFE 524541
F101 44494E
F104 472C20
F107 434F4E
F10A 54524F
F10D 4C
F10E 202753          FCC  / +7S+7 WHEN DONE/
F111 272057
F114 48454E
F117 20444F
F11A 4E45
F11C 0D0A7F          FCB  $0D,$0A,$7F,$7F,00
F11F 7F00
F121 524541  DONEIN FCC  +7REASSIGN PORTS+/(+7
F124 535349
F127 474E20
F12A 504F52
F12D 54533F
F130 00          FCB 00
*****
**THIS SLROUTINE WAITS FOR THE
**INPUT OF A YES OR A NO.  +IF A
**YES+7 IS TYPED A 0 IS RETURNED
**IN +A.  +IF ANYTHING ELSE IS TYPED
**A 1 IS RETURNED IN +A.
F131 3E      CHYES  WAI          ;WAIT FOR AN +I+R+Q
F132 9612    LCAA   RECEIV      ;+A=LAST CHAR. RECEIVED

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F134 8159      CMPA  ~$+7Y+7
F136 2611      BNE   NOTY      ;BRANCH IF NOT A +7Y+7
F136 3E        WAI           ;WAIT FOR THE SECOND CHAR
F139 9612      LCAA  RECEIV    ;+A=LAST CHAR. RECEIVED
F136 8145      CMPA  ~$+7E+7
F130 2608      BNE   NOTYES    ;BRANCH TO NOTYES IF NOT A +7E+7
F13F 3E        WAI           ;WAIT FOR THE +7S+7
F140 9612      LCAA  RECEIV    ;+A=LAST CHAR RECEIVED
F142 8153      CMPA  ~$+7S+7
F144 2604      BNE   NOTYES    ;BRANCH IF NOT A +7S+7
F146 4F        CLRA
F147 2003      BFA   YESTST    ;CLEAR +A MUST BE A YES
F149 3E        NOTY  WAI       ;BRANCH TO YES TEST
F14A 8601      NOTYES LCAA  ~$501 ;WAIT FOR ANOTHER CHAR
F14C BDF555    YESIST LCSR  LFCR  ;NOT A YES LOAD +A=1
F14F 7F0012    CLR   RECEIV    ;SKIP A LINE
F152 40        TSTA
F153 39        RTS           ;CLEAR RECEIV
                                ;TEST +A
                                ;RETURN TO CALLER

*****
**MULTIPLIES WANTS IN REG +A BY 10,
**LEAVING THE RESULT IN +A.
F154 48        AXTEN ASLA      ;CONTAINS 2+X
F155 16        TAB      ;CONTAINS 2+X
F156 48        ASLA      ;CONTAINS 4+X
F157 48        ASLA      ;CONTAINS 8+X
F158 18        AEA      ;ADDS 8+X AND 2+X
F159 39        RTS      ;+A=10+X

*****
**INPUTS TWO DIGITS AND LEAVES
**THEIR DECIMAL VALUE IN +A.
F15A 3E        TWODIG WAI      ;WAIT FOR THE CHAR
F15B 9612      LCAA  RECEIV    ;MOVE THE CHAR INTO +A
F15C BDF683    LCSR  ASCHEX    ;CONVERT TO HEX EQUIVALENT
F160 BDF154    LCSR  AXTEN     ;MULT +A BY 10
F163 16        TAB      ;STORE FIRST DIGIT IN +B
F164 3E        WAI      ;WAIT FOR NEXT DIGIT
F165 9612      LCAA  RECEIV    ;MOVE THE CHAR INTO +A
F167 BDF683    LCSR  ASCHEX    ;CONVERT TO HEX EQUIVALENT
F16A 18        AEA      ;ADD BOTH DIGITS
F16B 39        RTS      ;VALUE OF INPUT IN +A

*****
**INPUTS +S+T+A+R+T,+F+I+N+I+S+H,AND +P+R+E+S+E+N+T
**DATE AND TIME;WARM-UP TIME AND
**FREQ. OF MEASUREMENTS.
F16C CEF16C    INDAT LCX  ~$INDAT
F16E DF00      STX   BACKUP    ;SET BACKUP FOR *DEL*
F171 BDF600    LCSR  PSP
F174 BDF5F5    LCSR  PSPACE
F177 BDF248    LCSR  GETDAT    ;GETS THE PRESENT DATE AND TIME
F17A BDF280    LCSR  RCLK      ;RESET THE CLOCK TO 0 TIME
F170 CE4005    LCX  ~$PMONTH   ;TELL REMEMBER WHERE TO STORE
F180 BDF278    LCSR  REMEM     ;STORES PRESENT DATE AND TIME
F183 BDF555    LCSR  LFCR      ;PRINTS LF AND CR
F186 CEF16C    INSTAR LCX  ~$INDAT
F189 DF00      STX   BACKUP
F18B CEF022    LCX  ~$START
F18E BDF717    LCSR  PRINT     ;LOAD START
                                ;PRINT +7START+7

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F191 BDF248 JSR GETDAT ;GET STARTING DATE AND TIME
F194 CE4011 LCX ~$SMONTH ;TELL REMEMBER THIS IS STARTING
;INFORMATION
F197 BDF276 JSR REMEM ;STORE THE STARTING TIME AND DATE
F19A BDF555 JSR LFCR ;PRINT CR AND LF
F19D CEF186 INSTOP LCX ~$INSTAR
F1A0 DF00 STX BACKUP ;SET BACKUP FOR *DEL*
F1A2 CEF029 LCX ~$STOP ;LOAD STOP
F1A5 BDF717 JSR PRINT ;PRINT ^7STOP^7
F1A6 BDF600 JSR PSP
F1AB BDF248 JSR GETDAT ;GET STOPPING DATE AND TIME
F1AE CE4017 LCX ~$FMONTH ;TELL REMEMBER TO STORE THIS AS
;THE STOPPING INFORMATION
;STORE STOPPING DATE AND TIME
;PRINT LF AND CR
F1B1 BDF276 JSR REMEM
F1B4 BDF555 JSR LFCR
F1B7 CEF19D INWARM LCX ~$INSTOP
F1BA DF00 STX BACKUP ;SET BACKUP FOR *DEL*
F1BC CEF051 LCX ~$WARMUP ;LOAD WARMUP
F1BF BDF717 JSR PRINT ;PRINT ^7WARMUP^7
F1C2 CEF092 LCX ~$WTIMEQ ;LOAD TIME^/
F1C5 BDF717 JSR PRINT ;PRINT ^7TIME^/7
F1CB BDF15A JSR TWO DIG ;GET TWO DIGITS
F1CD B74016 STAA WTIME ;STORE THE WARMUP TIME
F1CE BDF555 JSR LFCR ;PRINT LF AND CR
F1D1 CEF187 INREQ LCX ~$INWARM
F1D4 DF00 STX BACKUP ;SET BACKUP FOR *DEL*
F1D6 CEF02F LCX ~$FREQ ;LOAD FREQUENCY OF...
F1D9 BDF717 JSR PRINT ;PRINT ^7FREQ. OF ....^7
F1DC CEF049 LCX ~$QFREQ ;PRINT ^/(XXX)
F1DF BDF717 JSR PRINT ;PRINT IT
F1E2 BDF2AD JSR THRDIG ;GET THREE DIGITS
F1E5 B74016 STAA LFREQ ;STORE LOW HALF OF FREQ.
F1E8 F74016 STAB HFREQ ;STORE HIGH HALF OF FREQ.
F1EB CEF1D1 INPORT LCX ~$INFREQ
F1EE DF00 STX BACKUP ;SET UP BACKUP FOR *DEL*
F1F0 B603 LCAA ~$S03
F1F2 B7402B STAA NPORTS ;^$ OF READINGS NEED TO TAKE
F1F5 7F402A CLR PORTBT ;CLEARS WHICH ADDITIONAL PORTS
F1F8 BDF555 MOREP JSR LFCR
F1FB CEF09D LCX ~$ADDSN
F1FE BDF717 JSR PRINT ;PRINT ^7ADDITIONAL SENSOR..^7
F201 CEF0D2 LCX ~$YESNO
F204 BDF717 JSR PRINT ;PRINT ^7(YES+NO)^/7
F207 BDF131 JSR CHYES ;WAIT FOR RESPONSE
F20A 2622 BNE INDONE ;IF NOT ZERO THEN DONE
F20C CEF1EB LCX ~$INPORT
F20F DF00 STX BACKUP ;SETUP BACKUP FOR *DEL*
F211 CEF0B0 LCX ~$PORTB
F214 BDF717 JSR PRINT ;PRINT ^7PORT NUMBER.....^7
F217 3E WAI ;WAIT FOR RESPONSE
F218 9612 LCAA RECEIV
F21A BDF663 JSR ASCHEX ;CHANGE TO HEX
F21D 5F CLRB ;CLEAR ^B
F21E 0D SEC ;SET CARRY = 1
F21F 59 ROLL ;ROTATE LEFT ^B
F220 4A CECA ;DECREMENT ^A
F221 26FC BNE ROLL ;LOOP UNTIL ^A=0

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F223 FA402A CFAB PORTBT ;OR +B WITH PORTBT
F226 F7402A STAB PORTBT ;STORE THE RESULT
F229 7C402B INC NPORTS ;INCREMENT THE NUMBER OF READINGS
F22C 20CA BRA MOREP ;BRANCH BACK FOR MORE
F22E BDF555 INDONE JSR LFCR ;SKIP A LINE
F231 CEF121 LCX ~SDONEIN
F234 BDF717 JSR PRINT ;PRINT +7REASSIGN PORTS,...+7
F237 CEF002 LCX ~$YESNO
F23A BDF717 JSR PRINT
F23D BDF131 JSR CHYES ;CHECK RESPONSE
F240 27A9 BEQ INPORT ;IF YES BRANCH BACK TO INPORT
F242 CE0000 LCX ~$0000
F245 DF00 STX BACKUP
F247 39 RTS ;CLEAR BACKUP
;RETURN TO CALLER

*****
**GETS THE DATE AND TIME.
F248 CEF006 GETDAT LCX ~$DATEQ ;LOAD DATE+/
F24B BDF717 JSR PRINT ;PRINT +7DATE+/+7
F24E BDF38C JSR DATE ;INPUTS THE DATE
F251 CEF000 LCX ~$SPACES ;LOAD SPACES
F254 BDF717 JSR PRINT ;PRINT FIVE SPACES
F257 CEF014 LCX ~$TIMEQ ;LOAD TIME+/
F25A BDF717 JSR PRINT ;PRINT +7TIME+/+7
F25D BDF39E JSR TIME ;INPUTS THE TIME
F260 39 RTS

*****
**COMPARES START DATE TO PRESENT
**DATE, RETURNS A 0 IN +A IF NOT
**THE SAME, 1 IF SAME.
F261 864011 PSCOMP LCAA SMONTH ;LOAD +A WITH STARTING MONTH
F264 814005 GMPA PMONTH ;COMPARE +A TO THE PRESENT MONTH
F267 2702 BEQ MSAME ;IF EQUAL BRANCH TO MSAME
F269 4F CLRA ;IF NOT EQUAL CLEAR +A
F26A 39 RTS ;AND RETURN VALUE +A=0
F26B 864012 MSAME LCAA SDAY ;LOAD +A WITH STARTING DAY
F26E 814006 CMPA PDAY ;COMPARE +A TO THE PRESENT DAY
F271 2702 BEQ DSAME ;BRANCH IF THE SAME TO DSAME
F273 4F CLRA ;IF NOT THE SAME CLEAR +A
F274 39 RTS ;AND RETURN VALUE +A=0
F275 8601 DSAME LCAA ~$01 ;THE DATES ARE THE SAME
F277 39 RTS ;RETURN VALUE +A=1

*****
**STORES THE NUMBERS IN MONTH, DAY,
**TIMEH, AND TIMEL INTO 4 CONTIGUOUS
**LOCATIONS. +THE ADDRESS OF THE FIRST
**LOCATION MUST BE IN THE INDEX REG.
**BEFORE THE SUBROUTINE IS CALLED.
F278 864001 REMEM LCAA MONTH ;LOAD MONTH INTO +A
F27B A700 STAA 00,X ;STORE INTO FIRST LOCATION
F27D 864002 LCAA DAY ;LOAD DAY INTO +A
F280 A701 STAA 01,X ;STORE DAY INTO SECOND LOCATION
F282 864003 LCAA TIMEH ;LOAD TIMEH INTO +A
F285 A702 STAA 02,X ;STORE TIMEH INTO THIRD LOCATION
F287 864004 LCAA TIMEL ;LOAD TIMEL INTO +A
F28A A703 STAA 03,X ;STORE TIMEL INTO FOURTH LOCATION
F28C 39 RTS ;RETURN
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**THIS SUBROUTINE RESETS THE CLOCK
**TO TIME C.
F28D 36 RCLK FSHA ;SAVE +A
F28E 37 FSHB ;SAVE +B
F28F B6E000 LCAA CLOCKA ;LOAD CLOCKA INTO +A
F292 16 TAB ;COPY +A INTO +B
F293 8A01 CRAA ~$301 ;+O+R +A WITH 01 (HEX)
F295 847F ANDA ~$57F ;+A+N+D +A WITH 7F (HEX)
F297 B7E000 STAA CLOCKA ;STORE +A IN CLOCKA
F29A F7E000 STAB CLOCKA ;RETURN CLOCKA TO ORIGINAL VALUE
F29D 33 FLB ;RESTORE +B
F29E 32 FLA ;RESTORE +A
F29F 39 RTS

*****
**O+U+T+A TRANSMITS TO THE OUTPUT DEVICE
** (TERMINAL OR TELETYPE) THE CONTENTS
** OF REGISTER +A.
F2A0 37 OUTA FSHB ;SAVE +B
F2A1 F6D800 BACKO LCAB CNTRL ;LOAD +B WITH +A+C+I+A CONTROL WORD
F2A4 C402 ANDB ~$TMASK ;CHECK BIT 1
F2A6 27F9 BEQ BACKO ;WAIT IF BIT IS 0
F2A8 B7D801 FORWARD STAA DATA ;WRITE OUT +A
F2AB 33 FLB ;RESTORE +B
F2AC 39 RTS

*****
**INPUTS THREE DIGITS INTO [+A+B].
F2AD 3E THRDIG WAI ;WAIT FOR FIRST DIGIT
F2AE 9612 LCAA RECEIV ;LOAD DIGIT INTO +A
F2B0 BDF683 ~SR ASCHEX ;CONVERT TO HEX
F2B3 BDF154 ~SR AXTEN ;MULTIPLY BY TEN
F2B6 B7401C STAA TEMP1 ;STORE IN TEMP1
F2B9 5F CLRB ;CLEAR +B
F2BA CE0009 LCX ~$9 ;PREPARE TO MULT. TEN TIMES
F2BD B8401C TENTIM ACDA TEMP1 ;ADD TEMP1 TO SELF NINE TIMES
F2C0 C900 ACCB ~$00 ;A 2 REGISTER ADD,ADD CARRY
F2C2 09 CEX ;DECREMENT LOOP COUNTER
F2C3 26F8 BNE TENTIM ;BRANCH UNTIL ADDED 9 TIMES
F2C5 B7401C STAA TEMP1 ;STORE VALUE OF FIRST DIGIT
F2C8 F7401D STAB TEMP2 ;STORE LOWER HALF OF DIGIT
F2CB BDF15A ~SR TWODIG ;GET THE OTHER TWO DIGITS
F2CE 5F CLRB ;CLEAR +B
F2CF B8401C ACDA TEMP1 ;ADD LOWER HALF OF FIRST DIGIT
F2D2 F4401D ACCB TEMP2 ;ADD UPPER HALF OF FIRST DIGIT
F2D5 39 RTS ;DECIMAL VALUE IN [+A+B].

*****
**MIDNIGHT MINUS PRESENT TIME PUT IN [+A+B].
F2D6 8605 MIDNIG LCAA ~$505 ;24*100 = 05+00 IN HEX
F2D8 C6A0 LCAB ~$5A0 ;LOAD THIS INTO [+A+B]
F2DA F04008 ~LBB PTIMEL ;SUBTRACT LOWER HALF OF PRESENT TIME
F2DU 824007 ~SECA PTIMEH ;SUBTRACT UPPER HALF OF PRESENT TIME
F2E0 39 RTS

*****
**UPDATES THE PRESENT TIME, DAY, AND MONTH BASED
** ON THE LAST TIME IN THE CLOCK (LTIMEH,LTIME).
F2E1 864008 UPTIME LCAA PTIMEL
F2E4 F64007 LCAB PTIMEH

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F2E7 B6400A ACDA LMINL ;ADD LAST TIME IN CLOCK
F2EA F94009 ACCB LMINH ;BOTH LOW AND HIGH ORDER
F2ED B74008 STAA PTIMEL
F2F0 F74007 STAB PTIMEH
F2F3 4C INCA
F2F4 C105 QMPB ~$S05
F2F6 2703 BEQ LOWCHK ;IS IT EQUAL TO HIGH ORDER OF ONE DAY
F2F8 2E06 BGT FIXDAY ;IF YES GO CHECK LOW HALF
F2FA 39 RTS ;IF GREATER BRANCH TO FIX DAY
F2FB 81A0 LOWCHK CMPA ~$S05 ;RETURN TO CALLER
F2FD 2201 BFI FIXDAY ;IS LOW HALF = LOW HALF ON ONE DAY
F2FF 39 RTS ;IF LARGER GO TO FIXDAY
F300 80A1 FIXDAY SLBA ~$S01 ;RETURN TO CALLER
F302 C205 SECB ~$S05 ;SUBTRACT 24 HOURS (IN MINS.)
F304 B74008 STAA PTIMEL
F307 F74007 STAB PTIMEH
F30A B64008 LCAA PDAY ;STORE BACK INTO PTIMEH+L
F30D 4C INCA
F30E B74008 STAA PDAY ;INCREMENT THE DAY
F311 811C QMPA ~$S1C ;STORE THE DAY
F313 2C01 BGE MONCHK ;COMPARE TO 28 DAYS
F315 39 RTS ;IF GREATER THAN GOTO MONCHK
F316 C6CE MONCHK LCAB ~$CONE ;RETURN TO CALLER
F318 F74010 STAB TEMP2
F31B C6EF LCAB ~$CTWO
F31D F7401E STAB TEMP3
F320 C6BB LCAB ~$CTHREE
F322 F7401F STAB TEMP4
F325 F64005 LCAB PMONTH
F328 5A CSHIFT DECB ;+B=MONTH
F329 2D08 BLT MONDAY ;DECREMENT THE MONTH
F32B BDF34B JSR TEMPSH ;IF ZERO DONE SHIFTING
F32E BDF34B JSR TEMPSH
F331 20F5 BFA CSHIFT ;SHIFT CONE,CTWO,CTHREE TWICE
F333 F6401C MONDAY LCAB TEMP1 ;LOOP BACK
F336 C403 ANDB ~$S03
;GET OUT THE NUMBER OF DAYS
;OVER 28 THAT ARE IN THIS MONTH
;ADD 28 TO GET DAYS IN MONTH
;COMPARE DAY TO DAYS IN MONTH
;IF GREATER THAN GOTO MONFIX
;RETURN TO CALLER
F336 C61C ACDB ~$S28
F33A 11 CBA
F33B 2E01 BGT MONFIX
F33D 39 RTS
F33E 8601 MONFIX LCAA ~$S1
F340 B74008 STAA PDAY ;RESET DAY TO 1
F343 B64005 LCAA PMONTH
F346 4C INCA
F347 B74008 STAA PMONTH
F34A 39 RTS ;INCREMENT THE MONTH
;RETURN TO CALLER
*****
**LEFT SHIFT TEMP1,2,3,4.
F34B 78401F TEMPSH ASL TEMP4
F34E 79401E FCL TEMP3
F351 79401D FCL TEMP2
F354 79401C FCL TEMP1
F357 39 RTS
*****
**TURNS ALL POWER OFF.
F358 B6E000 POWOFF LCAA CLOCA

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F358 8A20      CFAA  ~$S20
F35U 67E000    STAA  CLOCKA
F360 8A18      CFAA  ~$S18
F362 840F      ANDA  ~$SDF
F364 87E000    STAA  CLOCKA
F367 39        RTS

*****
**TURNS CFF +-15(S),+5(S).
F368 86E000    WRMP0W LCAL  CLOCKA
F368 8A10      CFAA  ~$S10
F36D 87E000    STAA  CLOCKA
F370 8A28      CFAA  ~$S28
F372 84EF      ANDA  ~$SEF
F374 87E000    STAA  CLOCKA
F377 39        RTS

*****
**THIS SUBROUTINE MOVES WHAT IS IN
**PMONTH-TIME INTO MONTH-TIME.
F378 CE4001    UNSTOP LCX  ~$MONTH      ;I+R=LOCATION OF MONTH
F378 A604      UNROCK LCAL  04,X        ;A=I+R+4
F37D A700      STAA  00,X              ;STORE IT IN I+R
F37F 08        INX                    ;INCREMENT I+R
F380 8C4005    CPX  ~$PMONTH          ;IF EQUAL WE ARE DONE
F383 26F6      BNE   UNROCK           ;LOOP BACK IF NOT
F385 39        RTS

*****
**[(A+B)/(A+B)]=(WARM-UP TIME)
F386 F0401B    SUBWTI SLBB  WTIME
F389 8200      SECA  ~$S00
F38B 39        RTS

*****
**INPUTS DATE OF THE FORM +6+6/+6+6.
**STORES INPUT IN MONTH AND DAY.
F38C 36        DATE  PSMA
F38D 37        PSMB
F38E 8DF15A    JSR   TWODIG           ;INPUT TWO DIGITS
F391 874001    STAA  MONTH           ;STORE THEM IN MONTH
F394 3E        WAI                    ;WAIT FOR NEXT CHAR
F395 8DF15A    JSR   TWODIG           ;INPUT TWO DIGITS
F398 874002    STAA  DAY              ;STORE IN DAY
F39B 33        PLLB
F39C 32        PLLA
F39D 39        RTS

*****
**INPUTS TIME OF THE FORM +6+6+;+6+6.
**STORES INPUT IN TIMEH AND TIME.
F39E 36        TIME  PSMA
F39F 37        PSMB
F3A0 8DF15A    JSR   TWODIG           ;INPUT TWO DIGITS
F3A3 5F        CLRB                   ;CLEAR +B
F3A4 48        ASLA                   ;#2
F3A5 48        ASLA                   ;#4
F3A6 874004    STAA  TIMEH            ;STORE TEMPORARILY HERE
F3A9 48        ASLA                   ;#8
F3AA 48        ASLA
F3AB 59        RCLB                   ;#16
F3AC 48        ASLA

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F3A0 59          FCLB          ;#32
F3AE 48          ASLA
F3AF 59          FCLB          ;#64
F3B0 B04004      SLBA    TIMEL  ;[+B+A]=60*(2 DIGITS)
F3B3 C200        SECB    ~$500
F3B5 B74004      STAA    TIMEL
F3B6 F74003      STAB    TIMEH   ;STORE THE TIME IN TIMEH+L
F3Bb 3E          WAI           ;WAIT FOR ANOTHER INPUT
F3BC BDF15A      JSR      TWODIG  ;GET TO MINUTE DIGITS OF TIME
F3BF 5F          CLR8          ;CLEAR +B
F3C0 B84004      ACDA    TIMEL
F3C3 F94003      ACCB    TIMEH   ;ADD FIRST TWO DIGITS IN MINUTES
F3C6 B74004      STAA    TIMEL
F3C9 F74003      STAB    TIMEH   ;STORE THE TIME
F3CC 33          PLLB
F3CD 32          PLLA
F3CE 39          RTS

*****
**COMPARES [PDAY+WARMUP TIME] TO
**SDAY RETURNS 0 IN +A IF NOT THE
**SAME,1 IF THE SAME.
F3CF BDF592      PSCMPW JSR      TIME    ;SAVE PMONTH-LMINL
F3D2 B6401B      LCAA    WTIME    ;+A=WARMUP TIME
F3D5 B74004      STAA    LMINL
F3D6 7F4009      CLR      LMINH   ;SET LAST CLOCK TIME TO WARMUP TIME
F3Db BDF2E1      JSR      UPTIME   ;ADD THE WARMUP TIME TO THE PRESENT TIME
F3DE BDF261      JSR      PSCOMP   ;NOW COMPARE PRESENT TO STARTING TIME
F3E1 BDF5B3      JSR      RTIME    ;RESTORE PMONTH-LMINL
F3E4 4D          TSTA          ;TEST +A
F3E5 39          RTS          ;RETURN TO CALLER

*****
**THE CONTENTS OF +A ARE CONVERTED
**TO TWO +B+C+D DIGITS.
F3E6 B7401D      BCU      STAA    TEMP2
F3E9 7F401C      CLR      TEMP1
F3EC FE401C      LDX      TEMP1   ;+I+R=+A
F3EF 4F          CLRA          ;CLEAR +A
F3F0 8C0000      BCULOP   CFX      ~$500   ;IF ZERO THEN CONVERSION DONE
F3F3 2706        BEQ      BCDDON   ;IF EQUAL BRANCH TO BCDDON
F3F5 09          DEX          ;DECREMENT +I+R
F3F6 8B01        ACDA      ~$501   ;INCREMENT +A
F3Fb 19          CAA          ;BCD ADJUST +A
F3F9 20F5        BFA      BCULOP   ;BRANCH BACK FOR MORE
F3FB 39          BCDDON   RTS          ;RETURN TO CALLER

*****
**PRINTS THE USER+7S OPTIONS.
F3FC BDF7C0      DISPCW JSR      ALERT   ;PRINTS WARMUP POWER (+0+N,+0+F+E).
F3FF CEF406      LDX      ~$DSPEC
F402 BDF717      JSR      PRINT       ;PRINT THE OPTIONS
F405 39          RTS

*****
F406 412920      DSPEC   FCC      +7A) INITIALIZING+7
F409 494E49
F40C 544941
F40F 4C495A
F412 494E47
F415 205041      FCC      +7 PARAMETERS +7

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F416 52414C
F41B 455445
F41E 525320
F421 0D0A7F      FCB      $0D,$0A,$7F,$7F
F424 7F
F425 422920      FCC      +7B) CURRENT SENSOR +7
F428 43555C
F42B 52454E
F42E 542053
F431 454E53
F434 4F5220
F437 524541      FCC      +7READINGS+7
F43A 44494E
F43D 4753
F43F 0D0A7F      FCB      $0D,$0A,$7F,$7F
F442 7F
F443 432920      FCC      +7C) SEQUENTIAL READINGS+7
F446 534551
F449 55454E
F44C 544941
F44F 4C2052
F452 454144
F455 494E47
F458 53
F459 0D0A7F      FCB      $0D,$0A,$7F,$7F
F45C 7F
F45D 442920      FCC      +7D) INITIALIZATION+7
F460 494E49
F463 544941
F466 4C495A
F469 415449
F46C 4F4E
F46E 0D0A7F      FCB      $0D,$0A,$7F,$7F
F471 7F
F472 452920      FCC      +7E) DUMP+7
F475 445540
F478 50
F479 0D0A7F      FCB      $0D,$0A,$7F,$7F,$20,$20,$20,$20,$20
F47C 7F2020
F47F 202020
F482 28434F      FCC      /(CONTROL +7S+7 STOPS PRINTOUT)/
F485 4E5452
F488 4F4C20
F48B 275327
F48E 205354
F491 4F5053
F494 205052
F497 494E54
F49A 4F5554
F49D 29
F49E 0D0A7F      FCB      $0D,$0A,$7F,$7F,$00
F4A1 7F00

*****
**THIS SUBROUTINE DISABLES THE MEMORY.
DISMEN FSHA
F4A3 36
F4A4 863F      LCAA      +$53F
F4A6 B4E000      ANDA      CLOCA

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F4A9 B7E00v STAA CLOCKA
F4AC 32 PULA
F4AD 39 RTS

*****
**SHIFT LEFT [A+B].
F4AE 58 SHFROL ASLB
F4AF 49 RCLA
F4B0 39 RTS

*****
**COMPARES PDATE TO FDATE AND
**RETURNS A ZERO IF NOT THE
**SAME.
F4B1 37 PFCOMP FSHB
F4B2 F64011 LCAB SMONTH
F4B5 F7401C STAB TEMP1 ;SAVE SMONTH
F4B6 F64012 LCAB SDAY
F4B8 F74014 STAB TEMP2 ;SAVE SDAY
F4BE B64017 LCAA FMONTH
F4C1 B74011 STAA SMONTH ;PUT FMONTH IN SMONTH
F4C4 B64010 LCAA FDAY
F4C7 B74012 STAA SDAY ;PUT FDAY IN SDAY
F4CA BDF261 JSR PSCOMP ;COMPARE SDATE TO PDATE
F4CD F6401C LCAB TEMP1
F4D0 F74011 STAB SMONTH ;RESTORE SMONTH
F4D3 F6401D LCAB TEMP2
F4D6 F74012 STAB SDAY ;RESTORE SDAY
F4D9 33 FLLB
F4DA 4D TSTA ;TEST A
F4DB 39 RTS ;RETURN TO CALLER

*****
**THIS SUBROUTINE MOVES SMONTH-
**LFREQ INTO LOCATIONS PMONTH-
**LMINL.
F4DC CE4005 STOPS LCX ~SPMONTH
F4DF A60C ROCK LCAA 12,X ;LOAD THE NEW WORD
F4E1 A700 STAA $00,X ;WRITE OVER OLD WORK
F4E3 08 INX ;INCREMENT A+R
F4E4 8C400B CFX ~$LMINL+1 ;COMPARE TO LMINL+1
F4E7 26F6 BNE ROCK ;STOP IF EQUAL
F4E9 39 RTS ;RETURN TO CALLER

*****
**THIS SUBROUTINE INCREMENTS THE A+R
**EQUAL TO THE NUMBER OF MEMORY
**LOCATIONS NEEDED TO STORE EACH
**SET OF READINGS.
F4EA B6402E IRLOOP LCAA INCIR ;A=~$ OF INCREMENTS
F4ED 08 IRLOP INX ;INCREMENT A+R
F4EE 4A CECA ;DECREMENT A
F4EF 26FC BNE IRLOP ;IF A EQUALS 0 QUIT
F4F1 39 RTS ;RETURN TO CALLER

*****
**THIS SUBROUTINE TAKES THE DATE
**AND TIME THAT ARE IN MONTH, DAY,
**TIME, AND TIME AND COMES BACK
**WITH THE ADDRESS OF THE FIRST
**MEMORY LOCATION THAT HOLDS THAT
**READING IN THE A+R. IF THAT

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*READING WAS NEVER TAKEN THEN
**I+R=0000.
F4F2 BDF592 RTAKEN JSR STIME ;SAVE PMONTH-LMINL
F4F5 BDF4DC JSR STOPS ;MOVES SMONTH-LFREQ TO MONTH-LMINL
F4F6 CE4040 LCX ~$STARTM ;I+R=STARTING LOCATION OF DATA
F4F8 BC4025 LOCLOP CFX MDATAH ;IS I+R PAST CURRENT READINGS+/
F4FE 2713 BEQ OVERF ;IS YES THEN READING NOT TAKEN
F500 BDF520 JSR PINCMP ;COMPARES MONTH-TIMEH TO PMONTH-LMINL
F503 2611 BNE RETUNE ;IF NOT 0, THEN LOCATION FOUND
F505 01 NCP
F506 01 NCP
F507 01 NCP
F508 BDF2E1 RJMP JSR UPTIME ;IF 0, THEN INCREMENT THE TIME
F50B 01 NCP
F50C 01 NCP
F50D 01 NCP

;AND TRY AGAIN
F50E BDF4EA JSR IRLOOP ;INCREMENT I+R THE CORRECT AMOUNT
F511 20E8 BRA LOCLOP ;BRANCH BACK FOR TEST
F513 CE0000 OVERF LCX ~$0000 ;READING NOT TAKEN
F516 FF4021 RETUNE STX TEMP12 ;STORE TEMP12
F519 BDF5B3 JSR RTIME ;RESTORE PMONTH-LMINL
F51C FE4021 LCX TEMP12 ;RESORE I+R
F51F 39 RTS ;RETURN TO CALLER

*****
*****
**THIS SROUTINE COMPARES MONTH,
*DAY,TIMEH,TIMEL TO PMONTH,
*PDAY,PTIMEH,PTIMEL RESPECTIVELY.
**IF MONTH...TIMEL OCCURED BEFORE
*PMONTH...PTIMEL, THEN A ONE
*IS RETURNED IN A OTHERWISE A
*ZERO IS RETURNED.
F520 FF4021 PINCMP STX TEMP12 ;SAVE I+R
F523 CE4001 LCX ~$MONTH ;I+R = ADDRESS OF MONTH
F526 BC4003 XLOOP CFX ~$TIMEH ;IS I+R=ADDRESS OF PMONTH
F529 2709 BEQ ALEQUAL ;NOW CHECK TIME
F52B A600 LCAA $00,X ;ONE OF FIRST WORDS
F52D A104 GMPA $04,X ;COMPARE TO ONE OF SECOND WORDS
F52F 221E BFI XOVER ;IF FIRST TIME IS AFTER SECOND
;THEN THIS IS NOT TIME
;INCREMENT I+R
;BRANCH BACK TO TEST NEXT LOCATION
F531 08 INX
F532 20F2 BRA XLOOP
F534 F64003 ALEQUAL LCAB TIMEH
F537 B64004 LCAA TIMEL
F53A B04008 SLBA PTIMEL
F53D F24007 SECB PTIMEH
F540 2D09 BLT EQUAL
F542 2702 BEQ SECCHK
F544 2009 BRA XOVER
F546 4D SECCHK TSTA
F547 2702 BEQ EQUAL
F549 2004 BRA XOVER
F54B 8601 EQUAL LCAA ~$01 ;TIME FOUND LOAD 1
F54D 2001 BRA XDON ;GOTO DONE
F54F 4F XOVER CLRA ;NOT FOUND LOAD 0
F550 FE4021 XDON LCX TEMP12 ;RESTORE I+R

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F553 4D          TSTA          ;TEST +A
F554 39          RTS           ;RETURN TO CALLER

*****
**THIS SIEROUTINE PRINTS A
**LINE FEED (+L+F) AND A CARRAGE
**RETURN (+C+R). +RUBOUTS ARE
**INTEREC INCASE A TELETYPE
**IS USED.
F555 36          LFCR          FSHA
F556 860D        LCAA          ~550D
F558 B0F2A0      JSR           OUTA
F55B 860A        LCAA          ~550A
F55D B0F2A0      JSR           OUTA
F560 867F        LCAA          ~557F
F562 B0F2A0      JSR           OUTA
F565 B0F2A0      JSR           OUTA
F568 32          FLLA
F569 39          RTS

*****
**THIS SIEROUTINE SHIFTS A
**16 BIT WORD 4 BITS TO THE LEFT.
**THE 16 BIT WORD ARE 2 MEMORY
**LOCATIONS, THE FIRST IS POINTED
**TO BY THE +I+R.
F56A 4F          LSHFT4 CLRA          ;CLEAR +A
F56B 6801        ASL           $01,X  ;SHIFT LOW ORDER WORD
F56D 6900        RCL           $00,X  ;SHIFT HGH ORDER WORD
F56F 4C          INCA          ;INCREMENT +A
F570 8104        CMPA          ~5504  ;TEST FOR 4 SHIFTS
F572 26F7        BNE          AROUND  ;IF NOT DONE LOOP BACK
F574 39          RTS           ;RETURN TO CALLER

*****
**THIS SIEROUTINE SHIFTS A 16 BIT
**WORD 1 BIT TO THE RIGHT. +THE WORD
**IS POINTED TO BY THE +I+R.
F575 6400        SHIFTR LSR          00,X  ;SHIFT THE HIGH ORDER WORD
F577 6601        RCR          01,X  ;SHIFT THE LOW ORDER WORD
F579 39          RTS           ;RETURN TO CALLER

*****
**THIS SIEROUTINE PRINTS OUT ONE
**BYTE. +THE BYTE IS IN +A.
F57A 37          BYTOUT FSHB          ;SAVE +B
F57B 16          TAB           ;+B=+A
F57C 44          LSRA
F57D 44          LSRA
F57E 44          LSRA
F57F 44          LSRA
F580 C40F        ANDB          ~550F  ;SHIFT +A RIGHT 4 BITS
F582 37          FSHB          ;MASK OUT UPPER 4 BITS
F583 B0F68D      JSR           HEXASC  ;SAVE +B
F586 B0F2A0      JSR           OUTA    ;CONVERT TO HEX
F589 32          FLLA          ;PRINT
F58A B0F68D      JSR           HEXASC  ;GET LOWER 4 BITS
F58D B0F2A0      JSR           OUTA    ;CONVERT TO HEX
F590 33          PLLB          ;PRINT +A
F591 39          RTS           ;RESORE +B
                          ;RETURN TO CALLER

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*****
**THIS SUBROUTINE SAVES PMONTH-
**LMINL IN TEMPORARY LOCATIONS.
F592 36      STIME  FSHA      ;SAVE +A
F593 37      FSHB      ;SAVE +B
F594 BDF616   JSR      SAT5    ;CHECK IF AN STIME
                                   ;HAS BEEN DONE
                                   ;WITHOUT AN RTIME FOLLOWING IT
                                   ;IF IT HASN'T THEN BRANCH
F597 26C2     BNE      CSTIME   ;ILLEGAL REQUEST LEAVE SUBROUTINE
F599 2015     BFA      SDON     ;+I+R=ADDRESS OF PMONTH
F598 CE4005   LCX      ~$PMONTH
F59E A600     NOCH1    LCAA     $00,X ;+A=VALUE TO BE MOVED
F5A0 A706     STAA     $06,X      ;STORE VALUE IN A TEMP. LOC.
F5A2 08       INX      ;INCREMENT +I+R
F5A3 8C400B   CPX      ~$TEMP6   ;ARE WE DONE+/?
F5A6 26F6     BNE      NOCH1     ;IF NO LOOP BACK
F5A8 8620     LCAA     ~$S20
F5AA BA4000   CFAA     STATUS
F5AD B74000   STAA     STATUS      ;SET STATUS BIT SHOWING AN
                                   ;STIME HAS BEEN DONE
F5B0 33      SDON     PLLB      ;RESTORE +B
F5B1 32      PLLA      ;RESTORE +A
F5B2 39      RTS      ;RETURN TO CALLER

*****
**THIS SUBROUTINE RESTORES THE VALUES
**OF PMONTH~LMINL. (+NOTE+; +IF AN STIME
**HAS NOT BEEN DONE THIS SUBROUTINE
**WILL DO NOTHING.)
F5B3 36      RTIME  FSHA      ;SAVE +A
F5B4 37      FSHB      ;SAVE +B
F5B5 BDF616   JSR      SAT5    ;CHECK FOR STIME DONE
F5B6 2702     BEQ      CRTIME   ;IF AN STIME HAS BEEN DONE BRANCH
F5B8 2015     BFA      RDON     ;STIME NOT DONE EXIT ROUTINE
F5BA CE4005   CRTIME  LCX      ~$PMONTH
F5BC A606     NOCH2    LCAA     $06,X ;+I+R=PMONTH
F5BF A700     STAA     $00,X      ;LOAD VALUE
F5C1 08       INX      ;RESORE THE VALUE
F5C3 8C400B   CPX      ~$TEMP6   ;INCREMENT +I+R
F5C4 26F6     BNE      NOCH2     ;ARE WE DONE+/?
F5C7 860F     LCAA     ~$SDF      ;IF NO, THEN LOOP BACK
F5C9 B44000   ANDA     STATUS
F5CB B74000   STAA     STATUS
F5CD 33      RDON     PLLB      ;RESET STATUS BIT
F5D1 32      PLLA      ;RESTORE +B
F5D2 39      RTS      ;RESTORE +A
F5D3 39      RTS      ;RETURN TO CALLER

*****
**THIS SUBROUTINE TAKES WHAT IS
**IN +B AND PRINTS IT. +THEN IT
**PRINTS WHATS IN TEMP5 (USUALLY
**A +7+;+7 OR A +7/+7). +AND FOLLOWS
**THAT BY THE CHARACTER IN +A.
F5D4 36      VAROUT FSHA      ;SAVE +A
F5D5 17      TEA      ;+A=+B
F5D6 BDF3E6   JSR      BCD      ;CONVERT TO BCD
F5D9 BDF57A   JSR      BYTOUT   ;PRINT THE TWO CHARS
F5DC B64020   LCAA     TEMP5
F5DF BDF2A0   JSR      OUTA     ;PRINT WHAT IS IN TEMP5

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F5E2 32          PULA          ;RESTORE +A
F5E3 BDF3E6      JSR          BCD ;CONVERT TO BCD
F5E6 BDF57A      JSR          BYTOUT ;PRINT BOTH CHARS
F5E9 39          RTS          ;RETURN TO CALLER

*****
**THIS SUBROUTINE PRINTS THE DATE.
**THE MONTH MUST BE IN +B AND THE
**DAY MUST BE IN +A.
F5EA 36          PDATE        PSHA          ;SAVE +A
F5EB 862F        LCAA         ~$52F
F5ED B74020      STAA         TEMP5         ;TEMPS=+7/+7
F5F0 32          PULA          ;RESTORE +A
F5F1 BDF5D4      JSR          VAROUT        ;VAROUT PRINTS OUT DATE
F5F4 39          RTS          ;RETURN TO CALLER

*****
**THIS SUBROUTINE PRINTS OUT
**FIVE SPACES.
F5F5 36          PSPACE       PSHA          ;SAVE +A
F5F6 37          RSHB         ;SAVE +B
F5F7 CEF000      LDX          ~$SPACES      ;LOAD +I+R WITH ADDRESS
F5FA BDF717      JSR          PRINT         ;PRINT +7 +7
F5FD 33          PLB          ;RESTORE +B
F5FE 32          PULA          ;RESTORE +A
F5FF 39          RTS          ;RETURN TO CALLER

*****
**THIS SUBROUTINE PRINTS ONE SPACE.
F600 36          PSP          PSHA          ;SAVE +A
F601 37          RSHB         ;SAVE +B
F602 8620        LCAA         ~$520
F604 BDF2A0      JSR          OUTA         ;PRINT +7 +7
F607 33          PLB          ;RESTORE +B
F608 32          PULA          ;RESTORE +A
F609 39          RTS          ;RETURN TO CALLER

*****
**THIS SUBROUTINE CLEARS THE STACK
**IN EXACTLY THE SAME MANNER THAT
**A +R+T+I INSTRUCTION WOULD, BUT WITH-
**OUT THE RETURN JUMP. (+A +R+T+I IS
**A RETURN FROM AN INTERRUPT.)
F60A 32          FAKERI       PULA          ;SAVE +A (IN +A)
F60B 33          PLB          ;SAVE +B (IN +B)
F60C 31          JNS
F60D 31          JNS
F60E 31          JNS
F60F 31          JNS
F610 31          JNS
F611 31          JNS
F612 31          JNS          ;INCREMENT THE STACK POINTER SEVEN
                                ;TIMES JUST LIKE AN +R+T+I
F613 37          RSHB         ;PUSH +B BACK ON STACK
F614 36          PSHA          ;PUSH +A BACK ONTO THE STACK
F615 39          RTS          ;RETURN TO CALLER

*****
**THIS SUBROUTINE CHECKS THE
**STATUS BIT THAT IS SET AND
**CLEARED BY RTIME AND STIME.
F616 8620        SAIS        LCAA         ~$520          ;PUT A ONE IN THE CORRECT BIT

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F616 B44000 ANDA STATUS ;+A+D THE STATUS WORD
F618 8020 SLBA ~$520 ;SUBTRACT 20(HEX) TO TEST BIT
F61D 39 RTS ;RETURN TO CALLER

*****
*THIS SUBROUTINE INHIBITS
*THE CLOCK BOARD FROM GEN-
*ERATING AN +N+M+I (INTERRUPT).
F61E 36 OFFCLK FSHA ;SAVE +A
F61F 8604 LCAA ~$504 ;SET THE PROPER BIT
F621 BAE002 CFAA CLOCKB ;FORM WORD TO WRITE TO CLOCK
F624 B7E002 STAA CLOCKB ;WRITE WORD TO CLOCK
F627 32 FLLA ;RESTORE +A
F628 39 RTS ;RETURN TO CALLER

*****
*THIS SUBROUTINE ENABLES THE
*MEMORY. +THE TWO BITS THAT
*CONTROL THIS ARE ON THE CLOCK
*BOARD (PCRT +B).
F629 36 ENMEM FSHA ;SAVE +A
F62A 867F LCAA ~$57F ;ZERO CORRECT BIT
F62C B4E000 ANDA CLOCKA ;FORM WORD TO WRITE TO CLOCK
F62F 8A40 CFAA ~$540 ;SET (1) CORRECT BIT
F631 B7E000 STAA CLOCKA ;WRITE WORD TO CLOCK
F634 32 FLLA ;RESTORE +A
F635 39 RTS ;RETURN TO CALLER

*****
*THIS SUBROUTINE ENABLES THE
*INTERRUPT FROM THE CLOCK BOARD.
F636 B6E002 CLCLK LCAA CLOCKB
F639 84FB ANDA ~$5FB ;RESET BIT 2
F63B B7E002 STAA CLOCKB ;WRITE TO CLOCK
F63E 39 RTS ;RETURN TO CALLER

*****
*THIS SUBROUTINE TAKES THE NUMBER
*IN [+A+B] AND STUFFS IT INTO THE
*TIMER ON THE CLOCK BOARD. +THE
*TIMER IS ONLY 12 BITS AND THE
*4 HIGH ORDER BITS OF +A ARE
*ASSUMED TO BE ZERO (DISCARDED).
*(+BIT 2 OF CLOCKA IS THE DATA IN
*TO THE SHIFT REGISTER. +BIT 1
*IS THE CLOCK ON THE SHIFT REG-
*ISTER. )
F63F B74009 CLKSET STAA LMINH ;STORE TIME IN LMINH
F642 F7400A STAB LMINL ;STORE TIME IN LMINL
F645 BDF4AE JSR SHFROL
F648 BDF4AE JSR SHFROL
F64B BDF4AE JSR SHFROL ;SHIFT LEFT [+A+B] 3 TIMES
F64E CE000C LCX ~$12 ;+I+R=12
F651 BDF4AE OVERHER JSR SHFROL ;SHIFT LEFT [+A+B]
F654 36 FSHA ;SAVE +A
F655 4D TSTA ;TEST HIGH ORDER BIT
F656 2C14 BCE BITZERO ;IF HIGHEST BIT IS 0 BRANCH
F658 B6E000 LCAA CLOCKA
F65B 8A04 CFAA ~$504 ;FORM CLOCK WORD WITH DATA IN = 1
F65D B7E000 STAA CLOCKA ;WRITE WORD TO CLOCK
F660 8A06 CFAA ~$506

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F662  B7E000      .STAA  CLOCKA      ;MAKE SHIFT CLOCK GO HIGH
F665  84FC        ANDA   ~$5FC
F667  B7E000      STAA   CLOCKA      ;MAKE SHIFT CLOCK GO LOW
F66A  2012        BFA    RIGHTH      ;CLEAN UP BEFORE LOOPING BACK
F66C  B6E000  BITZERO LCAA  CLOCKA    ;HIGHEST BIT IS ZERO
F66F  84F8        ANDA   ~$5F8
F671  B7E000      .STAA  CLOCKA      ;SET INPUT BIT TO ZERO
F674  8A02        .CFAA  ~$502
F676  B7E000      STAA   CLOCKA      ;SET SHIFT CLOCK TO HIGH
F679  84F8        ANDA   ~$5F8
F67B  B7E000      .STAA  CLOCKA
F67E  32          RIGHTH  FLA
F67F  09          CEX
F680  26CF        BNE    OVERHER
F682  39          RTS
*****
**THIS SUBROUTINE CONVERTS THE
**ASCII CHARACTER IN +A INTO A
**HEX NUMBER. (+IT IS ASSUMED
**THAT THE CHARACTER IS A
**HEX NUMBER IN ASCII, IE. 0-F.)
F683  8039        ASCHEX  .SLBA  ~$539      ;SUBTRACT 39 (HEX)
F685  2E03        BCT     BETA      ;BRANCH IF CHAR WAS +A~+F
F687  8B09        ACDA   ~$509      ;ADD 9
F689  39          RTS              ;RETURN TO CALLER
F68A  8B02        BEIA  ACDA   ~$502      ;IS +A,+B,+C,+D,+E, OR +F ADD 2
F68C  39          RTS              ;RETURN TO CALLER
*****
**THIS SUBROUTINE CONVERTS A
**HEX NUMBER INTO A ASCII CHAR.
F68D  8BF7        HEXASC  ACDA   ~$5-9      ;SUBTRACT 9
F68F  2E03        BGT     ALPHA      ;BRANCH IF +A~+F
F691  8B39        ACDA   ~$539      ;ADD 39 (HEX)
F693  39          RTS              ;RETURN TO CALLER
F694  8B40        ALPHA  ACDA   ~$540      ;ADD 40 (HEX)
F696  39          RTS              ;RETURN TO CALLER
*****
**THIS SUBROUTINE INITIALIZES
**ALL OF THE PIAS IN THE SYSTEM.
**THERE ARE TWO PIAS; THE CLOCK
**AND THE A/D PIA.
F697  7FE801      PIAS   CLR     ADPIAA+1
F69A  7FE803      CLR     ADPIAB+1
F69D  7FE001      CLR     CLOCKA+1
F6A0  7FE003      CLR     CLOCKB+1      ;CLEAR THE CONTROL REGISTER
F6A3  4F          CLRA
F6A4  B7E800      STAA   ADPIAA      ;CONFIGURE ALL BITS AS INPUT
F6A7  86E0        LCAA  ~$5E0
F6A9  B7E802      STAA   ADPIAB      ;CONFIGURE BITS 5,6, AND 7 AS OUTPUT
                                      ;THE REST AS INPUT
F6AC  8604        LCAA  ~$504
F6AE  B7E801      STAA   ADPIAA+1      ;DISABLE INTERRUPTS AND
                                      ;SET LOCATION
F6B1  B7E802      STAA   ADPIAB      ;TO THE PERIPHERAL REGISTER
                                      ;DISABLE INTERRUPTS AND SET
F6B4  4A          CECA              ;TO THE PERIPHERAL REGISTER

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F6B5  B7D800      STAA  CNTRL      ;RESET THE +A+C+I+A
F6B8  8689        LCAA  ~$5B9
F6BA  B7D800      STAA  CNTRL      ;SET CLOCK RATE, WORD LENGTH,
F6BD  86FF        LCAA  ~$5FF      ;AND INTERRUPT
F6BF  B7E000      STAA  CLOCKA     ;CONFIGURE ALL BITS AS OUTPUT
F6C2  8605        LCAA  ~$505
F6C4  B7E001      STAA  CLOCKA+1   ;SET INTERRUPT TO THE NEGATIVE EDGE
                                      ;AND CHANGE ACCESS TO PERIPHERAL
F6C7  86F7        LCAA  ~$5F7
F6C9  B7E002      STAA  CLOCKB     ;SET BIT 3 AS INPUT, THE REST AS OUTPUT
F6CC  8604        LCAA  ~$504
F6CE  B7E003      STAA  CLOCKB+1   ;CHANGE ACCESS TO THE PERIPHERAL
                                      ;AND DISABLE ALL INTERRUPTS
F6D1  8630        LCAA  ~$530
F6D3  B7E000      STAA  CLOCKA     ;DISABLE MEMORY WITH THE NORMAL BITS
F6D6  B6E000      LCAA  CLOCKA     ;CLEAR ANY INTERRUPT ON PORT +A
F6D9  39          RTS

*****
**THIS SUBROUTINE CONVERTS AN ANALOG VOLTAGE
**ON PORT SPECIFIED BY SENNUM TO A 12 BIT
**DIGITAL NUMBER AND STORES IT IN CONVH,CONVL.
F6DA  36          ADCVAL  PSHA
F6DB  37          ADCVAL  PSHB
F6DC  B6402C      LCAA  SENNUM
F6DF  48          ASLA
F6E0  48          ASLA
F6E1  48          ASLA
F6E2  48          ASLA
F6E3  48          ASLA
F6E4  B7E802      STAA  ADPIAB      ;SELECTS PORT
F6E7  BDF9E5      JSR   MDELAY      ;ALLOWS MULTIPLEXER TO SETTLE
F6EA  8620        LCAA  ~$520
F6EC  BAE002      CRAA  CLOCKB
F6EF  B7E002      STAA  CLOCKB      ;INITIATES +A/+D CONVERSION
F6F2  860F        LCAA  ~$50F
F6F4  B4E002      ANDA  CLOCKB
F6F7  B7E002      STAA  CLOCKB
F6FA  BDF9E5      JSR   MDELAY      ;ALLOWS +A/+D TIME TO START CONVERSION
F6FD  B6E802      READY  LCAA  ADPIAB
F700  8410        ANDA  ~$510
F702  26F9        BNE  READY
F704  B6E802      LCAA  ADPIAB      ;CHECKS STATUS OF +A/+D CONVERSION
                                      ;TAKES COMPLEMENTARY BINARY OUTPUT,
                                      ;CONVERTS TO SIGNED BINARY AND
                                      ;STORES IN CONVH,CONVL
F707  8AF0        CRAA  ~$5F0
F709  43          CCMA
F70A  B74023      STAA  CONVH
F70D  B6E800      LCAA  ADPIAA
F710  43          CCMA
F711  B74024      STAA  CONVL
F714  33          PULB
F715  32          PULA
F716  39          RTS

*****
**THIS SUBROUTINE PRINTS OUT
**CHARACTER STRINGS STORED IN
**THE PROGRAM. +THE STARTING

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*ADDRESS OF THE CHARACTER
*STRING MUST BE IN THE +I+R
*WHEN THE SUBROUTINE IS CALLED,
**THE SUBROUTINE THEN PRINTS
*OUT CHARACTER FROM CONSEC-
*UTIVE MEMORY LOCATIONS UNTIL
*IT READS A 00.
F717 36 PRINT FSHA ;SAVE +A
F718 A600 BACKW LCAA $00,X ;MOVE CHARACTER INTO +A
F71A 8100 CNPA ~$500 ;IS IT A ZERO+/?
F71C 2602 BNE HERE ;IF IT ISN'T THEN BRANCH TO HERE
F71E 32 RLLA ;RESTORE +A
F71F 39 RTS ;RETURN TO CALLER
F720 BDF2A0 HERE LSR OUTA ;PRINT THE CHARACTER
F723 08 INX ;INCREMENT THE +I+R
F724 20F2 BFA BACKW ;LOOP BACK FOR MORE CHARACTERS

*****
**THIS SUBROUTINE PRINTS OUT
**A TIME. +THE TIME TO BE PRINTED
**MUST BE IN MINUTES AND MUST
**BE STORED IN [+B+A].
F726 CE0000 PTIME LCX ~$500 ;CLEAR +I+R
F729 08 PLOOP INX ;INCREMENT +I+R (COUNT HOURS)
F72A 803C SLBA ~$60 ;SUBTRACT SIXTY MINUTES
F72C C200 SECB ~$500 ;SUBTRACT CARRY
F72E 2CF9 BGE PLOOP ;IF NOT NEGATIVE LOOP BACK
F730 8B3C ACDA ~$60 ;MAKE TIME POSITIVE AGAIN
F732 09 CEX ;ADJUST +I+R
F733 FF4021 STX TEMP12 ;SAVE IN A TEMPORARY LOC.
F736 F64022 LCAB TEMP13 ;WRITE ~$ OF HOURS INTO +B
F739 36 FSHA ;SAVE +A
F73A 863A LCAA ~$53A
F73C B74020 STAA TEMP5 ;TEMP5=+7+;+7
F73F 32 RLLA ;RESTORE +A
F740 BDF5D4 LSR VAROUT ;PRINT THE TIME
F743 39 RTS ;RETURN TO CALLER

*****
**MULTIPLIES SUCCESSIVE MEMORY LOCATIONS SPECIFIED
**BE INDEX REGISTER BY 2.44140625 MAINTAINING
**THE RUNNING SUM IN +A AND +B. (CONTENTS OF THE
**MEMORY LOCATIONS ARE DESTROYED).
F744 A601 MULT1 LCAA $01,X
F746 E600 LCAB $00,X
F748 48 ASLA
F749 59 RCLB
F74A BDF575 LSR SHIFTR
F74D BDF763 LSR SHIFAD
F750 BDF763 LSR SHIFAD
F753 BDF763 LSR SHIFAD
F756 BDF575 LSR SHIFTR
F759 BDF575 LSR SHIFTR
F75C BDF575 LSR SHIFTR
F75F BDF763 LSR SHIFAD
F762 39 RTS

*****
**THIS ROUTINE RIGHT SHIFTS THE 16
**BIT WORD IN SUCCESSIVE MEMORY LOCATIONS SPEC-

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*IFIED BY THE INDEX REGISTER, THEN ADDS THEM
*TO CONTENTS OF +A,+B (WITH ROUNDING).
F763 6400 SHIFAD LSR 00,X
F765 6601 FCR 01,X
F767 A901 ACCA 01,X
F769 E900 ACCB 00,X
F76b 39 RTS
*****
**THIS SUBROUTINE CONVERTS THE BINARY NUMBER
**IN CONVH,CONVL TO +B+C+D AND RETURNS IT
**TO CONVH, CONVL.
F76C FE4023 DECADJ LCX CONVH
F76F 4F CLRA
F770 5F CLRB
F771 8C0000 DONYET CFX ~$50000 ;CHECK IF +I+R=0000
F774 2711 BEQ FINISH
F776 09 CEX ;DECREMENT +I+R
F777 8B01 ACDA ~$501 ;ADD ONE TO +A
F779 19 CAA ;CONVERT +A TO A DECIMAL NUMBER
F77A C900 ACCB ~$500 ;ADD CARRY BIT TO +B
F77C B74023 STAA CONVH
F77F 17 TEA
F780 19 CAA ;CONVERT +B TO A DECIMAL NUMBER
F781 16 TAB
F782 B64023 LCAA CONVH
F785 20EA BFA DONYET
F787 B74024 FINISH STAA CONVL ;STORE +B+C+D RESULT IN CONVL,CONVH
F78A F74023 STAB CONVH
F78D 39 RTS
*****
F78E 484F57 HOW FCC +7HOW MANY+ /+7
F791 204041
F794 4E593F
F797 00 FCB 00
F798 285858 THREE FCC +7(XXX) +7
F79b 582920
F79E 00 FCB 00
F79F 4F4E2E ON FCC +7ON,+7
F7A2 00 FCB 00
F7A3 4F4640 OFF FCC +7OFF,+7
F7A6 2E
F7A7 00 FCB 00
F7A8 574152 POWIS FCC +7WARM-UP POWER IS +7
F7AB 4D2D55
F7AE 502050
F7B1 4F5745
F7B4 522049
F7B7 5320
F7B9 00 FCB 00
F7BA 2A4445 DEL FCC +7*DEL*+7
F7BD 4C2A
F7BF 00 FCB 500
*****
**THIS SUBROUTINE PRINTS OUT THE
**STATE OF THE WARMUP POWER (+0+N,
**+0+F*F).
F7C0 CEF7A8 ALERT LCX ~$POWIS

```

F7C3	BDF717		JSR	PRINT	;PRINT +7+WARM-UP POWER IS+7
F7C6	BDF629		JSR	ENMEM	;ENABLE MEMORY
F7C9	B64000		LCAA	STATUS	
F7CC	8402		ANDA	~\$502	;CHECK WARMUP POWER BIT
F7CE	270D		BEG	WOFF	;IF ZERO THEN IT IS OFF
F7D0	B6401B		LCAA	WTIME	
F7D3	2708		BEQ	WOFF	;IF WTIME=0 THEN THERE IS
					;NO WARM-UP TIME
F7D5	CEF79F		LCX	~\$0N	
F7D8	BDF717		JSR	PRINT	;PRINT +0+N+.
F7DB	2006		BRA	DALERT	;BRANCH TO DALERT
F7DD	CEF7A3	WOFF	LCX	~\$0FF	
F7E0	BDF717		JSR	PRINT	;PRINT +0+F+F+.
F7E3	BDF555	DALERT	JSR	LFCR	;SKIP A LINE
F7E6	BDF4A3		JSR	DISMEM	;DISABLE MEMORY
F7E9	39		RTS		;RETURN TO CALLER

					END

* THE BINARY IN IN PHYSICAL BLOCK 2

```

*
*
*
      CRG      $F800
*****
**THIS SUBROUTINE APPLIES A LINEAR CONVERSION
**TO THE BINARY DATA IN CONVH,CONVL DEPENDING
**ON THE CONTENTS OF SENNUM**
*      SENNUM      OUTPUT RANGE
*      0           -30.00 - 70.00
*      1           -34.00 - 50.00
*      2           800.00 - 1100.00
*      >2          00.00 - 5.00
**THE OUTPUT IS PRINTED IN DECIMAL FORMAT.
F800 36      SENSCV  FSHA
F801 FF402b    STX      TEMPIR
F804 37      FSHB
F805 CE4023    LCX      -SCONVH
F806 B6402C    LCAA     SENNUM
F80B 4D      TSTA
F80C 2603     BNE      CHERE
F80E 7EF892    JMP      TEMPCV
F811 8101     CHERE    CMPA     -S$01
F813 2603     BNE      SKIPJMP
F815 7EF881    JMP      DEWPT
F816 8102     SKIPJMP CMPA     -S$02
F81A 2603     BNE      HOP1
F81C 7EF8E4    JMP      PRESUR
F81F B0F575    HOP1     JSR      SHIFTR
F822 AB01     LCAA     $01,X
F824 E600     LCAB     $00,X
F826 B0F744    JSR      MULT1
F829 A701     STAA     $01,X
F82L E700     STAB     $00,X
F82D B0F76C    JSR      DECADJ
F82U CE4023    LCX      -SCONVH
F833 AB00     LCAA     $00,X
F835 84F0     ANDA     -S$F0
F837 44      LSRA
F838 44      LSRA
F839 44      LSRA
F83A 44      LSRA
F83B 44      LSRA
F83D BA30     CFAA     -S$30
F83D B0F2A0    JSR      OUTA
F841 862E     LCAA     -S$2E
F842 B0F2A0    JSR      OUTA
F843 AB00     LCAA     $00,X
F847 840F     ANDA     -S$0F
F849 8A30     CFAA     -S$30
F84B B0F2A0    JSR      OUTA
F84E AB01     LCAA     $01,X
F850 B0F57A    JSR      BYTOUT
F853 7EF889    JMP      MISSED
F855 B0F76C    TOGETR  JSR      DECADJ
F859 B64023    JOINUP  LCAA     CONVH
;TEST FOR SENNUM=0, GOTO TEMPCV
;TEST FOR SENNUM=1, GOTO DEWPT
;TEST FOR SENNUM=2, GOTO PRESUR
;DIVIDE NUMBER BY 2
;MULTIPLY NUMBER BY 2.44140625
;CONVERT FIRST A+B+C+D DIGIT
;TO ASCII
;PRINT THE DIGIT
;PRINT THE DECIMAL POINT
;CONVERT FIRST A+B+C+D DIGIT
;TO ASCII
;PRINT THE DIGIT
;PRINT THE THIRD AND FOURTH DIGITS
;CONVERT TO DECIMAL NUMBER

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```

F85C 4D          TSTA
F85D 2008        BNE      SECND
F85F 3DF600      JSR      PSP
F862 8DF600      JSR      PSP
F865 2017        BRA      SKIP
F867 84F0        SECND   ANDA      ~$5F0
F869 200D        BNE      PRTALL
F86B 8DF600      JSR      PSP
F86E 804023      LCAA      CONVH
F871 8A30        CFAA      ~$530
F873 8DF2A0      JSR      OUTA
F875 2006        BRA      SKIP
F878 804023      PRTALL  LCAA      CONVH
F87D 8DF57A      JSR      BYTOUT
F87E 862E        SKIP    LCAA      ~$52E
F880 80F2A0      JSR      OUTA
F883 804024      LCAA      CONVL
F886 8DF57A      JSR      BYTOUT
F889 8DF600      MISSED  JSR      PSP
F88C FE4020      LCA      TEMPIR
F88F 33          FLLB
F890 32          FLLA
F891 39          RTS
F892 5F01        TEMPCV  CLR      $01,X
F894 8DF94A      JSR      RSHIFT4

F897 4601        LCAA      $01,X
F899 E600        LCAB      $00,X
F89B 8DF744      JSR      MULT1
F89E 80B8        SLBA      ~$5B8
F8A0 C208        SECB      ~$50B
F8A2 A701        STAA      01,X
F8A4 E700        STAB      00,X
F8A6 2C03        BCE      NONEG
F8A8 7EF937      JMP      NEGNUM
F8AA 8DF600      NONEG   JSR      PSP

F8AE 7EF850      JMP      TOGETR
F8B1 5F01        DEWPT   CLR      $01,X
F8B3 8DF94A      JSR      RSHIFT4

F8B5 A601        LCAA      $01,X
F8B7 E600        LCAB      $00,X
F8B9 4B          ASLA
F8BB 59          HCLB
F8BD 8DF575      JSR      SHIFTR
F8BF 8DF575      JSR      SHIFTR
F8C2 8DF575      JSR      SHIFTR
F8C5 8DF575      JSR      SHIFTR
F8C8 8DF763      JSR      SHIFAD
F8CB 80F760      JSR      SHIFAD
F8CE 8DF575      JSR      SHIFTR
F8D1 8DF763      JSR      SHIFAD
F8D4 8048        SLBA      ~$548
F8D6 C2CD        SECB      ~$50D
F8D8 A701        STAA      $01,X
F8DA E700        STAB      $00,X

;CHECK FOR TWO LEADING ZEROS
;IF NOT, GO TO SECND
;PRINT A SPACE
;PRINT ANOTHER SPACE

;CHECK FOR ONE LEADING ZERO
;IF NOT, GO TO PRTALL
;PRINT A SPACE

;CONVERT SECOND DIGIT TO ASCII
;PRINT THE DIGIT

;PRINT BOTH LEADING DIGITS

;PRINT A DECIMAL POINT

;PRINT LAST TWO DIGITS
;PRINT A SPACE

;SHIFT 8 BIT TEMP. WORD TO
;THE RIGHT 4 TIMES

;MULT. BY 2.44140625
;SUBTRACT 3000
;FORM THE RESULT

;CHECK IF RESULT IS NEGATIVE
;IF SO, GO TO NEGNUM
;IF NOT, PRINT SPACE AND
;SHIFT TO THE RIGHT 4 TIMES

;SHIFT 8 BIT DEW PT. WORD TO
;THE RIGHT 4 TIMES

;MULTIPLY DATA BY 2.00078125

;SUBTRACT 3400 FROM THE RESULT

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F8D0 2D03      ELT      HOPUP
F8D1 7EF8AB    JPP      NONEG      ;IF RESULT IS NOT NEGATIVE,
                                      ;BRANCH TO NONEG
                                      ;IF NOT JUMP TO NEGNUM
F8E1 7EF937    HOPUP    JPP      NEGNUM
F8E4 A601      PRESUR   LCAA     $01,X
F8E6 E600      LCAB     $00,X
F8E8 BDF744    JSR      MULT1    ;MULTIPLY BY 2.44140625
F8EB A701      STAA     $01,X
F8ED E700      STAB     $00,X
F8EF BDF76C    JSR      DECAJ    ;CONVERT TO A DECIMAL NUMBER
F8F2 CE4023    LCX      -SCONVH
F8F5 5F        CLRB
F8F6 A601      LCAA     $01,X
F8F8 A801      ACDA     $01,X
F8FA 19        CAA
F8FB B74032    STAA     CONVL1    ;AND STORE IN CONVL1
F8FC A600      LCAA     $00,X
F900 A900      ACDA     $00,X
F902 19        CAA
F903 B74031    STAA     CONVH1
F906 01        NCP
F907 C900      ACDB     -$$00
F909 B64032    LCAA     CONVL1
F90C AB01      ACDA     $01,X
F90E 19        CAA
F90F A701      STAA     $01,X
F911 B64031    LCAA     CONVH1
F914 A900      ACDA     $00,X
F916 19        CAA
F917 A700      STAA     $00,X
F919 C900      ACDB     -$$00
F91B 17        TEA
F91C 8B08      ACDA     -$$08
F91E 19        CAA
F91F 36        PSHA
F921 8410      ANDA     -$$10
F922 2707      BEQ      ZERO
F924 32        PLLA
F925 BDF57A    JSR      BYTOUT
F926 7EF876    JPP      PRTALL
F928 BDF600    JSR      PSP
F92E 32        PLLA
F92F 8430      CFAA     -$$30
F931 BDF2A0    JSR      OUTA
F934 7EF876    JPP      PRTALL
F937 A601      NEGNUM   LCAA     $01,X
F939 E600      LCAB     $00,X
F93B 40        NEGA
F93C 50        INEGB
F93D 5A        DECB
F93E A701      STAA     $01,X
F940 E700      STAB     $00,X
F942 862D      LCAA     -$$2D
F944 BDF2A0    JSR      OUTA
F947 7EF856    JPP      TOGETR
                                      ;PRINT THE MINUS SIGN
                                      ;JUMP TO TOGETR

```

*THIS ROUTINE SHIFTS CONSECUTIVE MEMORY


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*LOCATIONS (SPECIFIED BY THE +I+R) FOUR TIMES
*TO THE RIGHT.
F94A BDF575 RSHIFT4 LSR SHIFTR
F94C BDF575 LSR SHIFTR
F950 BDF575 LSR SHIFTR
F953 BDF575 LSR SHIFTR
F956 39 RTS
*****
**THIS SUBROUTINE CONVERTS A 12 BIT DATA
*WORD IN [+A+B] TO A 8 BIT DATA WORD IN +A,
*WITH ROUNDING.
F957 FF4028 MOVEIT STA TEMPIR ;SAVE +I+R
F95A CE0004 LCX ~$504
F95D 54 MOVLOP LSRB ;SHIFT +B
F95E 40 RCRB ;SHIFT +A
F95F 09 CEX ;DECREMENT +I+R
F960 26FB BNE MOVLOP ;TEST IF DONE
F962 0900 ACCA ~$500 ;ROUND THE NUMBER IF DONE
F964 C900 ACCB ~$500
F966 FE4028 LCX TEMPIR ;RESORE +I+R
F969 39 RTS
*****
**THIS SUBROUTINE SHIFTS [+B+A] TO THE
*RIGHT 4 TIMES.
F96A FF4028 LEFTSH STX TEMPIR
F96C CE0004 LCX ~$504
F970 48 LSH ASLA
F971 59 RCLB
F972 09 CEX
F973 26FB BNE LSH
F975 FE4028 LCX TEMPIR
F978 39 RTS
*****
**THIS ROUTINE IS EXECUTED WHENEVER
*A HARDWARE INTERRUPT OCCURS(+N+O+T A
*NUM=MASKABLE INTERRUPT). ANY TIME
*THE +A+C+I+A RECEIVES A CHARACTER THIS
*ROUTINE IS EXECUTED.
F979 860800 INH LCAA CNTRL ;LOAD +A WITH STATUS
F97C 8470 ANDA ~$EMASK ;TEST FOR A TRANSMISSION ERROR
F97E 270A EEQ NOERR ;IF NO ERROR BRANCH
F980 863F LCAA ~$53F
F982 80F2A0 LSR OUTA ;IF AN ERROR OCCURED PRINT
F985 860801 LCAA DATA ;A QUESTION MARK
F988 2028 BFA IRQHERE ;FINISH THE ROUTINE
F98A 860801 NOERR LCAA DATA
F98D 80F2A0 LSR OUTA ;PRINT THE RECEIVED CHARACTER
F990 9712 STAA RECEIV ;STORE THE RECEIVED CHARACTER
F992 0613 ENDING LCAH SIRO
F994 CA04 CFAB ~$504
F996 0118 ORPA ~$518 ;IS THE CHARACTER AN +E+S+C+A+P+E
F998 2719 EEQ ESC ;IF YES BRANCH TO ESC
F99A 8113 ORPA ~$513 ;IS THE CHARACTER A CONTROL +S
F99C 2512 BNE STATSO ;IF NOT BRANCH TO STATSO
F99E 80F60A FANE LSR FAKERI ;PULL ANYTHING AN +R+I+S WOULD
F9A0 CL0000 LCX ~$0000
F9A4 0F00 STX BACKUP ;CLEAR BACKUP

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F9A6 7F000A CLR WHEPE ;CLEAR WHERE
F9A7 BDF555 JSR LFCR ;DO A +L+F +C+R
F9AC 0E CLI ;ENABLE FURTHER +I+R+Q+7S
F9AD 7EFCCB JMP DISDON ;RETURN TO USER PAGE
F9B0 D713 STATSO STAB SIRQ ;INDICATE AN +I+R+Q OCCURED
F9B2 3B IRWHERE RTI ;RETURN TO PREVIOUS WORK
F9B3 7D0000 ESL TEST BACKUP ;TEST BACKUP
F9B6 27F8 BEQ STATSO ;IF ZERO THEN DONE
F9B6 BDF60A JSR FAKERI ;IF NOT ZERO PULL FROM THE
;STACK LIKE A +R+T+I
;THIS LOOP FIXES THE STACK

F9B6 32 ESCLOP PLLA
F9B6 B7+023 STAA CONVMH
F9BF 33 FLLB
F9C6 F74024 STAB CONVL
F9C3 FE4023 LCX CONVMH
F9C6 8CF079 CFX -SSSTRIP ;CHECK FOR THE ADDRESS OF STRIP
F9C9 2BF0 BNE ESCLOP ;IF NOT PULL MORE OFF THE STACK
F9CE 37 FSHB ;IF STRIP REPLACE ON STACK
F9C6 36 FSHA
F9C6 BDF5F5 JSR PSPACE ;SKIP A SPACE
F9D0 CEF7BA LCX -SDEL
F9D3 BDF717 JSR PRINT ;PRINT A +7+D+E+L+7
F9D6 BDF555 JSR LFCR ;SKIP A LINE
F9D9 0E00 LCX BACKUP
F9DE 0E CLI ;ENABLE INTERRUPT
F9DC 6E00 JMP $00,X ;JUMP BACK A LINE

*****
**THIS SUBROUTINE GENERATES A
**0.50 SECCND DELAY.
F9D6 CE446V DELAY LCX -S$A480
F9E1 09 DLOOP CEX
F9E2 26FD BNE DLOOP
F9E4 39 RTS

*****
**THIS SUBROUTINE GENERATES A
**ONE MILLISECOND DELAY.
F9E5 DF02 MDLAY STX TEMPI3
F9E7 CE0054 MDLAY LCX -S$54
F9E8 09 MDLOOP CEX
F9E8 26FD BNE MDLOOP
F9E8 0E02 LCX TEMPI3
F9EF 39 RTS

*****
**THIS ROUTINE RESPONDS TO ALL INTERRUPTS
**FROM THE CLOCK BOARD. +USUALLY THIS MEANS
**A READING IS TO BE TAKEN OR WARMUP POWER
**IS TO BE TURNED ON.
F9F0 8BF7 NMI LCAA -S$F7 ;OPTIONAL
F9F2 B7E002 STAA CLOCKB ;OPTIONAL
F9F5 6604 LCAA -S$04 ;OPTIONAL
F9F7 B7E003 STAA CLOCKB+1 ;OPTIONAL
F9F7 BDF61E JSR OFFCLK ;PREVENT MULTIPLE INTERRUPTS
F9F8 BDF9DE JSP DELAY ;LET SENSORS STABILIZE
FA00 BDF629 JSR ENMEM ;ENABLE MEMORY IN CASE IT IS OFF
FA03 B04001 LCAA MONTH ;THE STORE AND LOADS SAVE
FA06 9704 STAA TMONTH ;THE VARIABLES USED FOR

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FA00  B04002      LCAA  DAY          ;SEQUENTIAL READINGS
FA01  9705        STAA  TODAY
FA02  B04003      LCAA  TIMEH
FA03  9706        STAA  TTIMEH
FA04  B04004      LCAA  TIMEL
FA05  9707        STAA  TTIMEL
FA06  B04033      LCAA  NUMLOW
FA07  9708        STAA  TNUMLOW
FA08  B04034      LCAA  NUMHI
FA09  9709        STAA  TNUMHI
FA10  B0F5B3      JSR   RTIME

FA24  B0F2E1  UP   JSR   UPTIME
FA25  B0400V      LCAA  STATUS
FA26  B406        ANDA  ~$50B
FA27  2707        EEQ   CHKW
FA28  C0FF        LCAB  ~$5FF
FA29  B0FF        LCAA  ~$5FF
FA30  7EFB17      JMP   SLEEPY
FA31  B04000  CHKW LCAA  STATUS
FA32  B401        ANDA  ~$501
FA33  271E        EEQ   WARM
FA34  B0F3CF      JSR   PSCMPW

FA35  260A        BNE   NMITODY
FA36  B005        LCAA  ~$505
FA37  C6A0        LCAB  ~$5A0
FA38  B0F63F      JSR   CLKSET
FA39  7EFB1D      JMP   SHUTOFF
FA40  B04013  NMITODY LCAA  STIMEH
FA41  F64014      LCAB  STIMEL
FA42  B0F63F      JSR   CLKSET
FA43  7A4000      DEC   STATUS
FA44  7EFB1B      JMP   SHUTOFF
FA45  B04000  WARM   LCAA  STATUS
FA46  B402        ANDA  ~$502

FA5F  261E        BNE   MEASUR

FA61  B04000      LCAA  STATUS
FA62  B602        ANDA  ~$502
FA63  B74000      STAA  STATUS
FA64  4F          CLRA
FA65  F0401B      LCAB  WTIME
FA66  B0F63F      JSR   CLKSET
FA67  B0F630      JSR   CLICLK
FA68  B0F4A3      JSR   DISMEM
FA69  B0F36B      JSR   WRMP0W
FA70  B0F9DE      JSR   DELAY
FA71  7EFB27      JMP   RESUME
FA72  7F402C  MEASUR CLR   SENNUM
FA73  FE4025      LCX   MDATAH
FA74  B0F671  TEMPDO JSR   READ
FA75  B0F957      JSR   MOVEIT
FA76  A700        STAA  S00+X
FA77  08          INX
FA78  7C402C      INC   SENNUM

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FA91	86402C	LCAA	SENNUM	
FA94	81C2	CMPA	~\$502	
FA96	26ED	BNE	TEMP00	
FA98	BDFB71	JSR	READ	
FA9B	BDF96A	JSR	LEFTSH	
FA9E	E700	STAB	\$00,X	
FAA0	08	INX		
FAA1	A700	STAA	\$00,X	
FAA3	86402B	LCAA	NPORTS	
FAA6	8002	SLBA	~\$502	
FAA8	87402D	STAA	FULLR	
FAAB	BDFC15	JSR	SETUP	
FAAC	7A402D	DEC	FULLR	
FAB1	2723	BEQ	INCONE	
FAB3	BDFBE7	JSR	WHICHP	
FAB6	BDFB71	JSR	READ	
FAB9	EA00	CRAB	\$00,X	
FABU	E700	STAB	\$00,X	
FABU	08	INX		
FABU	A700	STAA	\$00,X	
FACU	7A402D	DEC	FULLR	
FAC3	2711	BEQ	INCONE	
FAC5	08	INX		
FAC6	BDFBE7	JSR	WHICHP	
FAC9	BDFB71	JSR	READ	
FACU	BDF96A	JSR	LEFTSH	
FACF	E700	STAB	\$00,X	
FAD1	08	INX		
FAD2	A700	STAA	\$00,X	
FAD4	2008	EFA	MORFUL	
FAD6	08	INX		
FAD7	FF402D	STX	MDATAH	
FADU	86401B	LCAA	*TIME	
FADU	2708	BEQ	OKSTAT	
FADF	864000	LCAA	STATUS	
FAE2	84FD	ANDA	~\$5FD	
FAE4	874000	STAA	STATUS	
FAE7	BDF4B1	JSR	PFCOMP	
FAEA	2722	BEQ	AWAY	
FAEL	F64019	LCAB	FTIMEH	
FAEF	86401A	LCAA	FTIMEL	
FAF2	86400B	SLBA	PTIMEL	
FAFS	F24007	SECB	PTIMEH	
FAF6	2C0E	BGE	AWAY1	
FAFA	01	NCP		
FAFB	86400C	LCAA	STATUS	
FAFC	8A08	CRAA	~\$508	
FBOU	874000	STAA	STATUS	
FBOU	86FF	LCAA	~\$5FF	
FBOU	1B	TAB		
FBOU	200F	EFA	SLEEPY	
FBOU	50	TSTB		
FBOU	2003	BNE	AWAY	
FBOU	4L	ISTA		
FBOU	27EC	BEQ	LETSO	

;ARE PORT 1 AND 2 DONE+/
 ;IF +N+O+T BRANCH BACK
 ;TAKE NEXT(AIR P.) READING
 ;ARRANGE 12 BITS FOR MEMORY
 ;STORE HIGH ORDER 8 BITS
 ;INCREMENT +I+R
 ;STORE LOW ORDER 4 BITS
 ;NUMBER OF PORTS TO READ
 ;SUBTRACT 2 ALREADY READ
 ;STORE THAT IN TEMP LOC.
 ;INITIALIZE SETUP
 ;DEC FROM LAST PORT READ
 ;BRANCH IF DONE
 ;FIND NEXT PORT TO READ
 ;READ THE PORT
 ;+O+R HIGH ORDER 4 BITS WITH
 ;WHAT IS ALREADY THERE
 ;STORE THE COMBINATION
 ;INCREMENT +I+R
 ;STORE THE LOWER 8 BITS
 ;DECREMENT THE ~\$ OF READINGS
 ;BRANCH IF DONE
 ;INCREMENT +I+R
 ;FIND THE NEXT PORT
 ;READ IT
 ;SHIFT READING
 ;STORE HIGH ORDER 8 BITS
 ;INCREMENT +I+R
 ;STORE LOW ORDER 8 BITS
 ;BRANCH AND CHECK FOR MORE
 ;INCREMENT +I+R
 ;STORE BACK IN MDATAH, THIS IS THE
 ;NEXT WORD FOR STORING DATA
 ;IF NO WARM-UP TIME STATUS WORD IS +O+K
 ;RESET WARM-UP BIT
 ;STORE BACK INTO MEMORY
 ;COMPARE TODAY TO FINAL DAY
 ;IF NOT EQUAL BRANCH TO AWAY
 ;[+B+A]=STOPPING TIME
 ;[+B+A]=STOP TIME-PRESENT TIME
 ;IF NEGATIVE GOTO LETSGO
 ;INDICATE READINGS ALL TAKEN
 ;+A+B=FF
 ;GO STUFF TIME IN CLOCK
 ;IF +B<>0 THEN GOTO AWAY
 ;IF +A=0 THEN GOTO AWAY1

FB0F	F64010	AWAY	LCAB	LFREQ	
FB11	864015		LCAA	HFREQ	
FB14	8DF386		JSR	SUBWTI	;[+A+B]=FREQ OF MEASUREMENTS
FB17	61	SLEEPY	ACP		;SUBTRACT THE WARM-UP TIME
FB18	8DF63F		JSR	CLKSET	
FB18	8DF636	SHUTOFF	JSR	CLICLK	;STUFF THE CLOCK WITH TIME
FB1E	8DF4A3		JSR	DISMEN	;ALLOW THE CLOCK TO INTERRUPT
FB21	8DF358		JSR	POWOFF	;DISABLE THE MEMORY
FB24	8DF9DE		JSR	DELAY	;TURN THE POWER OFF
FB27	960A	RESUME	LCAA	WHERE	;WAIT FOR THE RELAYS
FB29	2612		BNE	GOBACK	
FB2B	86B0		LCAA	JSR80	;IF NOT 0, SOMETHING WAS INTERRUPTED
FB2L	8DF629		JSR	ENMEM	
FB30	84400L		ANDA	STATUS	
FB33	8DF4A3		JSR	DISMEN	
FB36	40		TSTA		
FB37	2703		BEQ	GOODBYE	;IF NOT EQUAL, A DUMP WAS IN PROGRESS
					;THEREFORE MEMORY MUST BE LEFT ON
FB39	8DF629		JSR	ENMEM	
FB3C	38	GOODBYE	HTI		
FB3D	2E28	GOBACK	BCT	FIXCUR	
					;WAS CURRENT OR SEQUENTIAL
FB3F	8DF60A		JSR	FAKERI	;READINGS INTERRUPTED+1
FB42	8DF629		JSR	ENMEM	;IF SEQUENTIAL FAKE +R+T+1
FB45	9604		LCAA	TMONTH	;ENABLE MEMORY
FB47	874001		STAA	MONTH	;RESTORE VARIABLES TO LAST LINE PRINTED
FB4A	9605		LCAA	YDAY	
FB4C	874002		STAA	DAY	
FB4F	9606		LCAA	TTIMEH	
FB51	874003		STAA	TIMH	
FB54	9607		LCAA	TTIMEH	
FB56	874004		STAA	TIMEL	
FB59	9608		LCAA	TNUMLOW	
FB5B	874033		STAA	NUMLOW	
FB5E	9609		LCAA	TNUMHI	
FB60	874034		STAA	NUMHI	
FB63	8DF555		JSR	LFGR	
FB66	0E		CLI		;SKIP TO NEXT LINE
FB67	7EFE5B		JMP	HESSEQ	;CLEAR INTERRUPT
FB6A	8DF60A	FIXCUR	JSR	FAKERI	;JUMP BACK TO PRINT NEW LINE
FB6L	0E		CLI		;DO A FAKE +R+T+1 FOR CURRENT READINGS
FB6E	7EFDFF		JMP	LEZTE	;CLEAR INTERRUPT
FB71	FF4035	READ	STX	IMPIR	;JUMP BACK TO CURRENT READINGS
FB74	8DF60A		JSR	ADCVAL	;SAVE +I+R
FB77	8DF60A		JSR	ADCVAL	;THROW AWAY FIRST READING
FB7A	8608		LCAA	JSNUMAVR	;TAKE A GOOD READING
FB7C	874010		STAA	TEMP2	;TELLS HOW MANY READINGS TO AVERAGE
FB7F	87401F		STAA	TEMP4	;STORE NUMAVR
FB82	4F		CLRA		;STORE 0
FB85	87401C		STAA	TEMP1	;STORE 0
FB88	87401E		STAA	TEMP3	
FB89	864024		LCAA	CONVL	
FB8C	F64023		LCAB	CONVH	
FB8F	FE401C		LCX	TEMP1	;[+B+A]=READING
FB92	09	SUMCON	DEX		;+I+R=NUMAVR
FB93	270E		BEQ	SUMDON	;DECREMENT +I+R
FB95	8DF9E5		JSR	MDELAY	;IF ZERO THEN SUM IS DONE
					;DELAY BEFORE NEXT READING

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FB96  BDF6D4      JSR     ADCVAL      ;TAKE NEXT READING
FB97  B64024      ACDA     CONVL
FB9E  F94023      ACCB     CONVMH      ;[A+B]=[A+B]+READING
FBA1  20EF        BFA      SUMCON      ;BRANCH BACK FOR MORE READINGS
FBA3  CE0004      SUMDON   LCX     ~$504      ;=1+LN(NUMAVR)/LN(2)
FBA6  09          AVRDIV   CEX
FBA7  2704        EEQ      AVGDON      ;DECREMENT A+R
FBA9  54          USRB
FBA9  46          FCRA
FBA9  20F9        EFA      AVRDIV      ;DIVIDE SUM BY 2
FBA9  FE4035      AVGDON   LCX     TMPIR      ;BRANCH BACK FOR MORE DIVIDES
FBA9  39          RTS
FBA9  39          RTS      ;AVERAGE IS CALCULATED
FBA9  39          RTS      ;RESTORE A+R AND RETURN

*****
**SUBROUTINE TIMEIT CALCULATES THE NEXT TIME
**THE CLOCK HAS TO TURN ON.  IF THE SYSTEM
**DOESN'T HAVE TO TAKE READINGS TILL TOMORROW
**THEN MIDNIGHT MINUS THE PRESENT TIME MINUS
**THE WARMUP TIME IS STUFFED INTO THE CLOCK.
**IF THE READINGS START TODAY THEN THE STARTING
**TIME MINUS THE PRESENT TIME MINUS THE WARM-
**UP TIME IS STUFFED INTO THE CLOCK.  THE BIT
**IN THE STATUS WORD THAT INDICATES IF THE
**SYSTEM IS TO START TAKING READINGS NEXT TIME
**IT COMES UP, IS APPROPRIATELY SET OR RESET.
FBB1  2010      TIMEIT  BNE     TODAY      ;BRANCH IF STARTS TODAY
FBB2  BDF2D6      JSR     MIDNIG      ;[A+B]=MIDNIGHT-PRESENT TIME
FBB3  BDF386      JSR     SUBWTI      ;SUBTRACT WARMUP TIME
FBB4  BDF63F      JSR     CLKSET      ;STUFF INTO THE CLOCK
FBB5  BDF629      JSR     ENMEM       ;ENABLE MEMORY
FBB6  4F          CLRA
FBB7  4C          INCA
FBB8  2010      TODAY  BFA      OFFSET      ;BRANCH TO SET STATUS
FBB9  B64013      TODAY  LCAB     STIMEH      ;IT STARTS TODAY
FBB9  F64014      LCAB     STIME1
FBB9  F04000      SLBB     PTIME1
FBB9  B24007      SECA     PTIMEH      ;[A+B]=START TIME -PRESENT TIME
FBB9  BDF386      JSR     SUBWTI      ;SUBTRACT WARMUP TIME
FBB9  BDF63F      JSR     CLKSET      ;STUFF INTO THE CLOCK
FBB9  BDF629      JSR     ENMEM       ;ENABLE MEMORY
FBB9  4F          CLRA
FBB9  F6401B      LCAB     WTIME
FBB9  2002      BNE     OFFSET
FBB9  8B02      ACDA     ~$502      ;SET STATUS FOR NO WARMUP TIME
FBB9  C74000      OFFSET  STAA     STATUS      ;STORE STATUS WORD
FBB9  BDF4A3      JSR     DISMEM      ;TURN OFF MEMORY
FBB9  39          RTS      ;RETURN TO CALLER

*****
**THIS ROUTINE RETURNS THE NEXT ADDITIONAL
**PORT SELECTED IN A AND SENNUM.  IF NO
**MORE PORTS ARE SELECTED, IT RETURNS FF.
**SUBROUTINE SETUP MUST BE CALLED BEFORE
**WHICH IF FIRST USED.
FBE7  F6402F      WHICHP  LCAB     WHICH1
FBEA  B64030      LCAB     WHICH2
FBE9  36          DOMINO  FSHA
FBE9  6401      ANDA     ~$501      ;CHECK IF WHICH1 IS NUMBER OF SELECTED
FBE9  260E      BNE     IKNOW      ;PORT IF SO, GOTO IKNOW

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FBF2 32      GIVEUP  FLLA
FBF3 5C      INCB
FBF4 40      FCRA
FBF5 B74030  STAA  WHICH2
FBF6 C107    CMPB  ~SSC7
FBFA 2FF1    ELE   DONTNO
FBFL C0FF    LCAB  ~SSFF
FBFE 2011    EFA   DEPART
FC00 32      INNCW  FLLA
FC01 764030  FCR   WHICH2
FC04 5C      INCB
FC05 C108    CMPB  ~SS08
FC07 2F04    ELE   BARK
FC08 C6FF    LCAB  ~SSFF
FC09 2004    BFA   DEPART
FC0E F7402F  BARK  STAB  WHICH1
FC10 5A      DECB
FC11 F7402C  DEPART STAB  SENNUM
FC14 39      RTS

*****
**THIS ROUTINE INITIALIZES WHICH1
**AND WHICH2 FOR USE IN WHICHP
FC15 B6402A  SETUP  LCAB  PORTBT
FC1F B74030  STAA  WHICH2
FC1E 7F402F  CLR   WHICH1
FC1E 39      RTS

*****
**THIS ROUTINE INITIALIZES THE DATA LOGGER
**IF YOU PRESS ^7D^7 AND RESPOND WITH A YES.
FC1F BDF550  INIT   ~SR   LFCR
FC22 CEF0C2  LEX   ~SPINIT
FC25 BDF717  ~SR   PRINT
FC26 CEF0D2  LEX   ~SYESNO
FC2E BDF717  ~SR   PRINT
FC2E BDF131  ~SR   CHYES
FC31 2737    BEQ   FIRSON
FC33 BDF550  ~SR   LFCR
FC36 200A    BFA   DISP

*****
**THIS IS WHERE THE PROCESSOR STARTS WHENEVER
**IT IS FIRST TURNED ON. ^THE FIRST FEW INSTRUCTIONS
**BASICLY INITIALIZE ANY PARTS OF THE
**SYSTEM THAT NEED TO BE REINITIALIZED.
**(^BECAUSE THE TIME IN THE CLOCK MIGHT BE
**CHANGED WHEN THE SYSTEM IS TURNED ON
**THE SYSTEM RELOADS THE TIME IN THE CLOCK.)
**^^^N^C^T^E^; ^IF THE POWER IS OFF AND THE
**PROCESSOR TURNS ON IT JUMPS TO THIS SECTION
**OF CODE IMMATERIAL OF WHO TURNED ON THE
**POWER (THE SYSTEM ITSELF OR THE USER)^.
FC36 8E007F  RES^ART LCS   ~SSTKPTR
FC3B 7F000A  CLR   WHERE
FC3E BDF697  ~SR   PIAS
FC41 BDF629  ~SR   ENMEM
FC44 BDF61E  ~SR   OFFCLK
FC47 B64009  LCAB  LMINH
FC4A 2705    BEQ   HIGHZ

;IF NOT, INCREMENT ^B
;SHIFT ^A RIGHT
;STORE IN WHICH2
;CHECK IF THIS IS AFTER LAST PORT
;IF NOT, GO TO DONTNO
;IF SO, LOAD ^B WITH FF

;SHIFT WHICH2 RIGHT
;INCREMENT ^A
;CHECK IF THIS IS AFTER THE LAST PORT
;IF NOT, GO TO BARK
;IF SO, LOAD ^B WITH FF

;SKIP A LINE
;WRITES ^7DO YOU WANT TO ...
;PRINTS THE MESSAGE

;PRINTS ^7(YES,NO)^7
;TESTS FOR A YES OR A NO
;BRANCH IF ANSWER IS YES
;SKIP A LINE
;RETURN TO CHOICE PAGE

;INITIALIZE THE STACK POINTER
;INITIALIZE THE ^P^I^A^7^S
;ENABLE THE MEMORY
;DISABLE THE CLOCK^7S INTERRUPT
;CHECK IF LMINH = 0

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FC4C  F840CA      LCAB  LMINL
FC4F  2C05        BFA  RELOAD      ;TIME IN CLOCK WAS NOT 0.
FC51  F8400A  HIGHZ LCAB  LMINL
FC54  2703        EEQ  REDONE      ;BRANCH IF TIME IN CLOCK WAS 0
FC56  BDF63F  RELUAD JSR  CLKSET    ;RESET THE TIME IN THE CLOCK
FC59  BDF636  REDONE JSR  CLCLK     ;ENABLE THE CLOCK+TS INTERRUPT
FC5C  BDF6A3        JSF  DISMEM     ;DISABLE THE MEMORY
FC5F  0E          CLI  DISMEM     ;ALLOW KEYBOARD INTERRUPTS
FC60  BDF555        JSR  LFCR
FC63  CE000C        LCX  ~$0000
FC66  UF00        STX  BACKUP      ;INITIALIZE BACKUP TO ZERO
FC6B  2036  DISPDN BFA  DISP      ;GO TO THE OPTIONS PAGE
*****
**THE PROGRAM JUMPS HERE IF THE SYSTEM
**IS TO BE INITIALIZED (JUMPS FROM INIT).
**FIRSTON INITIALIZES DATA COLLECTING
**PARAMETERS, SET THE STATUS WORD, AND
**PUTS THE FIRST TIME IN THE CLOCK.
FC6A  BDF61E  FIRSTON JSR  OFFCLK    ;TURN OFF THE CLOCKS INTERRUPT
FC6L  BDF629        JSR  ENMEM      ;ENABLE MEMORY
FC70  CE4040        LCX  ~$STARTM
FC73  FF402D        STX  MDATAH     ;REPOSITION DATA POINTER TO
;THE FIRST WORD
FC76  BDF16C        JSR  INDAT      ;GET THE INITIALIZATION DATA
FC79  BDF261  STRIP JSR  PSCOMP     ;+IS TODAY STARTING DAY+
FC7C  BDF8B1        JSR  TIMEIT    ;LET TIMEIT STUFF THE CLOCK
FC7F  BDF629        JSR  ENMEM     ;ENABLE MEMORY
FC82  B6402B  LCAA  NPORTS        ;LOAD THE ~$ OF READINGS TO BE TAKEN
;EACH TIME THE PROCESSOR COMES UP
FC85  C004        LCAB  ~$S04
FC87  8C03        SLBA  ~$S03
FC89  270B        EEQ  GOTIT
FC8E  5C  OMYGOD  INCB
FC8C  4A  DECA
FC8L  2707        EEQ  GOTIT
FC8F  5C  INCB
FC90  5C  INCB
FC92  4A  DECA
FC94  2702        EEQ  GOTIT
FC96  20F5  BFA  OMYGOD
FC9B  F7402C  GOTIT STAB  INCIR
;IF EQUAL THEN WE ARE DONE
;ODD ~$ OF EXTRA PORTS
;IF EQUAL TO 0 WE ARE DONE
;EVEN ~$ OF EXTRA PORTS
;IF +A=0 WE ARE DONE
;CHECK FOR MORE
;INCIR IS THE ~$ OF WORDS EACH
;SET OF MEASUREMENTS USES OF MEMORY
;ENABLE THE CLOCK INTERRUPT
;DISABLE THE MEMORY
;TURN THE POWER OFF
FC99  BDF636        JSR  CLCLK
FC9C  BDF4A3        JSR  DISMEM
FC9F  BDF35B  PO  JSR  POWOFF
*****
FCA2  9613  DISP  LCAA  SIRO
FCA4  8404        ANDA  ~$S04
FCA6  2603        BNE  CDISP
;BRANCH ONLY IF AN INTERRUPT FROM
;THE KEYBOARD HAS OCCURRED.
;OTHERWISE LOOP AGAIN
FCA8  7EFC60  JNP  DISPDN
FCA9  01  CDISP  NCP
FCAE  BDF55D        JSR  LFCR
FCAI  BDF55D        JSR  LFCR
FCB2  9012  LCAA  RECEIV
FCB4  8141  CMFA  ~$+7A+7
FCB6  271F  EEQ  JINPARM
;LOAD +A WITH LAST CHAR RECEIVED
;IF AN +7A+7 GOTO JINPARM

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FCB8 8142          CNPA  ~S+7B+7
FCB4 271E          BEQ   JCURRS          ;IF A +7B+7 GOTO JCURRS
FCB0 8143          CNPA  ~S+7C+7
FCB4 271D          BEQ   JSEQUEN        ;IF A +7C+7 GOTO JSEQUEN
FCC0 8144          CNPA  ~S+7D+7
FCC2 271C          BEQ   JINIT          ;IF A +7D+7 THEN GOTO JINIT
FCC4 8145          CNPA  ~S+7E+7
FCC6 2718          BEQ   JDUMP          ;IF AN +7E+7 GOTO JDUMP
FCC8 8DF3FC        JSR   DISPCB        ;IF NONE REPRINT CHOICES
FCC8 9613          DISCON LCAB  SIRQ
FCC8 84FB          ANDA  ~BSFB          ;CLEAR THE INTERRUPT SIGNAL
FCCF 9713          STAA  SIRO
FCD1 8DF4A3        JSR   DISMEM        ;DISABLE MEMORY
FCD4 7EFC00        JMP   DISPDN        ;GO BACK AND WAIT FOR ANOTHER +I+R+0

*****
**THIS SECTION PROVIDES BRANCH
**EXTENSIONS TO THE RIGHT SECTION.
FC07 7EFD2F        JINPARM JMP  INPARM
FCC4 7EFD9D        JCURRS  JMP  CURRS
FCD0 7EFE25        JSEQUEN JMP  SEQUEN
FCE4 7EFC1F        JINIT   JMP  INIT
FCE5 7EFF1C        JDUMP   JMP  DUMP
*****
**THIS SLEROUTINE PRINTS THE STARTING
**DATE AND TIME.
FCE6 F64011        SPRINT LCAB  SMONTH
FCE9 864012        LCAB  SDAY
FCEC 8DF5E4        JSR   PDATE          ;PRINT THE DATE
FCEF 8DF5F5        JSR   PSPACE        ;SPACE
FCF2 F64013        LCAB  STIMEH
FCF5 864014        LCAB  STIMEL
FCF6 8DF726        JSR   PTIME          ;PRINTS THE STARTING TIME
FCF6 8DF555        JSR   LFCR
FCFE 39           RTS
*****
**THIS SLEROUTINE PRINTS THE STOPPING
**DATE AND TIME.
FCF8 F64017        FPRINT LCAB  FMONTH
FD02 864018        LCAB  FDAY
FD05 8DF5E4        JSR   PDATE          ;PRINT THE STOPPING DATE
FD08 8DF5F5        JSR   PSPACE
FD0C F64019        LCAB  FTIMEH
FD0E 86401A        LCAB  FTIMEL
FD11 8DF726        JSR   PTIME          ;PRINT THE STOPPING TIME
FD14 8DF555        JSR   LFCR
FD17 39           RTS
*****
**THIS SLEROUTINE PRINTS THE WARMUP
**TIME.
FD18 86401B        FPRINT LCAB  WTIME
FD1B 8DF3E6        JSR   BCD          ;CHANGE THE TIME TO BCD
FD1E 8DF574        JSR   BYTOUT        ;PRINT THE TIME
FD21 39           RTS
*****
**THIS SLEROUTINE PRINTS THE FREQ-
**UENCY OF MEASUREMENTS.
FD22 F64015        FPRINT LCAB  HFREQ

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FD2b 864010      LCXA  LFREQ
FD2c 8DF720      JSR  PTIME      ;PRINT THE TIME
FD2d 8DF555      JSR  LFCR
FD2E 39          RTS

*****
**THIS SROUTINE PRINTS THE INPUT
**PARAMTERS TO THE DATA LOGGER.
FD2F 8DF529  INPRM JSR  ENMEM      ;ENABLE MEMORY
FD3c CEF022      LCX  ~$START
FD3d 8DF717      JSR  PRINT      ;PRINT ^7START^7
FD3e 8DF5F5      JSR  PSPACE
FD3f 8DFCE0      JSR  SPRINT      ;PRINT THE STARTING DATE AND TIME
FD40 CEF029      LCX  ~$STOP
FD41 8DF717      JSR  PRINT      ;PRINT ^7STOPPING^7
FD42 8DF600      JSR  PSP
FD43 8DF5F5      JSR  PSPACE      ;PRINT A SPACE
FD44 8DFCFF      JSR  FPRINT      ;PRINT 5 SPACES
FD45 CEF051      LCX  ~$WARMUP
FD46 8DF717      JSR  PRINT      ;PRINT THE STOPPING DATE AND TIME
FD47 CEF05F      LCX  ~$TI
FD48 8DF717      JSR  PRINT      ;PRINT ^7WARMUP^7
FD49 8DF717      JSR  PRINT      ;PRINT ^7TIME^7
FD4a 8DFD1b      JSR  ~PRINT      ;PRINT THE WARMUP TIME
FD4b CEF065      LCX  ~$MIN
FD4c 8DF717      JSR  PRINT      ;PRINT ^7MIN^7
FD4d 8DF555      JSR  LFCR
FD4e CEF02F      LCX  ~$FREQ
FD4f 8DF717      JSR  PRINT      ;PRINT ^7FREQUENCY OF ...^7
FD50 8DF5F5      JSR  PSPACE
FD51 8DFD2c      JSR  FRPRINT      ;PRINT THE FREQ. OF MEAS.
FD52 8DF4A3      JSR  DISMEM
FD53 7EFCCb      JMP  DISDON      ;RETURN TO OPTIONS
*****
**THIS SROUTINE PRINTS THE PROPER
**HEADING (DEPENDING ON THE PORTS
**SELECTED) WHEN CALLED BY SEQUENTIAL
**READINGS OR CURRENT SENSOR READINGS.
FD71 CEFFB7  SUBBA LCX  ~$HEAD2
FD72 8DF717      JSR  PRINT
FD73 CEFCE      LCX  ~$SPORT
FD74 8DFC15      JSR  SETUP
FD75 8DFBE7  MORYET JSR  WHICHP
FD76 C1FF      CMPB  ~$SFF
FD77 270F      BEQ  L00M
FD78 CEFCE      LCX  ~$SPORT
FD79 8DF717      JSR  PRINT
FD7a 5C          LACB
FD7b 17          TEA
FD7c 8A30      CFAR  ~$S30
FD7d 8DF2A0      JSR  OUTA
FD7e 20EA      BFA  MORYET
FD7f 8DF555      JSR  LFCR
FD80 39          RTS
*****
**THIS SROUTINE DISPLAYS THE CURRENT
**READINGS AT EACH PORT THAT WAS CHOSEN
**DURING THE INITIALIZATION. ^ANY CHAR.

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*TYPED AT THE KEYBOARD WILL DISPLAY
 *THE CURRENT READINGS. *TO EXIT FROM
 *THIS SUBROUTINE YOU MUST HIT AN
 *CONTROL *7S*7.

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F09D  CEF0EF  CURRS  LCX  *SRS
F0A0  BDF717  *SR  PRINT  ;PRINT *7TYPE R FOR READ...*7
F0A3  BDF629  *SR  ENMEM  ;ENABLE MEMORY
F0A6  BDFD77  *SR  SUBBA  ;TYPE THE COLUMN HEADINGS
F0A9  BDF4A3  *SR  DISMEM ;DISABLE THE MEMORY
FDAL  7C000A  *INC  WHERE
F0AF  3E      *MORER  *AI
FDB0  BDF629  *SR  ENMEM  ;WAIT FOR A KEY PRESS
F0B3  BDFC15  *SR  SETUP  ;ENABLE MEMORY
FDB6  7F402C  CLR  SENNUM ;INITIALIZE WHATPORT ROUTINE
FDB9  BDF6DA  *SR  ADCVAL ;START AT PORT 0
FDBC  CE4023  LCX  *SCONVH ;TAKE A READING
FDBF  BDF56A  *SR  LSHFT4  ;PUT READING IN *I+R
FDC2  BDF800  *SR  SENSCV  ;SHIFT LEFT FOUR BITS
FDC5  7C402C  INC  SENNUM  ;CONVERT AND PRINT THE TEMP.
FDC8  CE4023  LCX  *SCONVH ;READ PORT 1
FDCB  BDF6DA  *SR  ADCVAL  ;PUT READING IN *I+R
FDC6  BDF56A  *SR  LSHFT4  ;TAKE THE READING
FDE1  BDF800  *SR  SENSCV  ;SHIFT LEFT FOUR BITS
FDE4  7C402C  INC  SENNUM  ;CONVERT AND PRINT THE DEW POINT
FDE7  BDF6DA  *SR  ADCVAL  ;TAKE THE READING AT PORT 2
FDEA  BDF800  *SR  SENSCV  ;TAKE THE AIR PRESSURE
FDE5  BDFBE7  *SR  WHICH  ;CONVERT AND PRINT THE AIR PRESSURE
FDE8  C1FF    *OMP  *$FFF  ;GET NUMBER OF ADDITIONAL PORT
FDEB  270D    *EQ  LEZTE  ;IF IT IS FF THEY WE ARE DONE
FDE6  BDF6DA  *SR  ADCVAL  ;GOTO LEZTE IF DONE
FDE9  8604    *LCA  *$S04  ;TAKE THE READING
FDEC  BDF800  *SR  SENSCV  ;TELL SENSCV IT*7S A VOLTAGE
FDEE  26EC    *BFA  ERSTE  ;STORE SENNUM
FDF1  BDF555  *SR  LFCR  ;CONVERT AND PRINT THE VOLTAGE
FDF4  BDF4A3  *SR  DISMEM  ;CHECK FOR MORE
FDF7  2CB6    *BFA  *MORER ;GO BACK AND WAIT FOR MORE

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 *THIS SUBROUTINE TAKES THE MEMORY LOCATION
 *THAT IS IN THE *I+R AND GIVES THE DATE AND
 *TIME OF THE READING THAT IS STORED IN THAT
 *MEMORY LOCATION. *IF NO READING WAS TAKEN THEN
 *THEN 0000 IS STORED IN THE *I+R. *IF THE
 *READING WAS TAKEN THEN THE DATE IS STORED
 *IN MONTH AND DAY, AND THE TIME IS STORED IN
 *TIMEH AND TIMEL.

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FDF9  DF14  *HKEAD  STX  SAVINGS  ;SAVE THE *I+R
FDFB  BDF592  *SR  TIME  ;SAVE PMONTH THRU LMINH
FDFE  BDF4DC  *SR  STOPS  ;MOVE SMONTH-LFREQ INTO PMONTH-LMINL
FE01  CE4040  LCX  *$STARTM ;START CHECKING AT THE START OF MEMORY
FE04  9C14  DAYLO  CFX  SAVINGS  ;IS THIS THE LOCATION IN QUESTION*/
FE07  2710  *BEQ  DDAY  ;IF IT IS BRANCH TO DDAY
FE0A  BCF4023  CFX  MDATAH  ;IF IT ISN*7T THE CORRECT DAY
FE0D  2708  *BEQ  OVDAY  ;IS IT IN THE FUTURE*/
FE10  BDF4EA  *SR  IRL00P  ;IF ITS*7S A FUTURE READING
FE13  *IF IT ISN*7T THE CORRECT DAY
FE16  *IF IT IS IN THE FUTURE*/
FE19  *IF ITS*7S A FUTURE READING
FE1C  *BRANCH TO OVDAY
FE1F  *IF IT*7S NONE OF THESE, INCREMENT

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FE10  B0F2E1      JSR    UPTIME      ;THE +I+R APPROPRIATELY
FE13  20CF        BFA    DAYLO      ;THEN CHANGE THE DATE AND TIME
FE15  CEF000V    OVDAY  LCX    ~$50000 ;BRANCH AND COMPARE AGAIN
FE15  FF4021     DDAY   STX    TEMP12  ;READING NEVER TAKEN
FE15  B0F378     JSR    UNSTOP      ;SAVE +I+R
                                           ;MOVE THE CORRECT DATE AND TIME
                                           ;INTO MONTH, DAY, TIMEH, TIMEI
FE1C  B0F5B3      JSR    RTIME      ;RESTORE PMONTH-LMINL
FE21  FE4021     LCX    TEMP12      ;RESTORE THE +I+R
FE24  J9          RTS              ;RETURN TO CALLER

*****
*THIS SLROUTINE TAKES A DATE AND TIME
*AS INPUT AND ASKS HOW MANY READINGS
*YOU WOULD LIKE PRINTED OUT. +THE
*SUBROUTINE THEN PRINTS OUT THE FIRST
*READING ASKED FOR PLUS EVERY READING
*AFTER THAT UNTIL THE NUMBER OF READ-
*INGS THE USER SPECIFIED HAS BEEN
*PRINTED. +THE ROUTINE PRINTS NOTHING
*IF A INVALID DATE OR TIME IS SPECIF-
*IED.

FE25  B0F629     SEWLEN JSR    ENMEM      ;ENABLE MEMORY
FE25  CEF022     LCX    ~$START
FE25  B0F717     JSR    PRINT          ;PRINT +7START+7
FE25  B0F240     JSR    GETDAT        ;GET THE DATE AND TIME
FE31  B0F555     JSR    LFCR
FE34  CEF78E     LCX    ~$HOW
FE37  B0F717     JSR    PRINT          ;PRINT +7HOW MANY HEADINGS+7
FE37  CEF798     LCX    ~$THREE
FE37  B0F717     JSR    PRINT          ;PRINT +7(+X+X+X)+7
FE40  B0F240     JSR    THRDIG        ;GET THE THREE DIGITS
FE43  B74033     STAA  NUMLOW        ;STORE THEM IN NUMLOW
FE45  F74034     STAB  NUMHI         ;AND NUMHI
FE49  B0F555     JSR    LFCR
FE4C  B0F600     JSR    PSP
FE4E  CEFFA0     LCX    ~$HEAD1
FE52  B0F717     JSR    PRINT          ;PRINT +7DATE TIME+7
FE53  B0FD77     JSR    SUBBA        ;PRINT +7TEMP AIR PRE...+7
FE55  7A000A     DEC    WHERE
FE55  B0F4F2     RESSEG JSR    RTAKEN   ;CHECK IF READING WAS TAKEN
FE5E  2503       BNE    SEQL         ;BRANCH IF READING WAS TAKEN
FE60  7EFFF04    JMP     SEQDON       ;QUIT IF +I+R=0000
FE63  B0FDF9     SEWL  JSR    WHREAD   ;FIND DATE AND TIME
FE65  B0F4F2     JSR    RTAKEN       ;FIND THE MEMORY LOCATION IT
                                           ;IS STORED IN
FE69  2503       BNE    JUMP1        ;BRANCH IF FOUND
FE69  7EFFF04    JMP     SEQDON       ;QUIT
FE6E  F04001     JUMP1  LCAB  MONTH   ;+B=MONTH
FE71  B04002     LCAB  DAY           ;+A=MONTH
FE74  FF4025     STX    TEMPIR      ;SAVE +I+R
FE77  B0F5EA     JSR    POATE       ;PRINT THE DATE
FE7A  B0F600     JSR    PSP         ;SKIP A SPACE
FE7D  B04004     LCAB  TIMEI        ;+A=TIMEI
FE7D  F64003     LCAB  TIMEH        ;+B=TIMEH
FE7D  B0F720     JSR    PTIME       ;PRINT THE TIME
FE80  FE4025     LCX    TEMPIR      ;RESTORE +I+R
FE80  F04025     LCAB  NPORTS      ;+B=NUMBER OF READINGS

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FE8C C002 SLBB -$$02 ;SUBTRACT TWO
FE8E BDF600 JSR PSP ;SKIP A SPACE
FE91 A000 LCAA $00,X ;STORE READING IN +A
FE92 E74023 STAA CONVMH ;STORE READING IN CONVMH
FE96 7F402C CLR SENNUM ;SET PORT TO 0
FE97 BDF800 JSR SENSCV ;CONVERT AND PRINT THE TEMP
FE9C 06 INX ;INCREMENT THE +I+R
FE9E A000 LCAA $00,X
FE9F B74023 STAA CONVMH ;STORE READING IN CONVMH
FEA2 7C402C INC SENNUM ;SET PORT TO 1
FEA5 BDF800 JSR SENSCV ;CONVERT AND PRINT THE DEW POINT
FEA8 06 INX ;INCREMENT THE +I+R
FEA9 50 HITHER TSTB ;ALL READINGS PRINTED FOR TIME
FEAA 2739 BEQ YON1 ;IF YES BRANCH TO YON1
FEAC 5A DECB ;DECREMENT -S OF READINGS TAKEN
FEAD 7C402C INC SENNUM ;LOOKING AT NEXT PORT
FEAE A000 LCAA $00,X
FEAF B74023 STAA CONVMH ;STORE HIGH ORDER HALF IN CONVMH
FEB2 A001 LCAA $01,X
FEB7 B74024 STAA CONVL ;STORE LOW ORDER HALF IN CONVL
FEBA FF4026 STX TEMPIR ;STORE THE +I+R
FEBC CE4023 LDX -S CONVMH ;LET THE +I+R POINT TO CONVMH
FECD BDF94A JSR RSHIFT4 ;SHIFT CONVMH+L RIGHT 4 BITS
FECE FE4026 LDX TEMPIR ;RESTORE +I+R
FECF BDF800 JSR SENSCV ;CONVERT AND PRINT THE READING
FED2 06 INX ;INCREMENT THE +I+R
FED3 50 TSTB ;HAS EACH READING BEEN PRINTED
FED6 2717 BEQ YON2 ;IF YES BRANCH TO YON2
FED8 5A DECB ;DECREMENT -S OF READINGS PRINTED
FEE2 A001 LCAA $01,X
FEE3 B74024 STAA CONVL ;STORE LOW ORDER 8 BITS IN CONVL
FEE5 A000 LCAA $00,X
FEE6 B40F ANDA -$$0F ;MASK OFF TOP 4 BITS
FEE7 B74023 STAA CONVMH ;STORE HIGH 8 BITS IN CONVMH
FEEA 7C402C INC SENNUM ;POINT TO NEXT PORT
FEED BDF800 JSR SENSCV ;CONVERT AND PRINT THE READING
FEEF 06 INX ;INCREMENT THE +I+R
FEF0 08 INX ;DO IT TWICE
FEF2 20C5 EFA HITHER ;CHECK FOR MORE READINGS
FEF4 08 YON2 INX ;INCREMENT THE +I+R
FEF5 BDF555 YON1 JSR LFCEP
FEF6 F64034 LCAB NUMHI
FEF8 B64033 LCAA NUMLOW
FEFA 0001 SLBA -S$01 ;DECREMENT -S OF READINGS PRINTED
FEFB C200 SECB -S$00 ;WITH CARRY
FEFC F74034 STAB NUMHI ;STORE IT BACK
FEFD B74033 STAA NUMLOW
FEFF 40 TSTA ;IS LOW ORDER 8 BITS ZERO
FEF9 2703 BEQ HOP12 ;IF YES GO TEST +B
FEFB 7EFE63 JMP SEQL ;JUMP BACK FOR MORE READINGS
FEFE 50 HOP12 TSTB ;TEST HIGH ORDER 8 BITS
FEFF 2703 BEQ SEQDON ;IF ZERO THEN QUIT
FF01 7EFE63 JMP SEQL ;IF NOT BRANCH BACK FOR MORE
FF04 BDF443 SEADON JSR DISMEM ;DISABLE MEMORY
FF07 7C0004 INC WHERE
FF0A 7CFC6B JMP DISDON ;RETURN TO OPTIONS PAGE

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FF00 0F02 DELAY1 STX TEMPI3
FF01 30 PSMA
FF10 0008 LCAA ~$308
FF12 BDF90E DLOOP1 JSR DELAY
FF15 4A CECA
FF16 26FA ENE DLOOP1
FF18 32 FLA
FF19 DE02 LCX TEMPI3
FF1B 39 RTS

*****
*THIS SUBROUTINE DUMPS ALL OF THE
*READINGS THAT HAVE BEEN TAKEN TO
*THE PERIPHERAL DEVICE (IF SWITCHED
*ON) AND TO THE TERMINAL. *THE
*READINGS ARE PRECEDED BY SOME
*INITIALIZATION PARAMETERS (SEE
*THE OPERATION MANUAL FOR DETAILS).
FF1C 30E00C DUMP LCAA CLOCKB
FF1F 8438 ANDA ~$308 ;CHECK IF DUMP SWITCH PRESSED
FF21 26F9 ENE DUMP ;LOOP UNTIL IT IS PRESSED
FF23 BDF629 JSR ENMEM
FF25 8000 LCAA ~$580
FF28 3A4000 CFAA STATUS
FF2B 574000 STAA STATUS
FF2E BDFCE0 JSR SPRINT
FF31 BDFCFF JSR FPRINT ;PRINT THE STARTING DATE AND TIME
FF34 BDFD10 JSR WPRINT ;PRINT THE STOPPING DATE AND TIME
FF37 BDF555 JSR LFCR ;PRINT THE WARMUP TIME
FF3A BDFD22 JSR FRPRINT ;SKIP A LINE
FF3D 86402B LCAA NPORTS ;PRINT THE FREQ. OF MEASUREMENTS
FF40 BDF57A JSR BYTOUT ;PRINT THE ~$ OF READINGS
;TAKEN EACH TIME
;+I+R=STARTING LOCATION OF DATA
FF43 CE4040 LCX ~$STARTM
FF46 BDF555 JSR LFCR
FF49 80402A LCAA PORTBT
FF4C BDF57A JSR BYTOUT
FF4F BDF555 JSR LFCR ;SKIP A LINE
FF52 8C4025 CFX MDATAH ;IS IT THE END OF THE READINGS
FF55 2747 EEQ HELLO ;IF YES THEN GOTO HELLO
FF58 F6402B LCAB NPORTS ;+B=~$ OF READINGS
FF5A A000 LCAA $00,X
FF5C BDF57A JSR BYTOUT ;PRINT TEMP READING
FF5F 00 INX ;INCREMENT +I+R
FF62 BDF600 JSR PSP ;SKIP A SPACE
FF65 A000 LCAA $00,X
FF68 BDF57A JSR BYTOUT ;PRINT DEW POINT
FF6B 00 INX ;INCREMENT +I+R
FF6D 5A DECB
FF6F 5A DECB ;DECREMENT +B TWICE
FF72 BDF600 FULBIT JSR PSP ;SKIP A SPACE
FF75 A000 LCAA $00,X
FF78 BDF57A JSR BYTOUT ;PRINT FIRST BYTE
FF7B 00 INX ;INCREMENT +I+R
FF7D A000 LCAA $00,X
FF7F 44 LSRA
FF80 44 LSRA

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FF79 44          LSRA          ;DROP 8 LOW ORDER BITS
FF7A BDF680     JSR          HEXASC ;CONVERT TO HEX
FF7J BDF2A0     JSR          OUTA   ;PRINT
FF80 08         INX          ;INCREMENT +I+R
FF81 5A         DECB        ;DECREMENT -S OF READINGS PRINTED
FF82 27CB       BEQ          OUTLOP ;IF DONE BRANCH BACK TO OUTLOP
FF84 BDF600     JSR          PSP    ;SKIP A SPACE
FF87 09         CEX          ;DECREMENT +I+R
FF88 A600       LCAA        $00,X
FF8A 840F       ANDA        ~$30F  ;MASK OUT HIGH 8 BITS
FF8C BDF660     JSR          HEXASC ;CHANGE TO HEX
FF8F BDF2A0     JSR          OUTA   ;PRINT THE CHARACTER
FF92 08         INX          ;INCREMENT +I+R
FF93 A600       LCAA        $00,X
FF95 BDF57A     JSR          BYTOUT ;PRINT THE BYTE
FF98 08         INX          ;INCREMENT +I+R
FF99 5A         DECB        ;DECREMENT -S OF READINGS PRINTED
FF9A 27B3       BEQ          OUTLOP ;BRANCH BACK IF ROW IS FINISHED
FF9C 20C0       BRA          FULBIT ;FINISH THE ROW
FF9E 807F       HELLO      LCAA    ~$3,7F
FFA0 B44000     ANDA        STATUS
FFA3 B74000     STAA        STATUS
FFA6 BDF4A3     JSR          DISMEM
FFA9 7EFF1C     JMP          DUMP
FFAC 444154     HEAD1      FCC     +7DATE TIME+7
FFAF 452020
FFB2 544940
FFB5 45
FFB6 00         FCB        00
FFB7 202054     HEAD2      FCC     +7 TEMP DEW PT PRESS +7
FFBA 454050
FFBD 202044
FFC0 455720
FFC3 505420
FFC6 205052
FFC9 455353
FFCC 20
FFCD 00         FCB        00
FFCE 205047     PORT      FCC     +7 PORT+7
FFD1 5254
FFD3 00         FCB        00
          END

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* THE BINARY IN IN PHYSICAL BLOCK 2

----- S Y M B O L L E G E N D -----

↑	SHIFTED KEY	^	SUPERSCRIPT
v	SUBSCRIPT	␣	BACKSPACE
↵	CARRIAGE RETURN	≥	FONT
~	ACCESS	#	TIMES
=	DIVIDE	→	ASSIGN
%	PERCENT		

CUMULATIVE LIST OF RADIO RESEARCH LABORATORY REPORTS

PREPARED UNDER NASA GRANT NSG-5049

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